



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY BX620 S3, Intel Xeon processor E5335, 2.0 GHz

SPECint®\_rate2006 = 77.8

SPECint\_rate\_base2006 = 74.1

CPU2006 license: 22

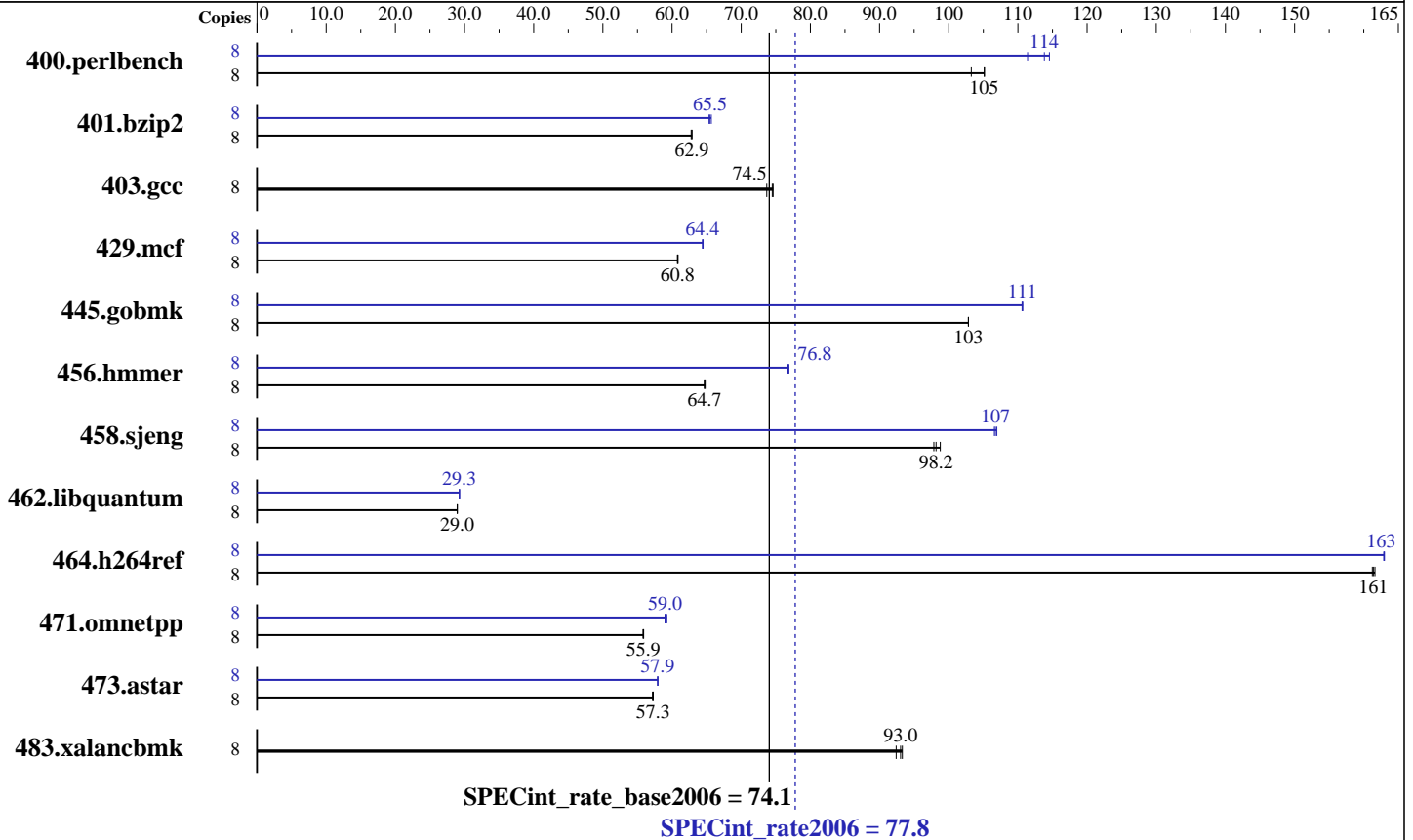
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Apr-2007

Hardware Availability: Feb-2007

Software Availability: Feb-2007



### Hardware

CPU Name: Intel Xeon E5335  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
 Disk Subsystem: SAS (36GB 10000 rpm)  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_cc\_p\_9.1.047  
 Auto Parallel: No  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	757	103	<b>743</b>	<b>105</b>	743	105	8	<b>687</b>	<b>114</b>	702	111	682	115
401.bzip2	8	1228	62.9	1230	62.8	<b>1228</b>	<b>62.9</b>	8	1175	65.7	<b>1179</b>	<b>65.5</b>	1181	65.4
403.gcc	8	874	73.7	863	74.6	<b>865</b>	<b>74.5</b>	8	874	73.7	863	74.6	<b>865</b>	<b>74.5</b>
429.mcf	8	<b>1200</b>	<b>60.8</b>	1200	60.8	1199	60.9	8	1131	64.5	1133	64.4	<b>1133</b>	<b>64.4</b>
445.gobmk	8	<b>816</b>	<b>103</b>	816	103	816	103	8	<b>758</b>	<b>111</b>	759	111	758	111
456.hmmer	8	1152	64.8	1154	64.7	<b>1154</b>	<b>64.7</b>	8	971	76.9	972	76.8	<b>972</b>	<b>76.8</b>
458.sjeng	8	989	97.8	<b>986</b>	<b>98.2</b>	980	98.8	8	<b>907</b>	<b>107</b>	905	107	908	107
462.libquantum	8	5729	28.9	<b>5725</b>	<b>29.0</b>	5724	29.0	8	<b>5664</b>	<b>29.3</b>	5664	29.3	5665	29.3
464.h264ref	8	1098	161	<b>1097</b>	<b>161</b>	1095	162	8	1086	163	1087	163	<b>1086</b>	<b>163</b>
471.omnetpp	8	894	55.9	896	55.8	<b>895</b>	<b>55.9</b>	8	844	59.2	<b>847</b>	<b>59.0</b>	848	59.0
473.astar	8	982	57.2	980	57.3	<b>981</b>	<b>57.3</b>	8	970	57.9	969	57.9	<b>969</b>	<b>57.9</b>
483.xalancbmk	8	592	93.3	597	92.4	<b>593</b>	<b>93.0</b>	8	592	93.3	597	92.4	<b>593</b>	<b>93.0</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

The system bus runs at 1333 MHz

All binaries were built with 32-bit Intel compiler except:  
401.bzip2, 456.hmmer and 462.libquantum in peak were built with  
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:  
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

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## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

456.hmmr: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmr: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof\_gen(pass 1) -prof\_use(pass 2) -xP -O3 -ipo  
-no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

473.astar: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml)

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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