SPEC® CINT2006 Result

Bull SAS

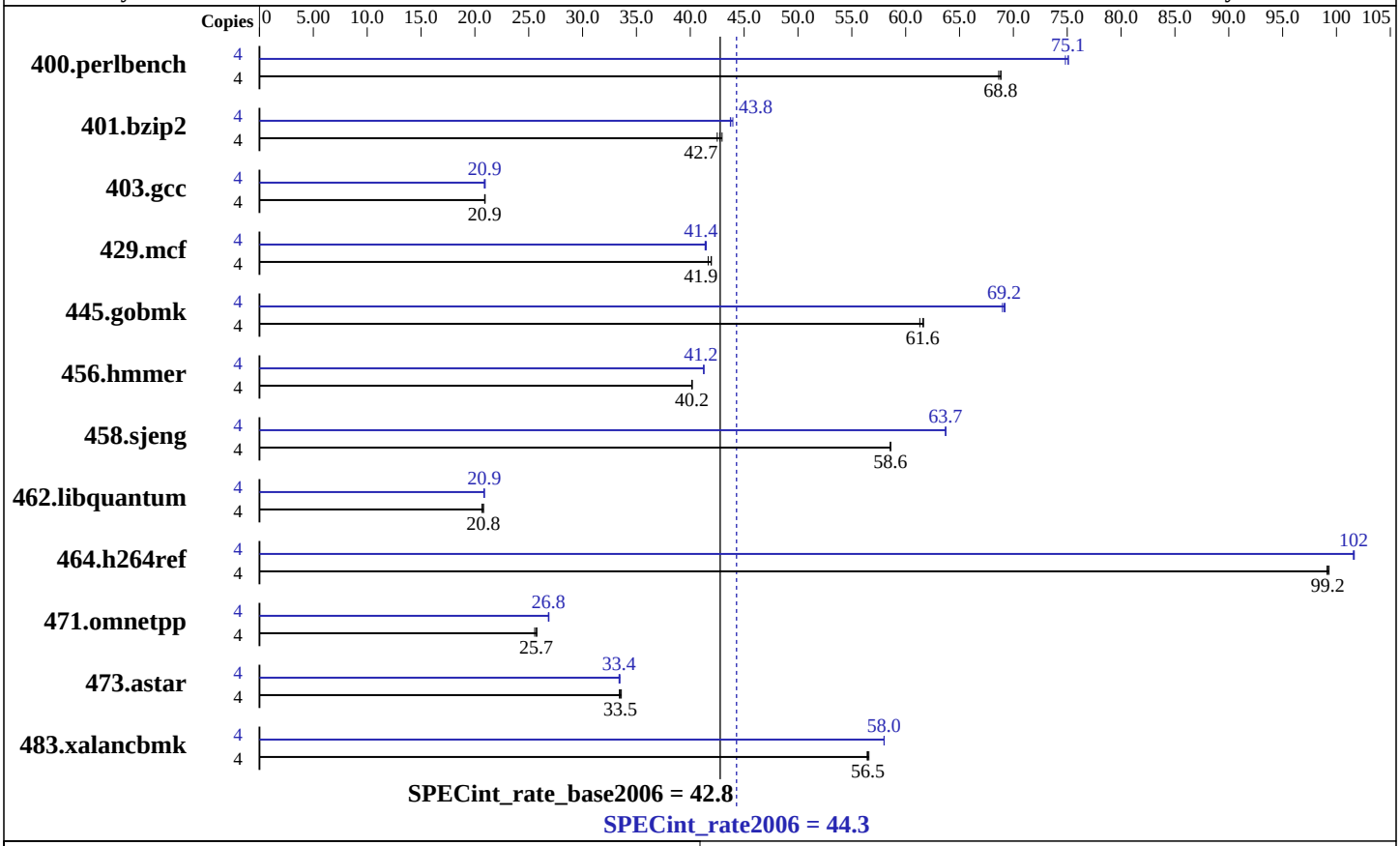
NovaScale T810 (Intel Xeon processor X3220,2.4GHz)

SPECint®_rate2006 = 44.3

SPECint_rate_base2006 = 42.8

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Apr-2007
Hardware Availability: Feb-2007
Software Availability: Dec-2006



Hardware

CPU Name:	Intel Xeon X3220
CPU Characteristics:	2.4GHz, 2x4 MB L2 shared, 1066 MHz system bus
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (2GB DIMMx4, PC2-5300E ECC CL5)
Disk Subsystem:	2x73 GB SAS, 10000RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition X64 Edition Service Pack1
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.033 Build no 20061103Z Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	568	68.8	569	68.6	**568**	**68.8**	4	520	75.1	**521**	**75.1**	522	74.8
401.bzip2	4	908	42.5	899	43.0	**903**	**42.7**	4	**882**	**43.8**	883	43.7	878	44.0
403.gcc	4	1540	20.9	**1539**	**20.9**	1537	21.0	4	1543	20.9	1536	21.0	**1539**	**20.9**
429.mcf	4	876	41.7	870	42.0	**870**	**41.9**	4	882	41.4	879	41.5	**881**	**41.4**
445.gobmk	4	**681**	**61.6**	680	61.7	684	61.3	4	**607**	**69.2**	606	69.2	608	69.0
456.hmmer	4	929	40.2	**929**	**40.2**	929	40.2	4	905	41.2	**905**	**41.2**	904	41.3
458.sjeng	4	826	58.6	**826**	**58.6**	826	58.6	4	760	63.7	**760**	**63.7**	760	63.7
462.libquantum	4	4013	20.7	**3987**	**20.8**	3986	20.8	4	3978	20.8	**3970**	**20.9**	3969	20.9
464.h264ref	4	**892**	**99.2**	891	99.3	893	99.1	4	871	102	**871**	**102**	871	102
471.omnetpp	4	978	25.6	**973**	**25.7**	971	25.8	4	**931**	**26.8**	930	26.9	932	26.8
473.astar	4	840	33.4	**838**	**33.5**	836	33.6	4	841	33.4	**841**	**33.4**	839	33.5
483.xalancbmk	4	489	56.4	**489**	**56.5**	488	56.6	4	476	58.0	476	58.0	**476**	**58.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/flags.20090714.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/flags.20090714.00.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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