SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

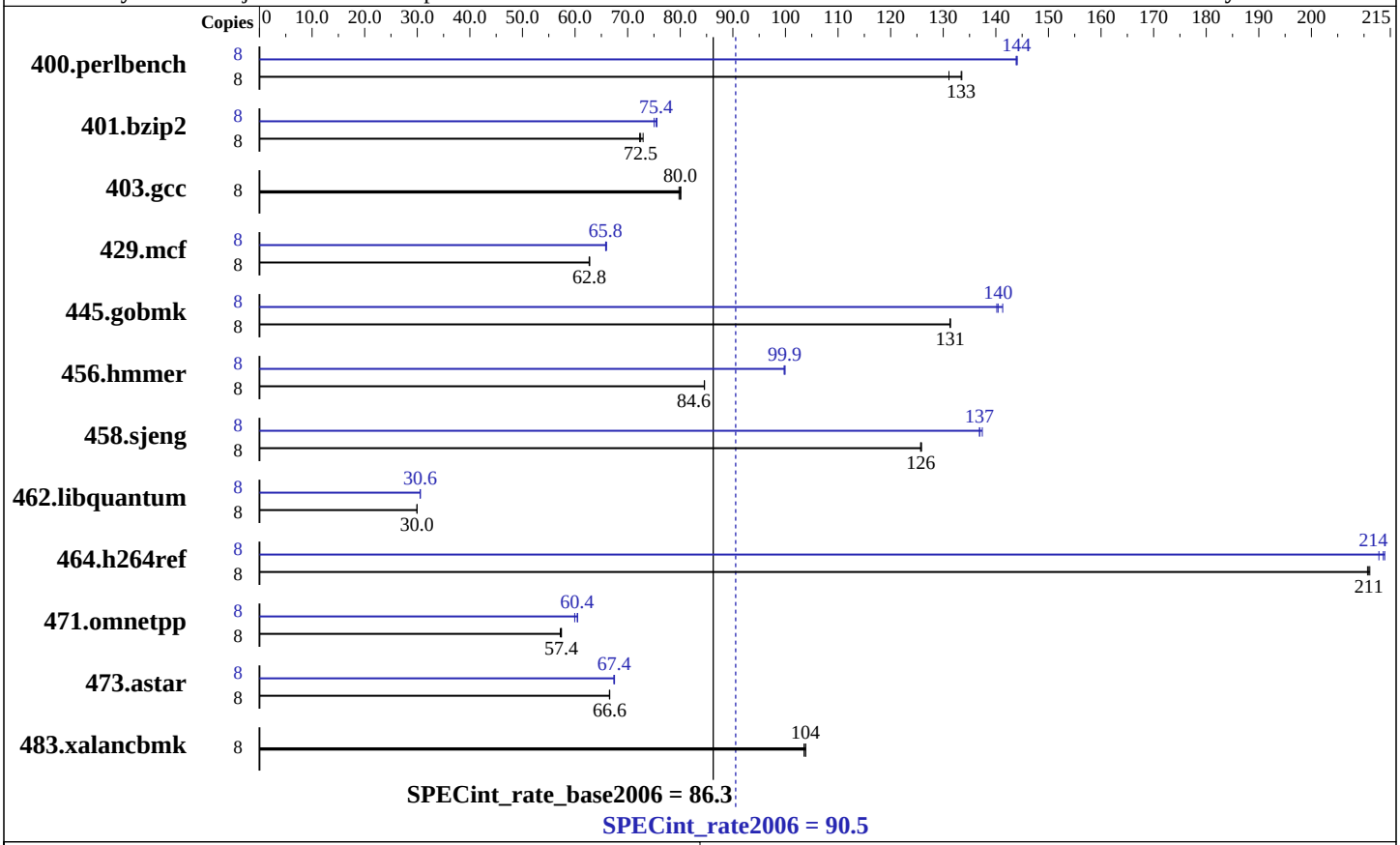
PRIMERGY RX200 S3, Intel Xeon processor X5355, 2.66 GHz

SPECint®_rate2006 = 90.5

SPECint_rate_base2006 = 86.3

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Mar-2007
Hardware Availability: Jan-2007
Software Availability: Feb-2007



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	X5355
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	SAS (73GB 15400 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor X5355, 2.66 GHz

SPECint_rate2006 = 90.5

SPECint_rate_base2006 = 86.3

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Mar-2007
Hardware Availability: Jan-2007
Software Availability: Feb-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	596	131	**586**	**133**	585	134	8	543	144	543	144	**543**	**144**
401.bzip2	8	1058	72.9	1068	72.3	**1065**	**72.5**	8	**1023**	**75.4**	1029	75.0	1021	75.6
403.gcc	8	804	80.1	**805**	**80.0**	807	79.8	8	804	80.1	**805**	**80.0**	807	79.8
429.mcf	8	**1163**	**62.8**	1164	62.7	1162	62.8	8	1109	65.8	**1108**	**65.8**	1106	66.0
445.gobmk	8	**639**	**131**	639	131	639	131	8	**598**	**140**	594	141	599	140
456.hmmer	8	**882**	**84.6**	883	84.6	882	84.6	8	747	99.9	748	99.7	**747**	**99.9**
458.sjeng	8	**770**	**126**	769	126	770	126	8	707	137	704	137	**707**	**137**
462.libquantum	8	5535	29.9	**5534**	**30.0**	5530	30.0	8	5424	30.6	**5424**	**30.6**	5423	30.6
464.h264ref	8	840	211	839	211	**839**	**211**	8	828	214	832	213	**829**	**214**
471.omnetpp	8	**871**	**57.4**	874	57.2	871	57.4	8	**828**	**60.4**	834	59.9	827	60.5
473.astar	8	844	66.5	**844**	**66.6**	843	66.6	8	832	67.5	833	67.4	**833**	**67.4**
483.xalancbmk	8	532	104	**532**	**104**	533	104	8	532	104	**532**	**104**	533	104

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1333 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`
`-L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 429.mcf`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 429.mcf`

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 429.mcf`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo`
`-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`
`-L/opt/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 11:58:36 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 17 April 2007.