SPEC® CINT2006 Result
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Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor X5355, 2.66 GHz

$SPECint®_rate2006 = $ 91.2

$SPECint_rate_base2006 = $ 86.8

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Mar-2007

Hardware Availability: Jan-2007

Software Availability: Feb-2007

Copies	Benchmark	base	peak
8	400.perlbench	134	146
8	401.bzip2	73.4	76.3
8	403.gcc	80.0	
8	429.mcf	63.3	66.8
8	445.gobmk	131	141
8	456.hmmer	84.7	100
8	458.sjeng	126	138
8	462.libquantum	30.1	30.6
8	464.h264ref	212	213
8	471.omnetpp	58.1	61.2
8	473.astar	67.0	67.9
8	483.xalancbmk	105	

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Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	X5355
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	SAS (73GB 15400 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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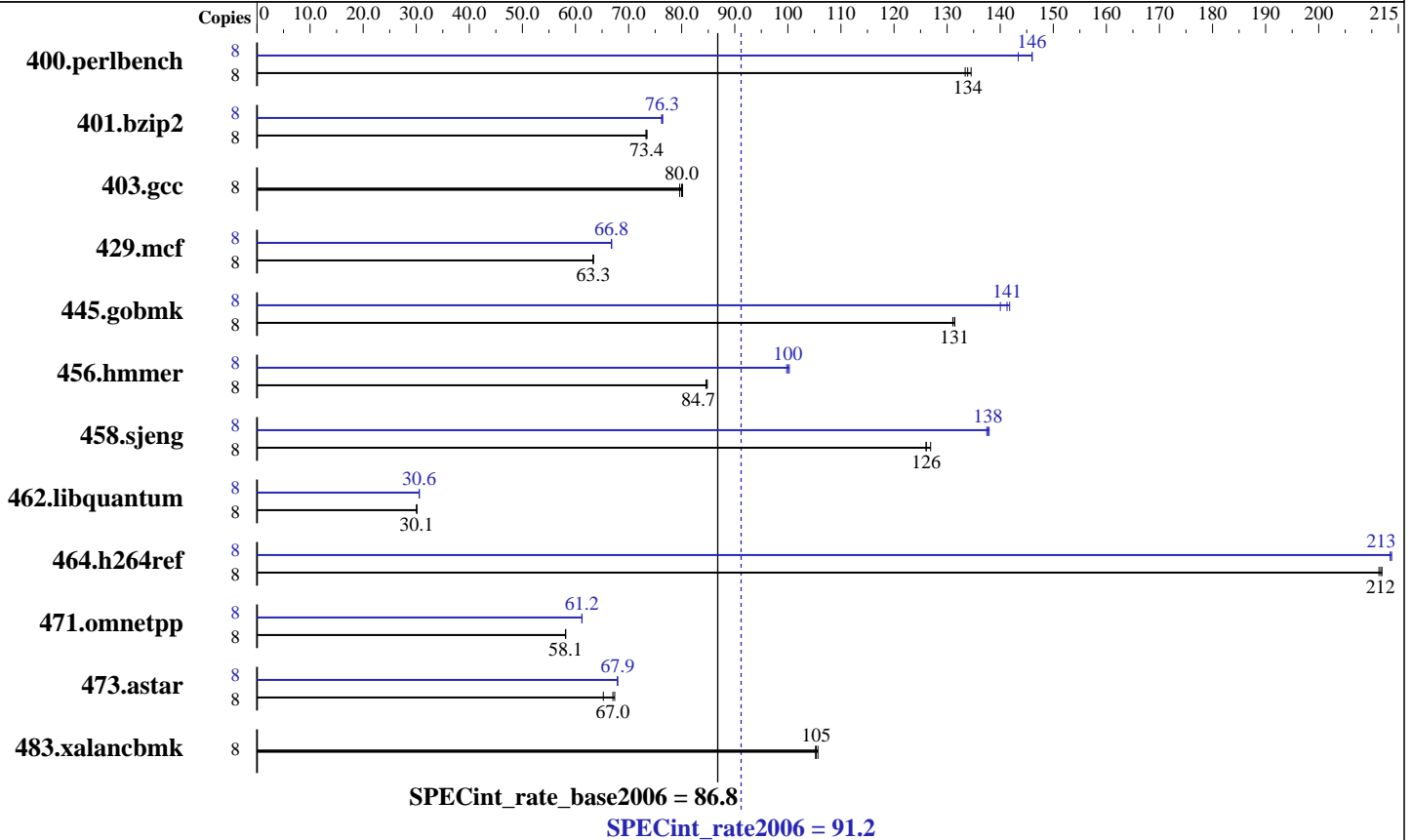
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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	581	135	**584**	**134**	586	133	8	545	143	**535**	**146**	535	146
401.bzip2	8	1051	73.4	1054	73.3	**1051**	**73.4**	8	**1012**	**76.3**	1013	76.2	1010	76.4
403.gcc	8	**805**	**80.0**	809	79.6	803	80.2	8	**805**	**80.0**	809	79.6	803	80.2
429.mcf	8	1152	63.3	**1152**	**63.3**	1151	63.4	8	1092	66.8	**1093**	**66.8**	1093	66.7
445.gobmk	8	640	131	**639**	**131**	638	131	8	**594**	**141**	592	142	599	140
456.hmmer	8	882	84.6	**882**	**84.7**	880	84.8	8	**746**	**100**	744	100	748	99.8
458.sjeng	8	768	126	763	127	**768**	**126**	8	702	138	**703**	**138**	704	137
462.libquantum	8	5509	30.1	**5509**	**30.1**	5512	30.1	8	5424	30.6	5424	30.6	**5424**	**30.6**
464.h264ref	8	835	212	838	211	**836**	**212**	8	828	214	829	213	**829**	**213**
471.omnetpp	8	**860**	**58.1**	860	58.2	861	58.1	8	817	61.2	**817**	**61.2**	816	61.2
473.astar	8	861	65.2	833	67.4	**838**	**67.0**	8	827	67.9	**827**	**67.9**	827	67.9
483.xalancbmk	8	522	106	525	105	**524**	**105**	8	522	106	525	105	**524**	**105**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
    '/usr/bin/taskset' used to bind processes to CPUs

General Notes

    The system bus runs at 1333 MHz

    All binaries were built with 32-bit Intel compiler except:
    401.bzip2, 456.hmmer and 462.libquantum in peak were built with
    64-bit Intel compiler by changing the path for include and library files.

    BIOS configuration:
    Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

    This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and
    the PRIMERGY TX300 S3 are electronically equivalent.

    For information about Fujitsu Siemens Computers in your country please see:
    http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
    icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
```
icpc
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_X64
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
    -fast
```

C++ benchmarks:
```
    -xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc

      401.bzip2: /opt/intel/cce/9.1.047/bin/icc
               -I/opt/intel/cce/9.1.047/include
               -L/opt/intel/cce/9.1.047/lib

      456.hmmer: /opt/intel/cce/9.1.047/bin/icc
               -I/opt/intel/cce/9.1.047/include
               -L/opt/intel/cce/9.1.047/lib

    462.libquantum: /opt/intel/cce/9.1.047/bin/icc
               -I/opt/intel/cce/9.1.047/include
               -L/opt/intel/cce/9.1.047/lib
```

C++ benchmarks:
```
    icpc
```

Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_X64
      401.bzip2: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
    456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

      401.bzip2: -fast

       403.gcc: basepeak = yes

       429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
                -L/opt/SmartHeap_8_1/lib -lsmartheap

     445.gobmk: Same as 429.mcf

     456.hmmer: Same as 400.perlbench

      458.sjeng: Same as 429.mcf

  462.libquantum: Same as 400.perlbench

    464.h264ref: Same as 429.mcf
```

C++ benchmarks:

```
    471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
                -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

       473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
                -L/opt/SmartHeap_8_1/lib -lsmartheap

   483.xalancbmk: basepeak = yes
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

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For other inquiries, please contact webmaster@spec.org.

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