SPEC® CINT2006 Result

Bull SAS

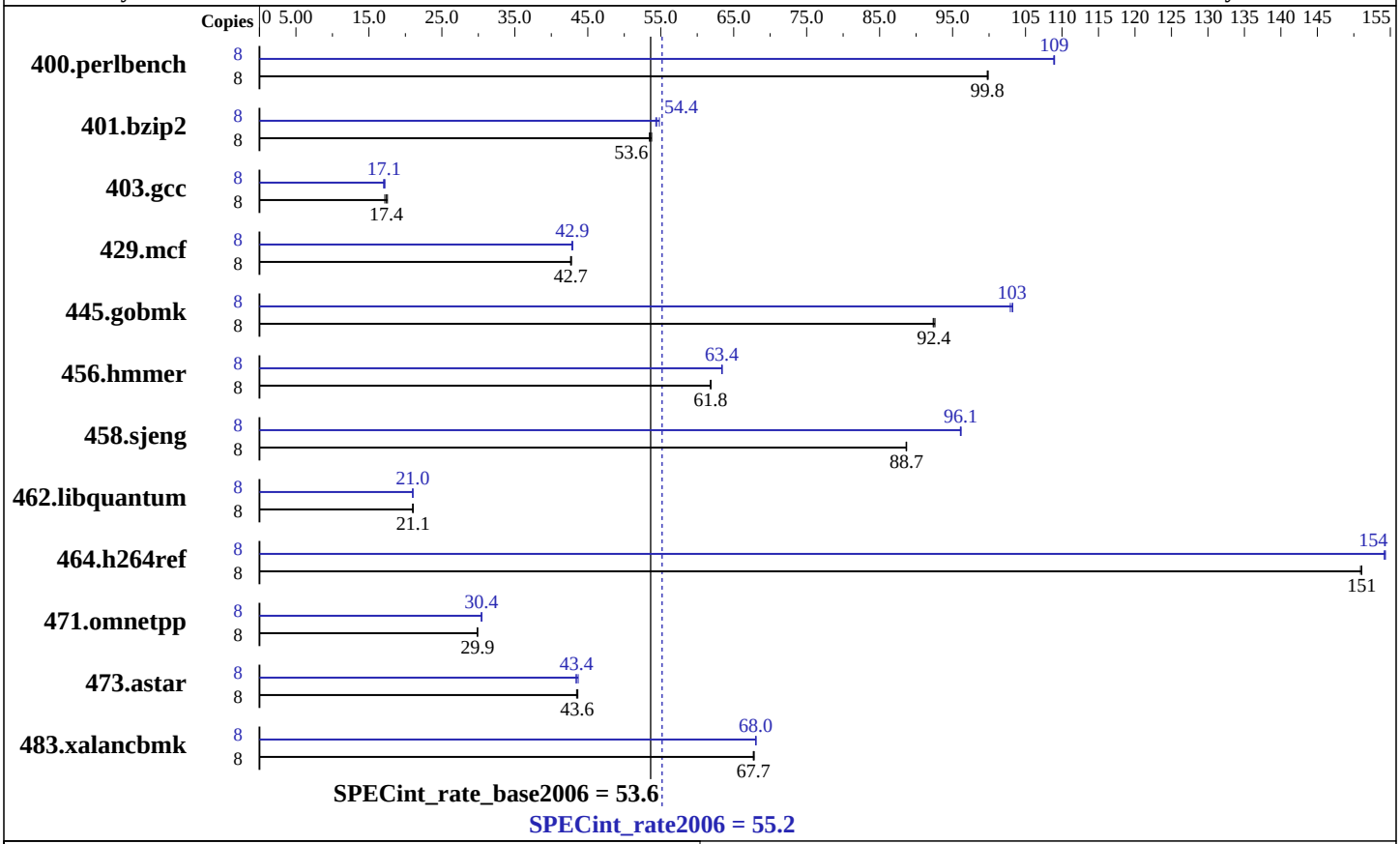
NovaScale B260 (Intel Xeon processor L5320,1.86GHz)

SPECint®_rate2006 = 55.2

SPECint_rate_base2006 = 53.6

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Mar-2007
Hardware Availability: Apr-2007
Software Availability: Dec-2006



Hardware

CPU Name:	Intel Xeon L5320
CPU Characteristics:	1.86 GHz, 8MB L2, 1066MHz bus
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1 to 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (2GB DIMMx4, FB-DIMM PC2-5300F ECC CL5)
Disk Subsystem:	73 GB SAS, 10000RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition (32 bits) Service Pack1
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.033 Build no 20061103Z Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	783	99.8	782	99.9	**783**	**99.8**	8	717	109	**717**	**109**	718	109
401.bzip2	8	1436	53.8	**1441**	**53.6**	1445	53.4	8	1421	54.3	1408	54.8	**1418**	**54.4**
403.gcc	8	**3703**	**17.4**	3670	17.5	3748	17.2	8	3745	17.2	**3765**	**17.1**	3785	17.0
429.mcf	8	1706	42.8	1711	42.6	**1709**	**42.7**	8	1700	42.9	**1701**	**42.9**	1704	42.8
445.gobmk	8	**908**	**92.4**	906	92.6	909	92.3	8	815	103	813	103	**813**	**103**
456.hmmer	8	1206	61.9	**1207**	**61.8**	1208	61.8	8	1177	63.4	**1178**	**63.4**	1178	63.4
458.sjeng	8	**1092**	**88.7**	1091	88.7	1092	88.6	8	1007	96.2	**1007**	**96.1**	1008	96.1
462.libquantum	8	7898	21.0	7872	21.1	**7873**	**21.1**	8	7885	21.0	**7886**	**21.0**	7891	21.0
464.h264ref	8	1172	151	1173	151	**1172**	**151**	8	1148	154	1147	154	**1148**	**154**
471.omnetpp	8	1675	29.9	**1673**	**29.9**	1673	29.9	8	1641	30.5	1644	30.4	**1643**	**30.4**
473.astar	8	**1289**	**43.6**	1287	43.6	1292	43.5	8	**1293**	**43.4**	1294	43.4	1286	43.7
483.xalancbmk	8	**815**	**67.7**	814	67.8	815	67.7	8	811	68.0	**811**	**68.0**	811	68.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/flags.20090714.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/flags.20090714.00.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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