



# SPEC® CINT2006 Result

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## Bull SAS

NovaScale B260 (Intel Xeon processor L5320, 1.86GHz)

**SPECint®2006 = 11.5**

**SPECint\_base2006 = 11.0**

CPU2006 license: 20

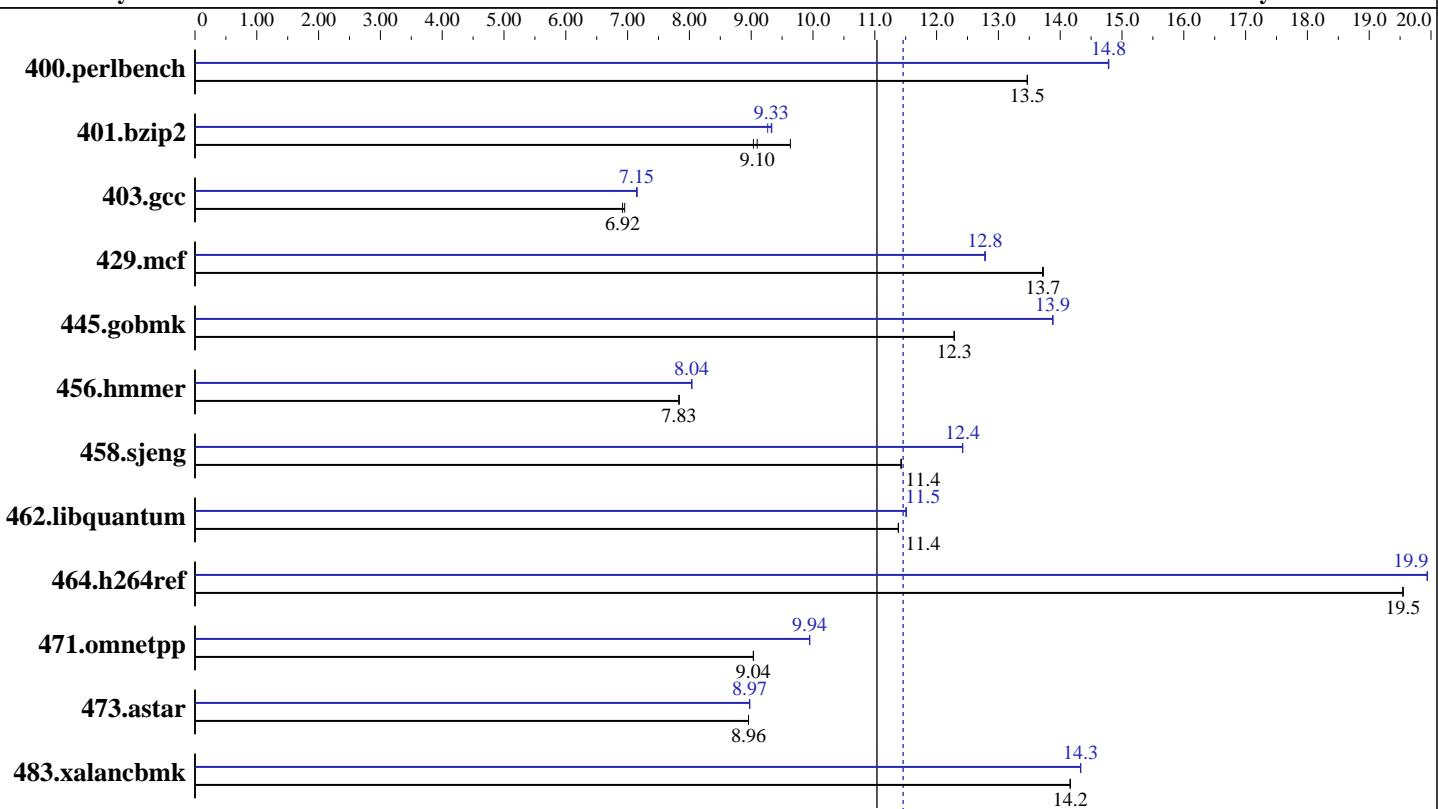
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Mar-2007

Hardware Availability: Apr-2007

Software Availability: Dec-2006



### Hardware

CPU Name: Intel Xeon L5320  
CPU Characteristics: 1.86 GHz, 8MB L2, 1066MHz bus  
CPU MHz: 1860  
FPU: Integrated  
CPU(s) enabled: 1 core, 1 chip, 4 cores/chip  
CPU(s) orderable: 1 to 2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
L3 Cache: None  
Other Cache: None  
Memory: 8 GB (2GB DIMMx4, FB-DIMM PC2-5300F ECC CL5)  
Disk Subsystem: 73 GB SAS, 10000RPM  
Other Hardware: None

### Software

Operating System: Windows Server 2003 Enterprise Edition (32 bits)  
Service Pack1  
Compiler: Intel C++ Compiler for IA32 version 9.1  
Package ID W\_CC\_C\_9.1.033 Build no 20061103Z  
Microsoft Visual Studio .NET 2003 (lib & linker)  
Auto Parallel: No  
File System: NTFS  
System State: Default  
Base Pointers: 32-bit  
Peak Pointers: 32-bit  
Other Software: MicroQuill SmartHeap Library 8.0 (shlw32M.lib)



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>725</b>	<b>13.5</b>	726	13.5	725	13.5	661	14.8	661	14.8	<b>661</b>	<b>14.8</b>
401.bzip2	1002	9.63	<b>1061</b>	<b>9.10</b>	1068	9.04	1041	9.27	1034	9.33	<b>1035</b>	<b>9.33</b>
403.gcc	1158	6.95	<b>1164</b>	<b>6.92</b>	1164	6.92	1126	7.15	<b>1126</b>	<b>7.15</b>	1125	7.15
429.mcf	664	13.7	<b>665</b>	<b>13.7</b>	665	13.7	714	12.8	713	12.8	<b>713</b>	<b>12.8</b>
445.gobmk	854	12.3	<b>854</b>	<b>12.3</b>	854	12.3	<b>756</b>	<b>13.9</b>	756	13.9	756	13.9
456.hmmer	1191	7.83	<b>1191</b>	<b>7.83</b>	1191	7.83	1161	8.04	<b>1161</b>	<b>8.04</b>	1161	8.04
458.sjeng	1059	11.4	<b>1059</b>	<b>11.4</b>	1059	11.4	974	12.4	974	12.4	<b>974</b>	<b>12.4</b>
462.libquantum	<b>1820</b>	<b>11.4</b>	1821	11.4	1820	11.4	<b>1801</b>	<b>11.5</b>	1800	11.5	1801	11.5
464.h264ref	1132	19.5	<b>1132</b>	<b>19.5</b>	1132	19.5	1110	19.9	<b>1110</b>	<b>19.9</b>	1110	19.9
471.omnetpp	692	9.04	692	9.03	<b>692</b>	<b>9.04</b>	628	9.95	628	9.94	<b>628</b>	<b>9.94</b>
473.astar	784	8.96	784	8.96	<b>784</b>	<b>8.96</b>	782	8.97	782	8.98	<b>782</b>	<b>8.97</b>
483.xalancbmk	<b>487</b>	<b>14.2</b>	487	14.2	487	14.2	<b>482</b>	<b>14.3</b>	481	14.3	482	14.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

### Other Configuration Notes

/NUMPROC=1 flag was added to boot.ini to invoke uniprocessor environment

## Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32

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## Base Portability Flags (Continued)

464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

## Peak Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32

464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Peak Optimization Flags

C benchmarks:

-Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

C++ benchmarks:

-Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx\_features  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags.20090714.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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