



SPEC® CFP2006 Result

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Hewlett-Packard Company

SPECfp®_rate2006 = 26.0

ProLiant BL480c
(1.86 GHz, Intel Xeon processor E5320)

SPECfp_rate_base2006 = 25.6

CPU2006 license: 3

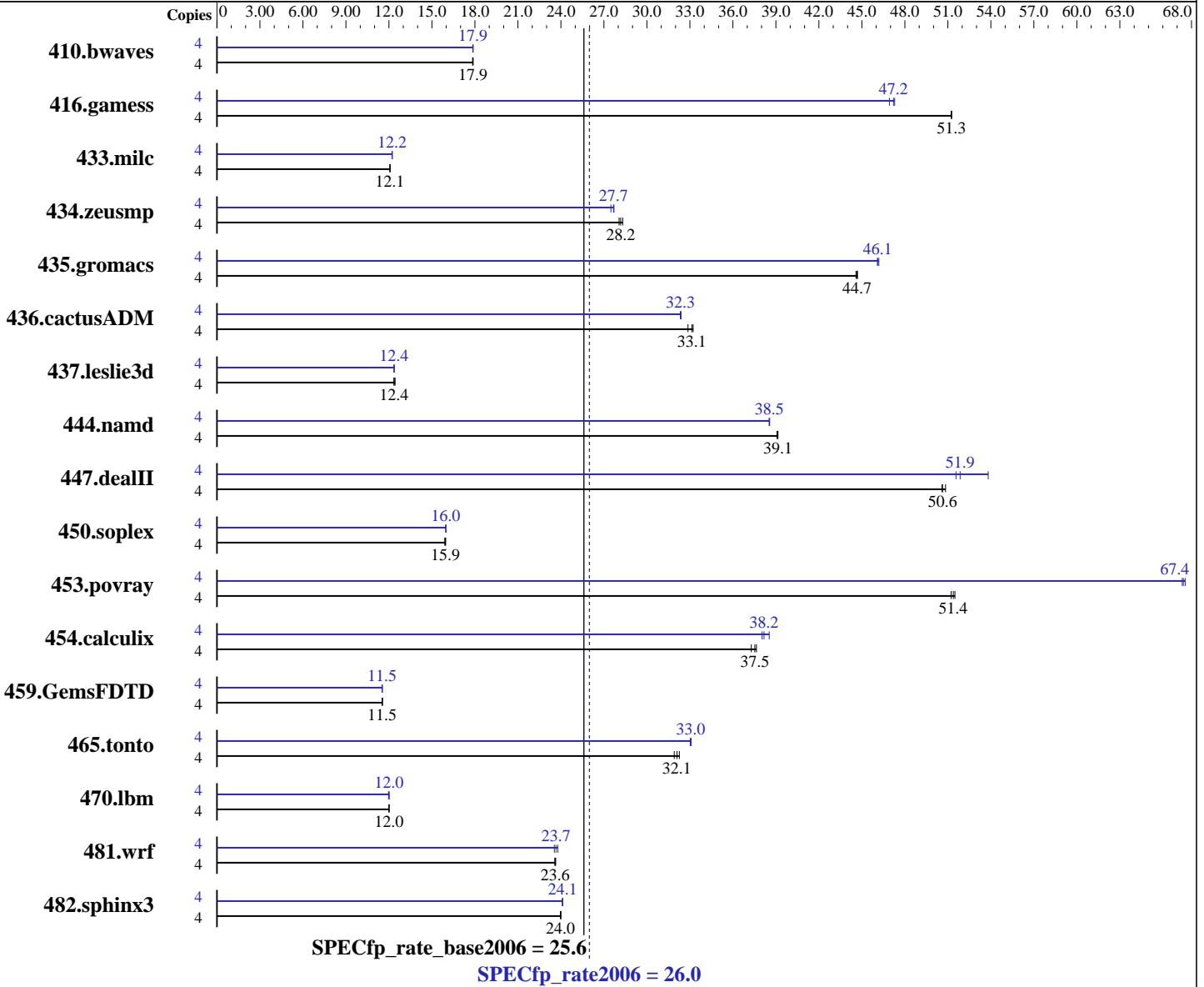
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64)
 kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1
 Build 20061101, Package ID: 1_cc_c_9.1.045
 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1
 Build 20061101, Package ID: 1_fc_c_9.1.040
 Auto Parallel: No

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 1x72 GB 10k SAS
Other Hardware: None

File System: ext2
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	3042	17.9	3048	17.8	3042	17.9	4	3041	17.9	3044	17.9	3045	17.9
416.gamess	4	1528	51.3	1528	51.3	1528	51.2	4	1659	47.2	1656	47.3	1669	46.9
433.milc	4	3041	12.1	3039	12.1	3041	12.1	4	3005	12.2	3003	12.2	3003	12.2
434.zeusmp	4	1292	28.2	1286	28.3	1297	28.1	4	1324	27.5	1316	27.7	1314	27.7
435.gromacs	4	641	44.6	639	44.7	639	44.7	4	620	46.1	620	46.1	618	46.2
436.cactusADM	4	1439	33.2	1442	33.1	1454	32.9	4	1478	32.3	1476	32.4	1478	32.3
437.leslie3d	4	3025	12.4	3040	12.4	3046	12.3	4	3043	12.4	3040	12.4	3042	12.4
444.namd	4	820	39.1	820	39.1	821	39.1	4	833	38.5	832	38.5	832	38.6
447.dealII	4	904	50.6	904	50.6	900	50.8	4	850	53.8	882	51.9	887	51.6
450.soplex	4	2090	16.0	2093	15.9	2099	15.9	4	2089	16.0	2089	16.0	2089	16.0
453.povray	4	414	51.4	413	51.5	415	51.2	4	316	67.4	316	67.4	315	67.6
454.calculix	4	885	37.3	877	37.6	879	37.5	4	856	38.5	868	38.0	865	38.2
459.GemsFDTD	4	3682	11.5	3674	11.6	3680	11.5	4	3682	11.5	3676	11.5	3683	11.5
465.tonto	4	1220	32.3	1234	31.9	1226	32.1	4	1192	33.0	1189	33.1	1191	33.0
470.lbm	4	4578	12.0	4578	12.0	4577	12.0	4	4576	12.0	4576	12.0	4590	12.0
481.wrf	4	1896	23.6	1892	23.6	1890	23.6	4	1898	23.5	1878	23.8	1889	23.7
482.sphinx3	4	3253	24.0	3251	24.0	3248	24.0	4	3231	24.1	3236	24.1	3232	24.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

Peak Compiler Invocation

C benchmarks:

icc

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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