SPEC® CFP2006 Result

Hewlett-Packard Company

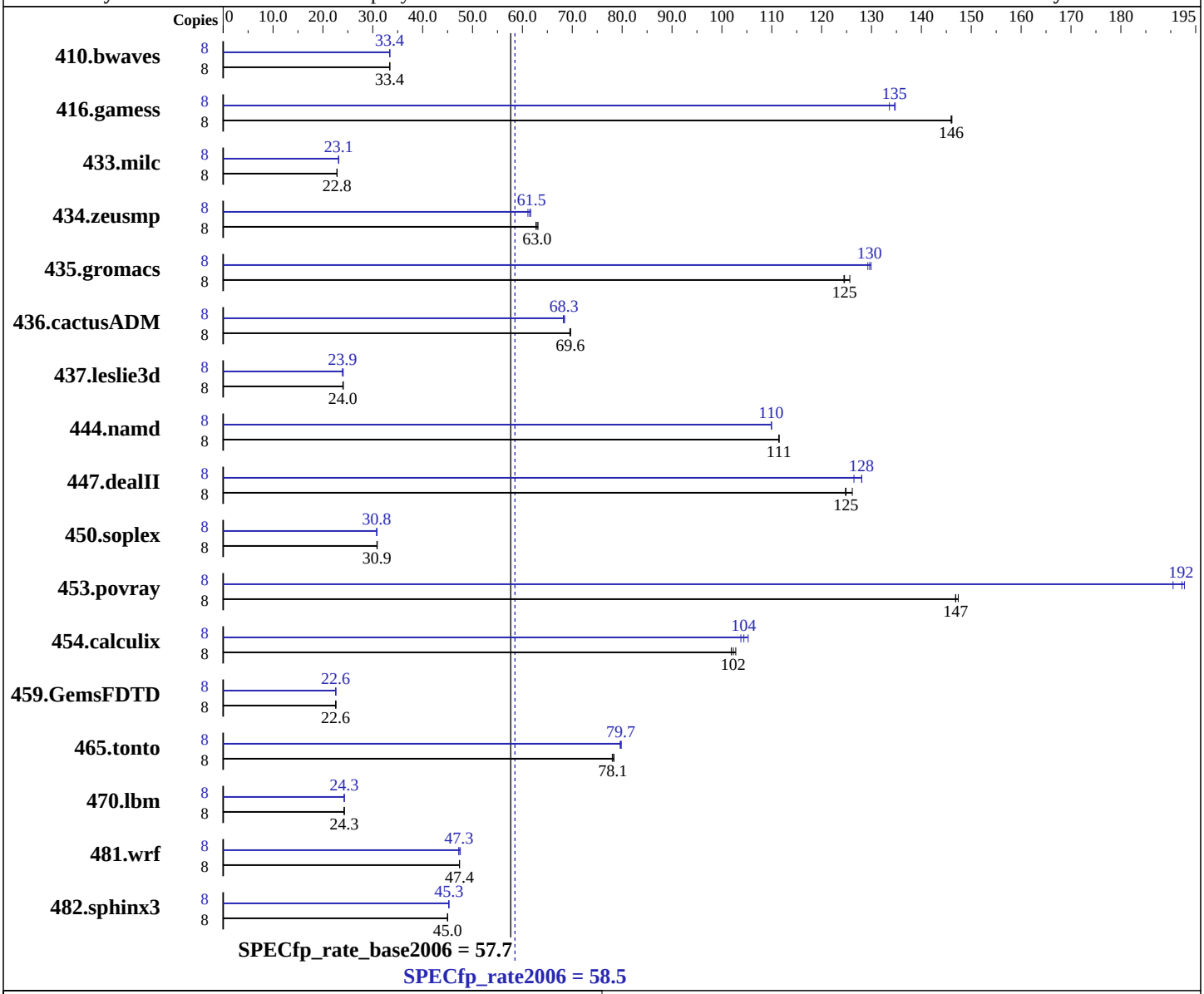
ProLiant ML370 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®_rate2006 = 58.5

SPECfp_rate_base2006 = 57.7

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Nov-2006
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333 MHz system bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_cc_c_9.1.045 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_fc_c_9.1.040
Auto Parallel:	No

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300F CL5)	Base Pointers:	64-bit
Disk Subsystem:	2x72 GB 10k SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	**3254**	**33.4**	3254	33.4	3255	33.4	8	**3254**	**33.4**	3256	33.4	3253	33.4
416.gamess	8	1073	146	1072	146	**1073**	**146**	8	**1164**	**135**	1173	134	1162	135
433.milc	8	3219	22.8	**3215**	**22.8**	3214	22.9	8	3173	23.1	3169	23.2	**3173**	**23.1**
434.zeusmp	8	1161	62.7	**1156**	**63.0**	1153	63.1	8	1191	61.1	**1184**	**61.5**	1181	61.7
435.gromacs	8	**458**	**125**	459	124	455	126	8	442	129	440	130	**441**	**130**
436.cactusADM	8	1372	69.7	**1373**	**69.6**	1375	69.5	8	1396	68.5	**1399**	**68.3**	1402	68.2
437.leslie3d	8	3120	24.1	3132	24.0	**3130**	**24.0**	8	**3140**	**23.9**	3142	23.9	3123	24.1
444.namd	8	576	111	576	111	**576**	**111**	8	**584**	**110**	583	110	584	110
447.dealII	8	733	125	725	126	**733**	**125**	8	**715**	**128**	715	128	724	126
450.soplex	8	2159	30.9	**2163**	**30.9**	2164	30.8	8	2167	30.8	2164	30.8	**2167**	**30.8**
453.povray	8	**290**	**147**	290	147	289	147	8	**221**	**192**	223	190	221	193
454.calculix	8	642	103	648	102	**645**	**102**	8	627	105	**632**	**104**	636	104
459.GemsFDTD	8	**3760**	**22.6**	3763	22.6	3753	22.6	8	3761	22.6	**3760**	**22.6**	3754	22.6
465.tonto	8	1009	78.0	1004	78.4	**1007**	**78.1**	8	**987**	**79.7**	989	79.6	986	79.8
470.lbm	8	4528	24.3	**4528**	**24.3**	4528	24.3	8	4527	24.3	**4526**	**24.3**	4526	24.3
481.wrf	8	1886	47.4	**1886**	**47.4**	1886	47.4	8	1892	47.2	1879	47.6	**1890**	**47.3**
482.sphinx3	8	3467	45.0	3462	45.0	**3466**	**45.0**	8	3449	45.2	3444	45.3	**3444**	**45.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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