SPEC® CINT2006 Result
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Fujitsu Siemens Computers

CELSIUS R540, Intel Xeon processor E5335

SPECint®2006 = 13.5

SPECint_base2006 = 12.9

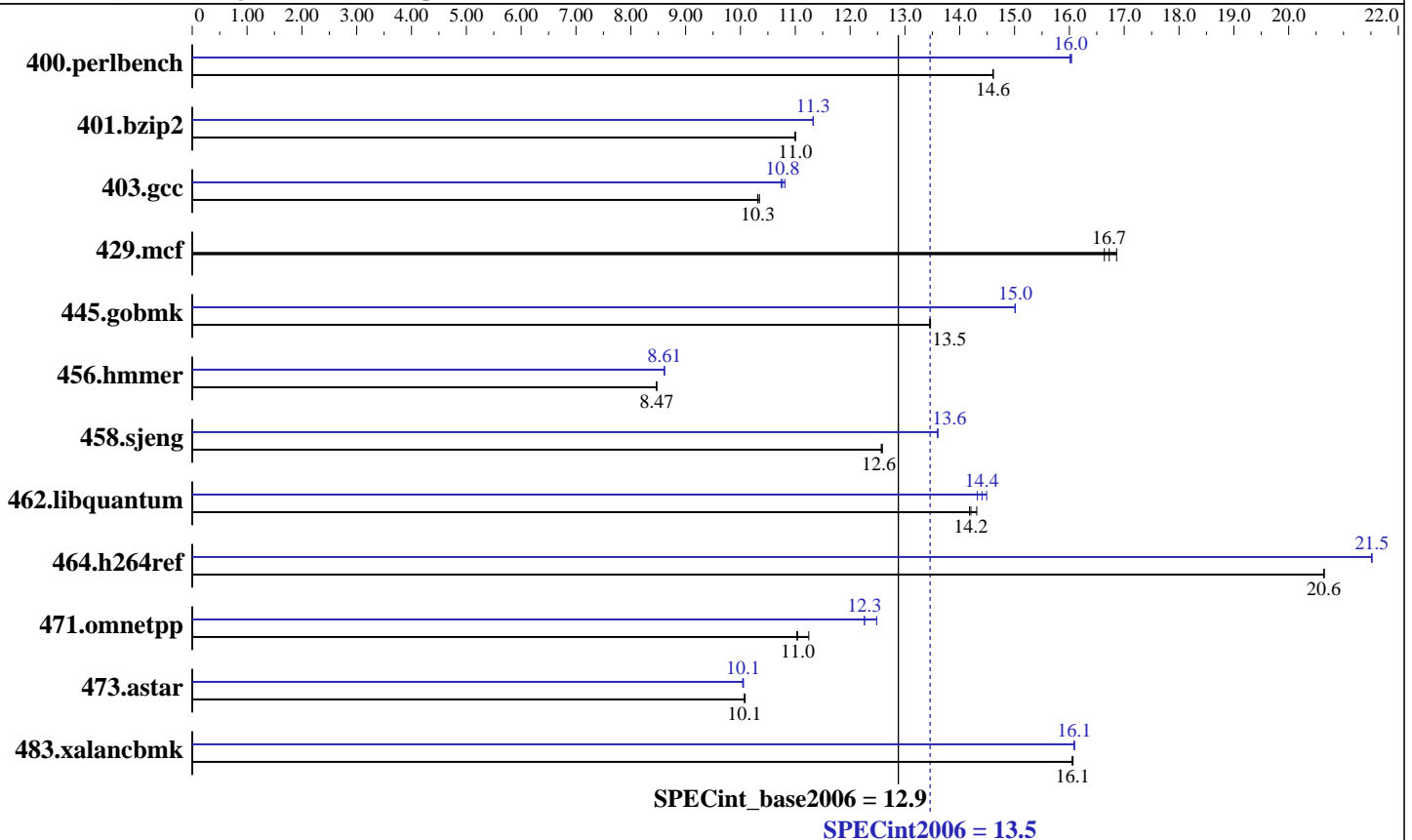**CPU2006 license:** 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Feb-2007
Hardware Availability: Nov-2006
Software Availability: Jan-2007

Benchmark	Value (peak)	Value (base)
400.perlbench	16.0	14.6
401.bzip2	11.3	11.0
403.gcc	10.8	10.3
429.mcf	16.7	
445.gobmk	15.0	13.5
456.hmmer	8.61	8.47
458.sjeng	13.6	12.6
462.libquantum	14.4	14.2
464.h264ref	21.5	20.6
471.omnetpp	12.3	11.0
473.astar	10.1	10.1
483.xalancbmk	16.1	16.1

SPECint_base2006 = 12.9
SPECint2006 = 13.5

Hardware

CPU Name: Intel Xeon E5335
CPU Characteristics: E5335
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB DDR2 5300F, 2 rank, CL5-5-5,with ECC)
Disk Subsystem: SATA II 7200 rpm
Other Hardware: None

Software

Operating System: Windows XP, 64 bit Edition
Compiler: Intel C++ Compiler for 32-bit applications
- Version 9.1, Build 20070109Z
Microsoft Visual Studio .NET 2003 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.0

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**<u>669</u>**	**<u>14.6</u>**	669	14.6	669	14.6	610	16.0	**<u>610</u>**	**<u>16.0</u>**	609	16.0
401.bzip2	**<u>877</u>**	**<u>11.0</u>**	878	11.0	877	11.0	**<u>852</u>**	**<u>11.3</u>**	852	11.3	852	11.3
403.gcc	**<u>780</u>**	**<u>10.3</u>**	781	10.3	778	10.3	745	10.8	**<u>748</u>**	**<u>10.8</u>**	749	10.7
429.mcf	**<u>545</u>**	**<u>16.7</u>**	541	16.9	548	16.6	**<u>545</u>**	**<u>16.7</u>**	541	16.9	548	16.6
445.gobmk	780	13.5	779	13.5	**<u>780</u>**	**<u>13.5</u>**	699	15.0	699	15.0	**<u>699</u>**	**<u>15.0</u>**
456.hmmer	1101	8.47	1101	8.47	**<u>1101</u>**	**<u>8.47</u>**	1083	8.61	1083	8.61	**<u>1083</u>**	**<u>8.61</u>**
458.sjeng	**<u>962</u>**	**<u>12.6</u>**	961	12.6	963	12.6	**<u>890</u>**	**<u>13.6</u>**	890	13.6	890	13.6
462.libquantum	1461	14.2	1448	14.3	**<u>1459</u>**	**<u>14.2</u>**	**<u>1438</u>**	**<u>14.4</u>**	1430	14.5	1447	14.3
464.h264ref	**<u>1072</u>**	**<u>20.6</u>**	1072	20.6	1072	20.6	1028	21.5	**<u>1028</u>**	**<u>21.5</u>**	1029	21.5
471.omnetpp	567	11.0	556	11.2	**<u>566</u>**	**<u>11.0</u>**	501	12.5	**<u>510</u>**	**<u>12.3</u>**	510	12.3
473.astar	696	10.1	**<u>697</u>**	**<u>10.1</u>**	697	10.1	**<u>698</u>**	**<u>10.1</u>**	699	10.0	698	10.1
483.xalancbmk	429	16.1	430	16.1	**<u>430</u>**	**<u>16.1</u>**	429	16.1	**<u>429</u>**	**<u>16.1</u>**	429	16.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
    BIOS default settings have been used, except:
    Snoop Filter Enabled
    Adjacent Cache Line Prefetch Enabled
```

General Notes

```
    For information about Fujitsu Siemens Computers in your country please see:
    http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
```
    icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
    icl -Qvc7.1
```

Base Portability Flags

```
    403.gcc: -DSPEC_CPU_WIN32
    464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

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Base Optimization Flags

C benchmarks:
```
-fast -F512000000 shlW32M.lib -link -FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx-features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
               shlW32M.lib -link -FORCE:MULTIPLE
```

```
401.bzip2: Same as 400.perlbench
```

```
403.gcc: Same as 400.perlbench
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: Same as 400.perlbench
```

```
456.hmmer: Same as 400.perlbench
```

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Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Jan-2007

Peak Optimization Flags (Continued)

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:
```
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
-F512000000 shlW32M.lib -link -FORCE:MULTIPLE
```

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.18.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.18.xml

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For other inquiries, please contact webmaster@spec.org.

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