



SPEC® CFP2006 Result

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Hewlett-Packard Company

SPECfp®_rate2006 = 26.0

ProLiant DL360 G5
(1.86 GHz, Intel Xeon processor E5320)

SPECfp_rate_base2006 = 25.6

CPU2006 license: 3

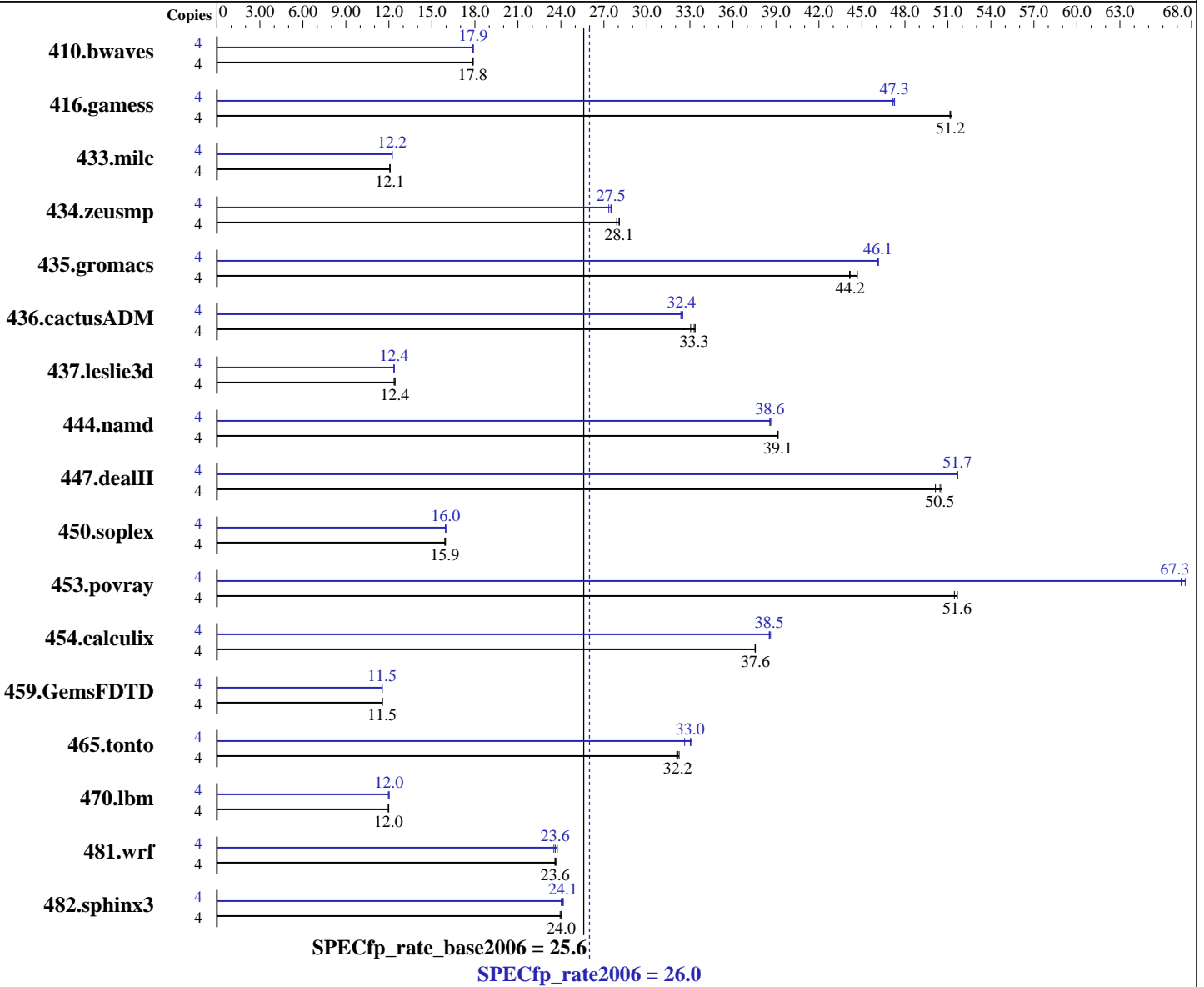
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1
 Build 20061101, Package ID: 1_cc_c_9.1.045
 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1
 Build 20061101, Package ID: 1_fc_c_9.1.040
 Auto Parallel: No

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 2x72 GB 10k SAS
Other Hardware: None

File System: ext2
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	3047	17.8	3039	17.9	<u>3046</u>	<u>17.8</u>	4	3039	17.9	<u>3040</u>	<u>17.9</u>	3045	17.9
416.gamess	4	1528	51.3	1531	51.1	<u>1530</u>	<u>51.2</u>	4	1657	47.3	<u>1657</u>	<u>47.3</u>	1661	47.2
433.milc	4	3041	12.1	3041	12.1	<u>3041</u>	<u>12.1</u>	4	3003	12.2	<u>3003</u>	<u>12.2</u>	3004	12.2
434.zeusmp	4	1305	27.9	1296	28.1	<u>1297</u>	<u>28.1</u>	4	1331	27.3	1324	27.5	<u>1324</u>	<u>27.5</u>
435.gromacs	4	647	44.1	639	44.7	<u>646</u>	<u>44.2</u>	4	619	46.1	619	46.1	<u>619</u>	<u>46.1</u>
436.cactusADM	4	1433	33.4	1446	33.1	<u>1435</u>	<u>33.3</u>	4	1471	32.5	<u>1475</u>	<u>32.4</u>	1477	32.4
437.leslie3d	4	<u>3029</u>	<u>12.4</u>	3025	12.4	3042	12.4	4	3039	12.4	<u>3043</u>	<u>12.4</u>	3045	12.3
444.namd	4	819	39.2	<u>820</u>	<u>39.1</u>	820	39.1	4	831	38.6	830	38.6	<u>831</u>	<u>38.6</u>
447.dealII	4	<u>907</u>	<u>50.5</u>	913	50.1	905	50.6	4	885	51.7	886	51.7	<u>886</u>	<u>51.7</u>
450.soplex	4	2091	16.0	2097	15.9	<u>2096</u>	<u>15.9</u>	4	2089	16.0	2091	16.0	<u>2090</u>	<u>16.0</u>
453.povray	4	<u>412</u>	<u>51.6</u>	412	51.7	414	51.5	4	315	67.6	<u>316</u>	<u>67.3</u>	316	67.3
454.calculix	4	878	37.6	<u>878</u>	<u>37.6</u>	879	37.5	4	856	38.5	854	38.6	<u>856</u>	<u>38.5</u>
459.GemsFDTD	4	3674	11.6	<u>3681</u>	<u>11.5</u>	3683	11.5	4	3680	11.5	<u>3681</u>	<u>11.5</u>	3684	11.5
465.tonto	4	1221	32.2	<u>1224</u>	<u>32.2</u>	1227	32.1	4	1189	33.1	<u>1192</u>	<u>33.0</u>	1206	32.6
470.lbm	4	4584	12.0	4599	11.9	<u>4590</u>	<u>12.0</u>	4	4575	12.0	4595	12.0	<u>4575</u>	<u>12.0</u>
481.wrf	4	1889	23.7	1894	23.6	<u>1894</u>	<u>23.6</u>	4	<u>1893</u>	<u>23.6</u>	1901	23.5	1881	23.8
482.sphinx3	4	3243	24.0	3256	23.9	<u>3244</u>	<u>24.0</u>	4	<u>3230</u>	<u>24.1</u>	3226	24.2	3245	24.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

Peak Compiler Invocation

C benchmarks:

icc

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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