SPEC® CFP2006 Result

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Itautec

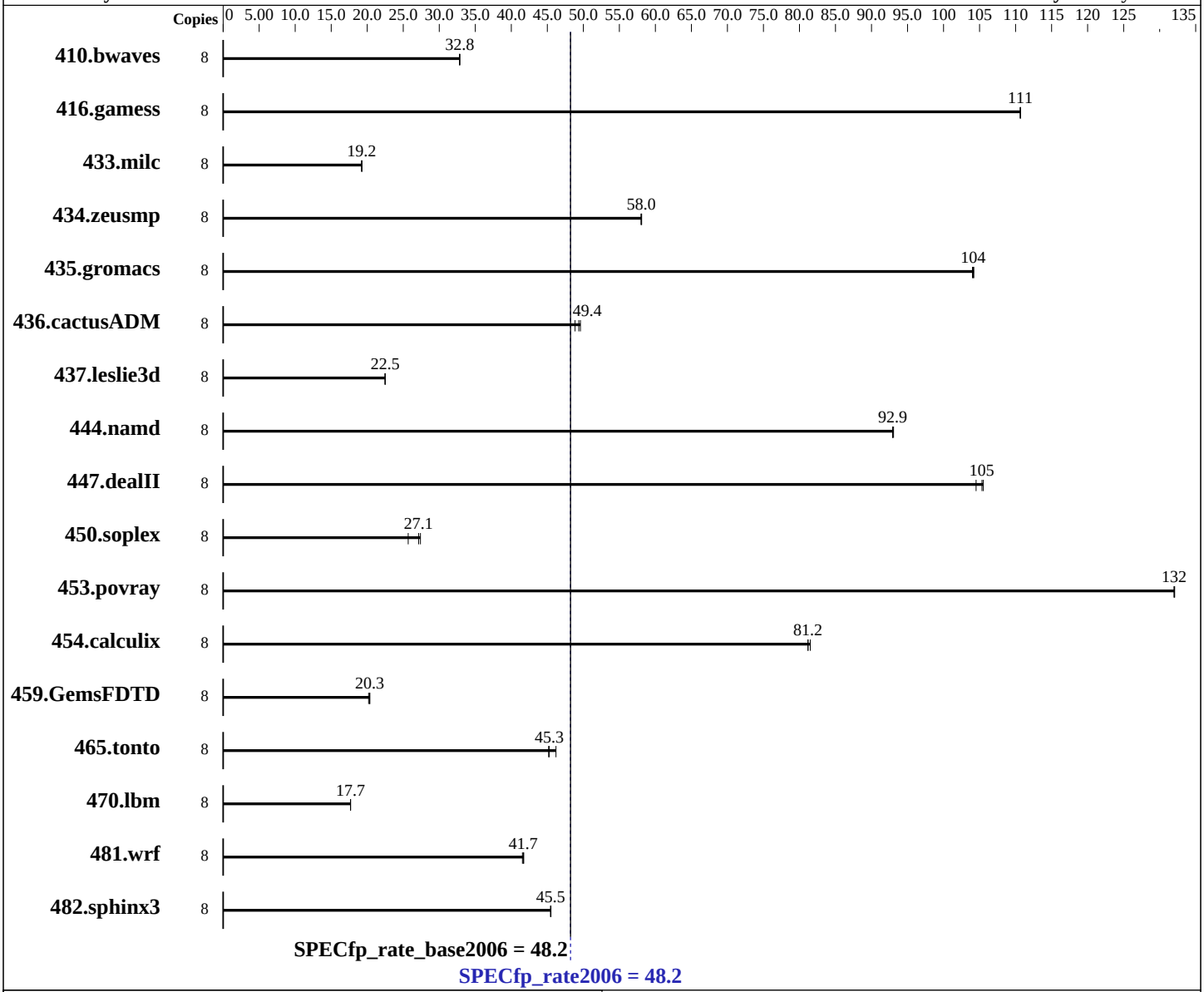
Servidor Itautec LX201 (Intel Xeon E5345)

SPECfp®_rate2006 = 48.2

SPECfp_rate_base2006 = 48.2

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006



Hardware

CPU Name:	Intel Xeon E5345
CPU Characteristics:	1333MHz system bus
CPU MHz:	2330
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1-2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.025 Build no 20060519Z Intel Fortran Compiler for IA32 version 9.1 Package ID W_FC_C_9.1.025 Build no 20060519Z Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: None
Other Cache: None
Memory: 8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
Disk Subsystem: 120 GB SATA, 7200RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: Microquill SmartHeap Library v.8.0 for SMP

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3314	32.8	3310	32.9	**3310**	**32.8**	8	3314	32.8	3310	32.9	**3310**	**32.8**
416.gamess	8	1416	111	**1415**	**111**	1415	111	8	1416	111	**1415**	**111**	1415	111
433.milc	8	3814	19.3	3819	19.2	**3817**	**19.2**	8	3814	19.3	3819	19.2	**3817**	**19.2**
434.zeusmp	8	1253	58.1	1255	58.0	**1254**	**58.0**	8	1253	58.1	1255	58.0	**1254**	**58.0**
435.gromacs	8	548	104	**549**	**104**	549	104	8	548	104	**549**	**104**	549	104
436.cactusADM	8	1957	48.8	**1937**	**49.4**	1928	49.6	8	1957	48.8	**1937**	**49.4**	1928	49.6
437.leslie3d	8	3348	22.5	**3342**	**22.5**	3342	22.5	8	3348	22.5	**3342**	**22.5**	3342	22.5
444.namd	8	689	93.1	**690**	**92.9**	690	92.9	8	689	93.1	**690**	**92.9**	690	92.9
447.dealII	8	**869**	**105**	867	106	876	105	8	**869**	**105**	867	106	876	105
450.soplex	8	2436	27.4	**2459**	**27.1**	2597	25.7	8	2436	27.4	**2459**	**27.1**	2597	25.7
453.povray	8	322	132	**322**	**132**	322	132	8	322	132	**322**	**132**	322	132
454.calculix	8	813	81.2	**813**	**81.2**	810	81.5	8	813	81.2	**813**	**81.2**	810	81.5
459.GemsFDTD	8	4170	20.4	4204	20.2	**4172**	**20.3**	8	4170	20.4	4204	20.2	**4172**	**20.3**
465.tonto	8	1743	45.2	**1739**	**45.3**	1704	46.2	8	1743	45.2	**1739**	**45.3**	1704	46.2
470.lbm	8	**6216**	**17.7**	6217	17.7	6216	17.7	8	**6216**	**17.7**	6217	17.7	6216	17.7
481.wrf	8	2142	41.7	**2145**	**41.7**	2151	41.5	8	2142	41.7	**2145**	**41.7**	2151	41.5
482.sphinx3	8	**3430**	**45.5**	3427	45.5	3430	45.5	8	**3430**	**45.5**	3427	45.5	3430	45.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

436.cactusADM: `-Qlowercase /assume:underscore`
444.namd: `-TP`
447.dealII: `-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T`
453.povray: `-DSPEC_CPU_WINDOWS_ICL`
454.calculix: `-DSPEC_CPU_NOZMODIFIER -Qlowercase`
481.wrf: `-DSPEC_CPU_WINDOWS_ICL`

Base Optimization Flags

C benchmarks:
`-fast /F950000000 shlSMPMt.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx_features /F950000000 shlSMPMt.lib -link /FORCE:MULTIPLE`

Fortran benchmarks:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Benchmarks using both Fortran and C:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`
470.lbm: `basepeak = yes`
482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `basepeak = yes`
447.dealII: `basepeak = yes`
450.soplex: `basepeak = yes`
453.povray: `basepeak = yes`

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: `basepeak = yes`

416.gamess: `basepeak = yes`

434.zeusmp: `basepeak = yes`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `basepeak = yes`

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml`

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