SPEC® CINT2006 Result

Bull SAS

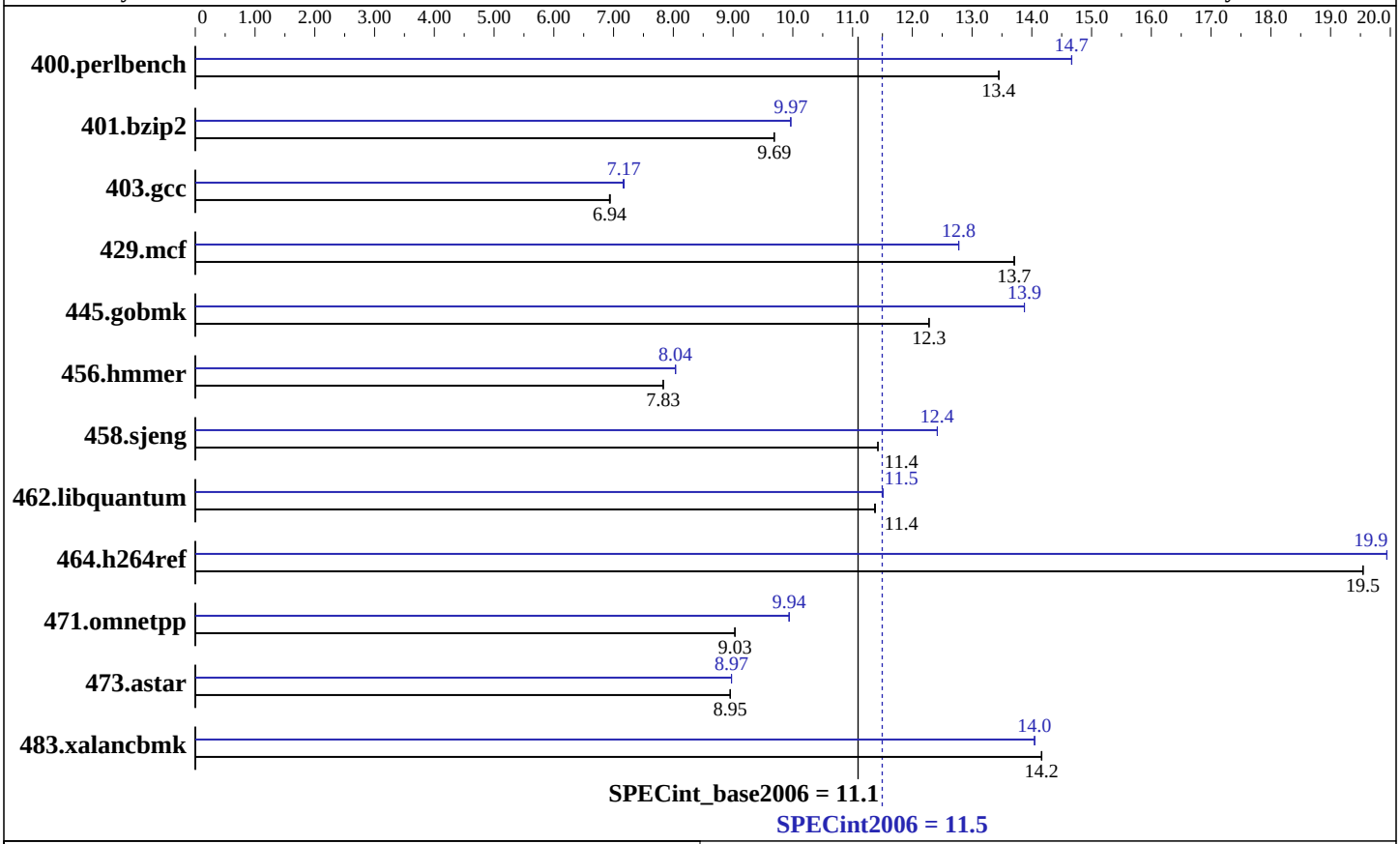
NovaScale B260 (Intel Xeon processor E5320,1.86GHz)

SPECint®2006 = 11.5

SPECint_base2006 = 11.1

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Dec-2006
Hardware Availability: Jan-2007
Software Availability: Dec-2006



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1.86 GHz, 8MB L2, 1066MHz bus
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 4 cores/chip
CPU(s) orderable:	1 to 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (2GB DIMMx4, FB-DIMM PC2-5300F ECC CL5)
Disk Subsystem:	73 GB SAS, 10000RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition (32 bits) Service Pack1
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.033 Build no 20061103Z Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	726	13.5	727	13.4	**726**	**13.4**	**666**	**14.7**	666	14.7	666	14.7
401.bzip2	**996**	**9.69**	996	9.69	996	9.69	968	9.97	968	9.97	**968**	**9.97**
403.gcc	**1160**	**6.94**	1161	6.93	1159	6.94	**1123**	**7.17**	1124	7.16	1122	7.18
429.mcf	**665**	**13.7**	666	13.7	665	13.7	714	12.8	**714**	**12.8**	714	12.8
445.gobmk	854	12.3	**854**	**12.3**	854	12.3	756	13.9	**756**	**13.9**	756	13.9
456.hmmer	1192	7.83	1191	7.83	**1191**	**7.83**	1161	8.04	1161	8.04	**1161**	**8.04**
458.sjeng	1059	11.4	**1059**	**11.4**	1059	11.4	974	12.4	974	12.4	**974**	**12.4**
462.libquantum	1823	11.4	**1821**	**11.4**	1821	11.4	**1800**	**11.5**	1801	11.5	1800	11.5
464.h264ref	1133	19.5	**1132**	**19.5**	1132	19.5	**1110**	**19.9**	1110	19.9	1110	19.9
471.omnetpp	692	9.03	692	9.03	**692**	**9.03**	**629**	**9.94**	629	9.94	629	9.94
473.astar	784	8.95	**784**	**8.95**	784	8.96	782	8.98	782	8.97	**782**	**8.97**
483.xalancbmk	487	14.2	487	14.2	**487**	**14.2**	491	14.0	491	14.0	**491**	**14.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/flags.20090715.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/flags.20090715.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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