SPEC® CINT2006 Result

Fujitsu Siemens Computers

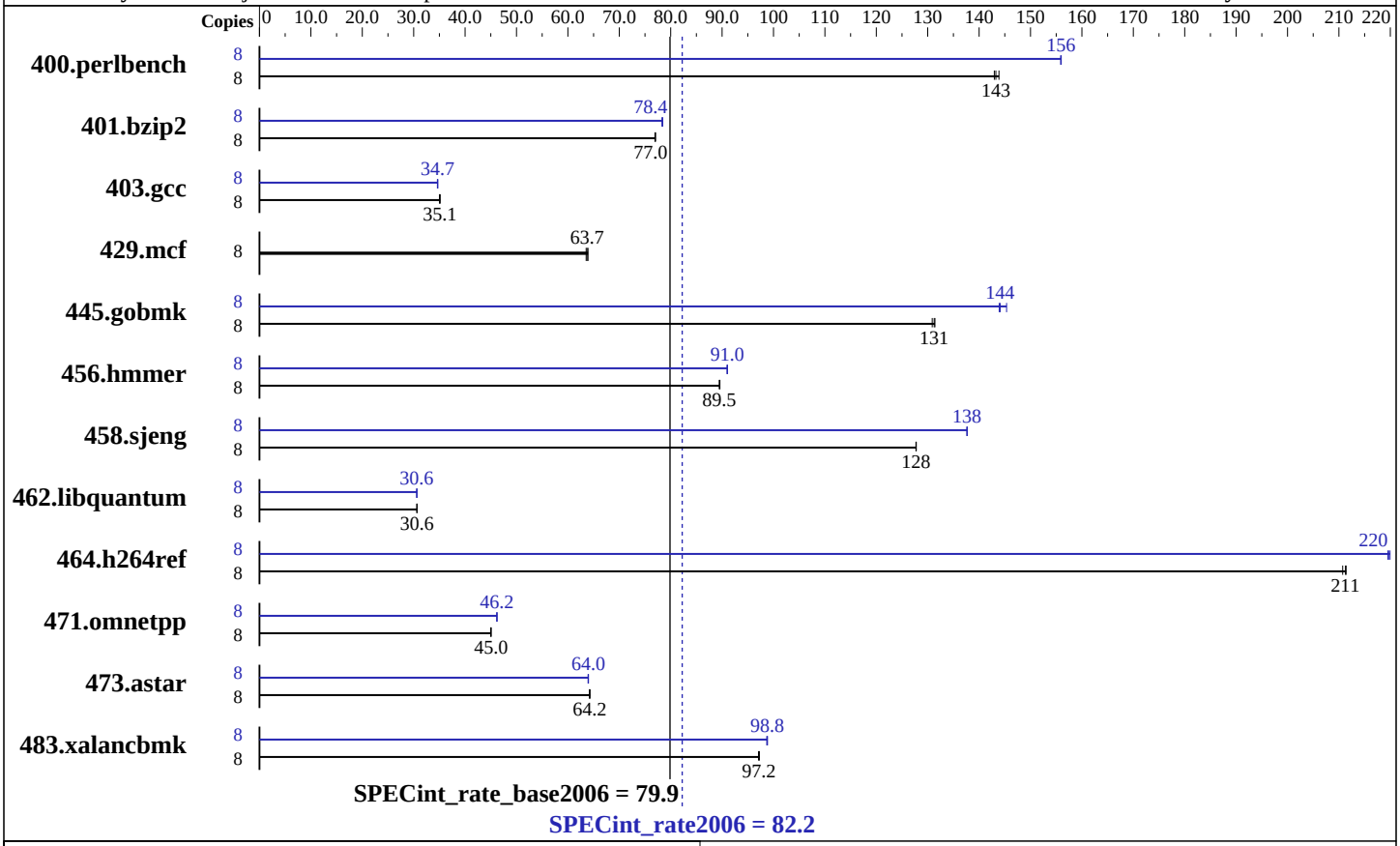
CELSIUS R640, Intel Xeon X5355 processor

SPECint®_rate2006 = 82.2

SPECint_rate_base2006 = 79.9

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Feb-2007
Hardware Availability: Nov-2006
Software Availability: Jan-2007



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	Dual Core, 2.66 GHz
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	SATA II 7200 rpm
Other Hardware:	None

Software

Operating System:	Windows XP, 64 bit Edition
Compiler:	Intel C++ Compiler for 32-bit applications, - version 9.1, Build 20070109Z Microsoft Visual Studio .NET 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	547	143	543	144	**546**	**143**	8	501	156	501	156	**501**	**156**
401.bzip2	8	1003	76.9	1002	77.1	**1003**	**77.0**	8	986	78.3	**985**	**78.4**	984	78.5
403.gcc	8	1833	35.1	**1835**	**35.1**	1840	35.0	8	1861	34.6	1857	34.7	**1857**	**34.7**
429.mcf	8	**1145**	**63.7**	1148	63.6	1141	63.9	8	**1145**	**63.7**	1148	63.6	1141	63.9
445.gobmk	8	641	131	**639**	**131**	639	131	8	577	145	**582**	**144**	583	144
456.hmmer	8	834	89.5	833	89.6	**834**	**89.5**	8	821	91.0	820	91.1	**820**	**91.0**
458.sjeng	8	**758**	**128**	758	128	758	128	8	703	138	703	138	**703**	**138**
462.libquantum	8	5412	30.6	5409	30.6	**5410**	**30.6**	8	5417	30.6	**5416**	**30.6**	5412	30.6
464.h264ref	8	**838**	**211**	840	211	837	211	8	**806**	**220**	806	220	805	220
471.omnetpp	8	1110	45.1	1111	45.0	**1110**	**45.0**	8	**1082**	**46.2**	1083	46.2	1082	46.2
473.astar	8	875	64.2	**874**	**64.2**	873	64.3	8	877	64.0	879	63.9	**878**	**64.0**
483.xalancbmk	8	569	97.1	**568**	**97.2**	568	97.2	8	559	98.7	559	98.8	**559**	**98.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
BIOS default settings have been used, except:
High Bandwith Option Enabled
```

General Notes

```
'start /b /wait /affinity' command is used to bind CPU(s) to processes
For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast -F512000000 shlW32M.lib -link -FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx-features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmer: Same as 400.perlbench

Continued on next page

Peak Optimization Flags (Continued)

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
-F512000000 shlW32M.lib -link -FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.19.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.19.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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