



SPEC® CINT2006 Result

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Itautec

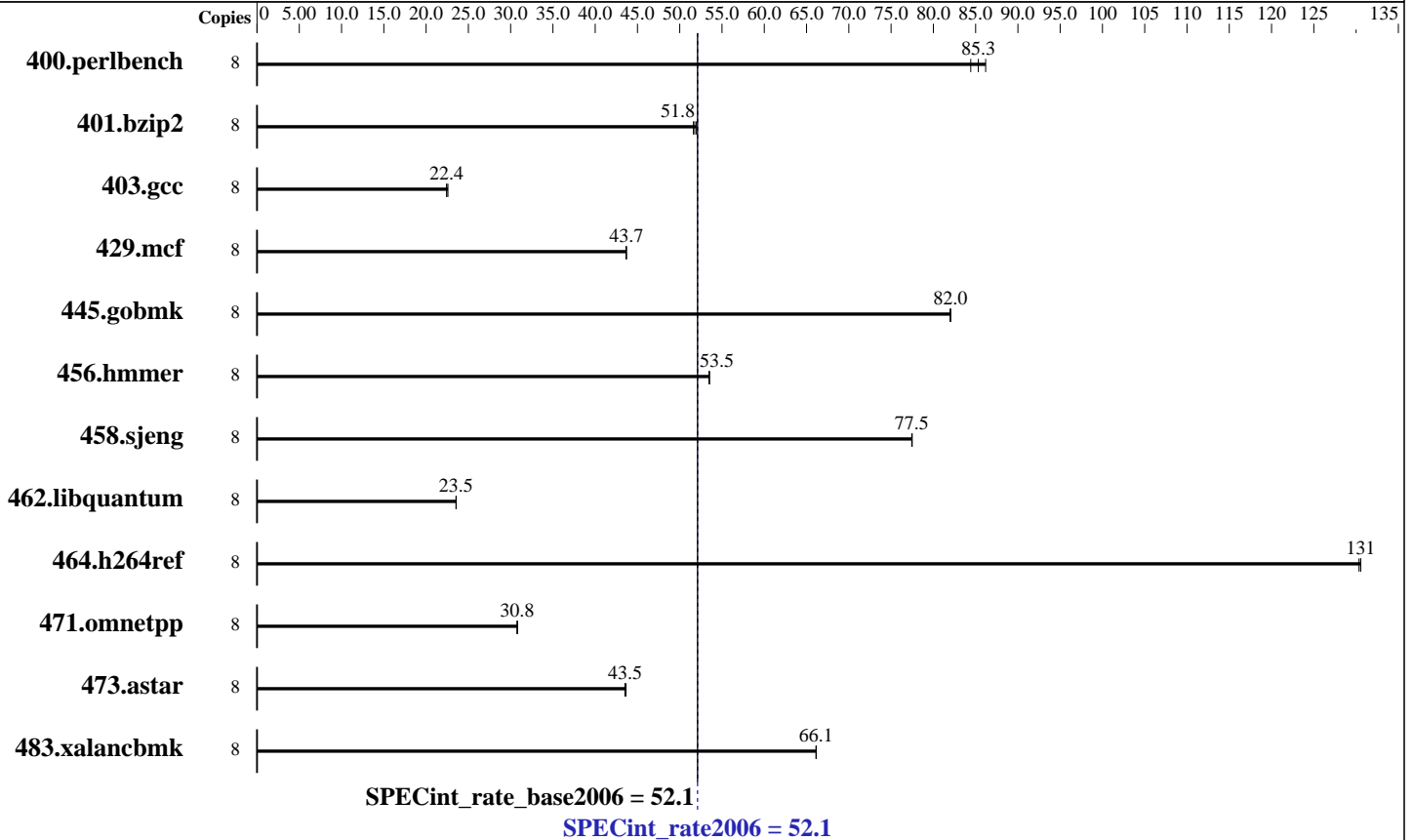
SPECint®_rate2006 = 52.1

Servidor Itautec MX201 (Intel Xeon E5310)

SPECint_rate_base2006 = 52.1

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006



Hardware

CPU Name: Intel Xeon E5310
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1-2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
 Disk Subsystem: 146 GB SCSI, 10000RPM
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.025 Build no 20060519Z
 Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: Microquill SmartHeap Library v.8.0 for SMP



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	907	86.2	926	84.4	<u>916</u>	<u>85.3</u>	8	907	86.2	926	84.4	<u>916</u>	<u>85.3</u>
401.bzip2	8	1496	51.6	1486	52.0	<u>1491</u>	<u>51.8</u>	8	1496	51.6	1486	52.0	<u>1491</u>	<u>51.8</u>
403.gcc	8	2852	22.6	<u>2871</u>	<u>22.4</u>	2871	22.4	8	2852	22.6	<u>2871</u>	<u>22.4</u>	2871	22.4
429.mcf	8	<u>1670</u>	<u>43.7</u>	1670	43.7	1672	43.6	8	<u>1670</u>	<u>43.7</u>	1670	43.7	1672	43.6
445.gobmk	8	1023	82.0	1023	82.0	<u>1023</u>	<u>82.0</u>	8	1023	82.0	1023	82.0	<u>1023</u>	<u>82.0</u>
456.hammer	8	<u>1395</u>	<u>53.5</u>	1396	53.5	1395	53.5	8	<u>1395</u>	<u>53.5</u>	1396	53.5	1395	53.5
458.sjeng	8	1249	77.5	<u>1250</u>	<u>77.5</u>	1250	77.5	8	1249	77.5	<u>1250</u>	<u>77.5</u>	1250	77.5
462.libquantum	8	7042	23.5	7034	23.6	<u>7039</u>	<u>23.5</u>	8	7042	23.5	7034	23.6	<u>7039</u>	<u>23.5</u>
464.h264ref	8	1356	131	1358	130	<u>1356</u>	<u>131</u>	8	1356	131	1358	130	<u>1356</u>	<u>131</u>
471.omnetpp	8	1623	30.8	<u>1625</u>	<u>30.8</u>	1625	30.8	8	1623	30.8	<u>1625</u>	<u>30.8</u>	1625	30.8
473.astar	8	1287	43.6	1291	43.5	<u>1290</u>	<u>43.5</u>	8	1287	43.6	1291	43.5	<u>1290</u>	<u>43.5</u>
483.xalancbmk	8	834	66.2	<u>835</u>	<u>66.1</u>	835	66.1	8	834	66.2	<u>835</u>	<u>66.1</u>	835	66.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result was measured on the Servidor Itaotec MX221.
The Servidor Itaotec MX221 and the Servidor Itaotec MX201
are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlsmpmt.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 sh1SMPMt.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Optimization Flags

C benchmarks:
400.perlbench: basepeak = yes
401.bzip2: basepeak = yes
403.gcc: basepeak = yes
429.mcf: basepeak = yes
445.gobmk: basepeak = yes
456.hmmmer: basepeak = yes
458.sjeng: basepeak = yes
462.libquantum: basepeak = yes
464.h264ref: basepeak = yes
C++ benchmarks:
471.omnetpp: basepeak = yes
473.astar: basepeak = yes
483.xalanbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Itaotec-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Itaotec-ic91-flags.20090714.xml>



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For other inquiries, please contact webmaster@spec.org.

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