SPEC® CINT2006 Result

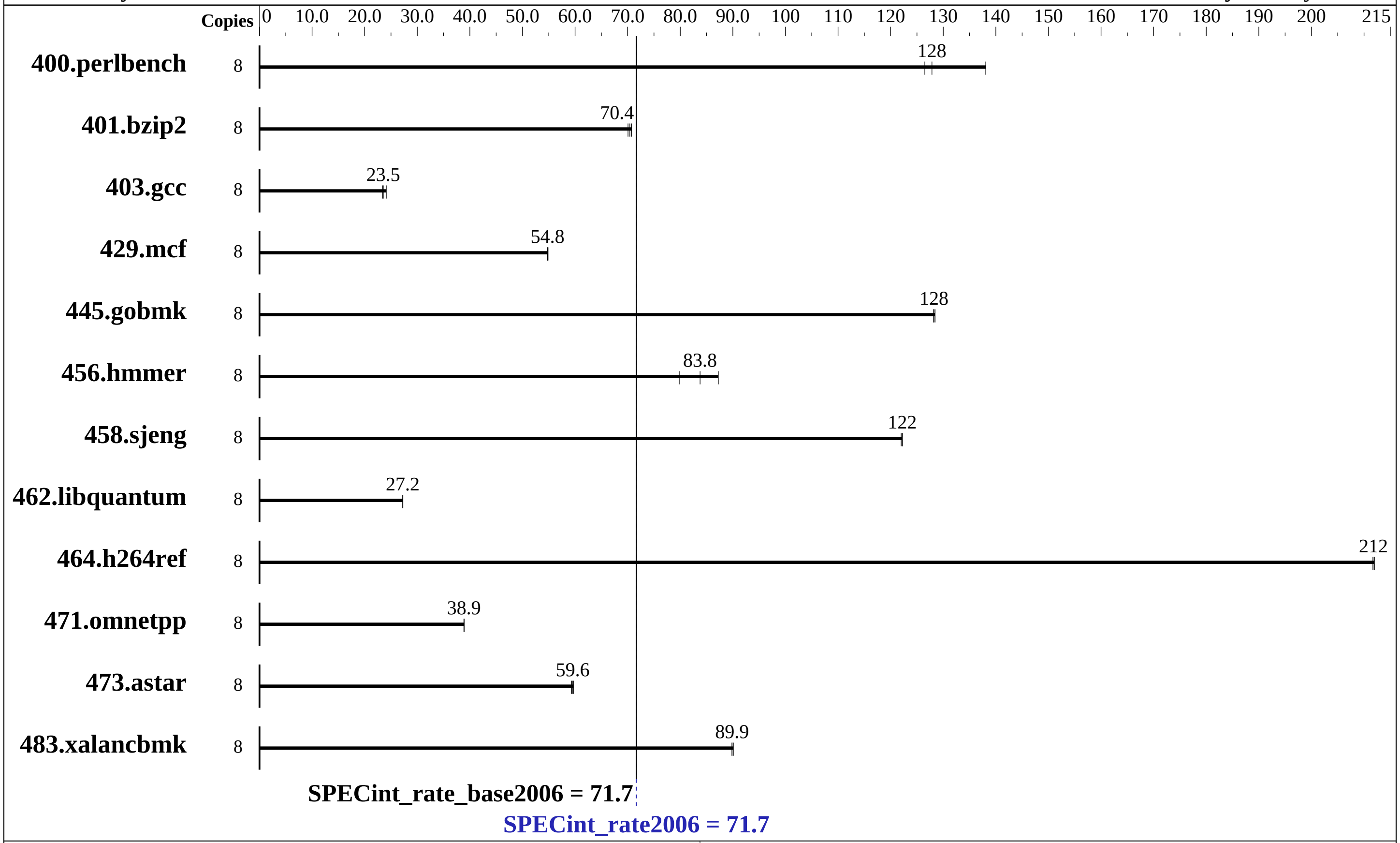
Itautec

Servidor Itautec LX201 (Intel Xeon X5355)

SPECint®_rate2006 = 71.7

SPECint_rate_base2006 = 71.7

CPU2006 license:	9001	**Test date:**	Feb-2007
Test sponsor:	Itautec	**Hardware Availability:**	Feb-2007
Tested by:	Itautec	**Software Availability:**	May-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	1333MHz system bus
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1-2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
Disk Subsystem:	120 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.025 Build no 20060519Z Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	Microquill SmartHeap Library v.8.0 for SMP

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Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	566	138	618	126	**611**	**128**	8	566	138	618	126	**611**	**128**
401.bzip2	8	1102	70.1	**1096**	**70.4**	1091	70.7	8	1102	70.1	**1096**	**70.4**	1091	70.7
403.gcc	8	2671	24.1	**2736**	**23.5**	2750	23.4	8	2671	24.1	**2736**	**23.5**	2750	23.4
429.mcf	8	1332	54.8	**1332**	**54.8**	1330	54.9	8	1332	54.8	**1332**	**54.8**	1330	54.9
445.gobmk	8	655	128	653	128	**654**	**128**	8	655	128	653	128	**654**	**128**
456.hmmer	8	856	87.2	935	79.8	**891**	**83.8**	8	856	87.2	935	79.8	**891**	**83.8**
458.sjeng	8	793	122	792	122	**792**	**122**	8	793	122	792	122	**792**	**122**
462.libquantum	8	**6084**	**27.2**	6086	27.2	6079	27.3	8	**6084**	**27.2**	6086	27.2	6079	27.3
464.h264ref	8	**835**	**212**	836	212	835	212	8	**835**	**212**	836	212	835	212
471.omnetpp	8	1285	38.9	**1285**	**38.9**	1287	38.9	8	1285	38.9	**1285**	**38.9**	1287	38.9
473.astar	8	941	59.7	947	59.3	**943**	**59.6**	8	941	59.7	947	59.3	**943**	**59.6**
483.xalancbmk	8	615	89.8	**614**	**89.9**	613	90.1	8	615	89.8	**614**	**89.9**	613	90.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
`-fast /F512000000 shlSMPMt.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx_features /F512000000 shlSMPMt.lib -link /FORCE:MULTIPLE`

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Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Optimization Flags

C benchmarks:

400.perlbench: `basepeak = yes`

401.bzip2: `basepeak = yes`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `basepeak = yes`

456.hmmer: `basepeak = yes`

458.sjeng: `basepeak = yes`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

C++ benchmarks:

471.omnetpp: `basepeak = yes`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:30:07 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 7 March 2007.