



# SPEC® CINT2006 Result

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Itautec

SPECint®\_rate2006 = 67.3

Servidor Itautec LX201 (Intel Xeon E5345)

SPECint\_rate\_base2006 = 67.3

CPU2006 license: 9001

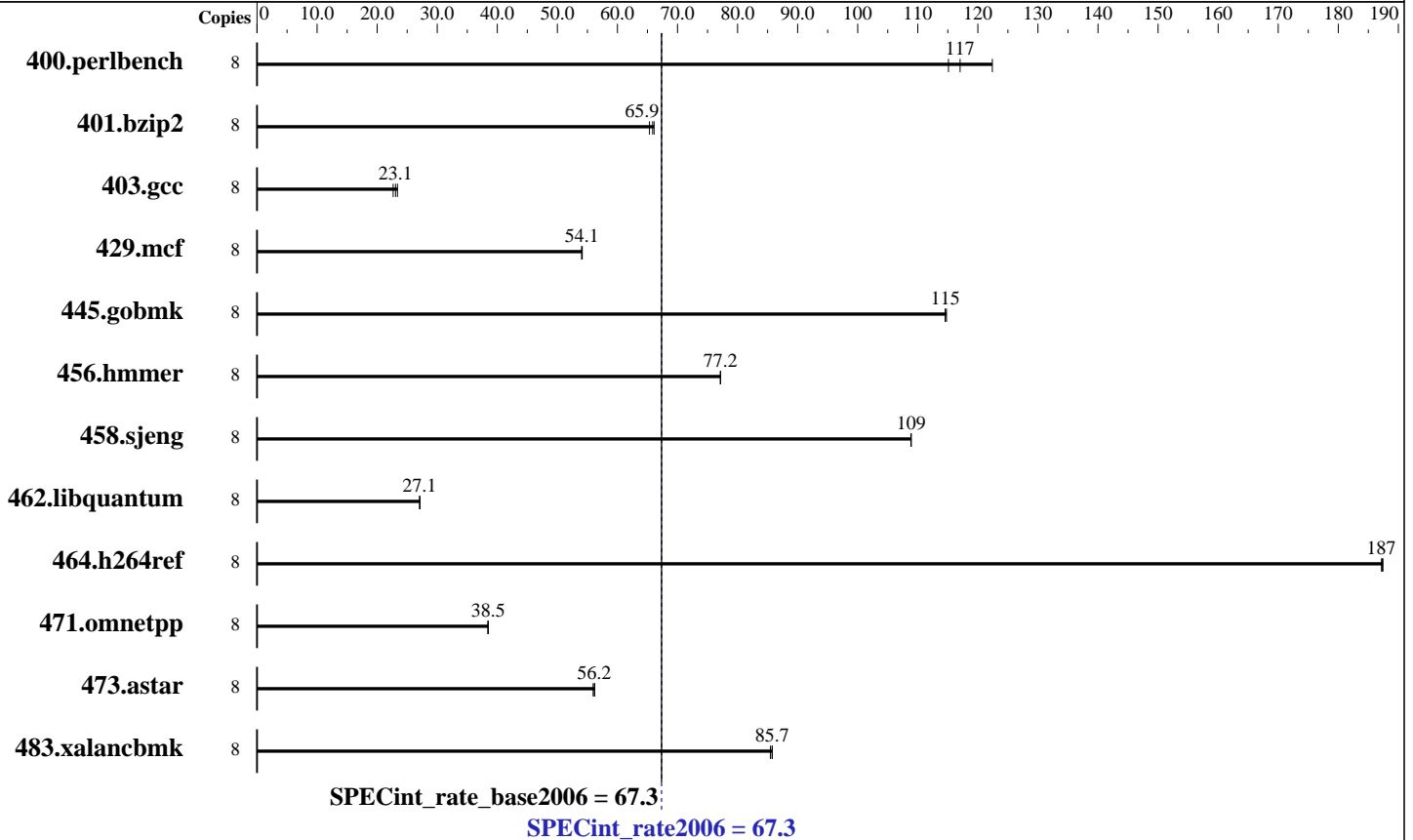
Test sponsor: Itautec

Tested by: Itautec

Test date: Feb-2007

Hardware Availability: Feb-2007

Software Availability: May-2006



## Hardware

CPU Name: Intel Xeon E5345  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 2330  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1-2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)  
 Disk Subsystem: 120 GB SATA, 7200RPM  
 Other Hardware: None

## Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Package ID W\_CC\_C\_9.1.025 Build no 20060519Z  
 Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: Microquill SmartHeap Library v.8.0 for SMP



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	639	122	679	115	<b>668</b>	<b>117</b>	8	639	122	679	115	<b>668</b>	<b>117</b>
401.bzip2	8	1182	65.3	<b>1172</b>	<b>65.9</b>	1168	66.1	8	1182	65.3	<b>1172</b>	<b>65.9</b>	1168	66.1
403.gcc	8	<b>2793</b>	<b>23.1</b>	2754	23.4	2841	22.7	8	<b>2793</b>	<b>23.1</b>	2754	23.4	2841	22.7
429.mcf	8	1350	54.1	1348	54.1	<b>1349</b>	<b>54.1</b>	8	1350	54.1	1348	54.1	<b>1349</b>	<b>54.1</b>
445.gobmk	8	732	115	731	115	<b>732</b>	<b>115</b>	8	732	115	731	115	<b>732</b>	<b>115</b>
456.hammer	8	<b>967</b>	<b>77.2</b>	968	77.1	967	77.2	8	<b>967</b>	<b>77.2</b>	968	77.1	967	77.2
458.sjeng	8	889	109	<b>889</b>	<b>109</b>	889	109	8	889	109	<b>889</b>	<b>109</b>	889	109
462.libquantum	8	<b>6120</b>	<b>27.1</b>	6112	27.1	6121	27.1	8	<b>6120</b>	<b>27.1</b>	6112	27.1	6121	27.1
464.h264ref	8	<b>945</b>	<b>187</b>	945	187	946	187	8	<b>945</b>	<b>187</b>	945	187	946	187
471.omnetpp	8	<b>1300</b>	<b>38.5</b>	1299	38.5	1300	38.4	8	<b>1300</b>	<b>38.5</b>	1299	38.5	1300	38.4
473.astar	8	<b>1000</b>	<b>56.2</b>	999	56.2	1004	56.0	8	<b>1000</b>	<b>56.2</b>	999	56.2	1004	56.0
483.xalancbmk	8	646	85.5	643	85.8	<b>644</b>	<b>85.7</b>	8	646	85.5	643	85.8	<b>644</b>	<b>85.7</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99  
C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlSMPMt.lib -link /FORCE:MULTIPLE  
C++ benchmarks:  
-fast -Qcxx\_features /F512000000 shlSMPMt.lib  
-link /FORCE:MULTIPLE



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Optimization Flags

C benchmarks:

400.perlbench: basepeak = yes

401.bzip2: basepeak = yes

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at  
<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:  
<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml>



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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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