



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = **Not Run**

IBM BladeCenter HS21 XM (Intel Xeon 5140)

SPECint_base2006 = **14.0**

CPU2006 license: 11

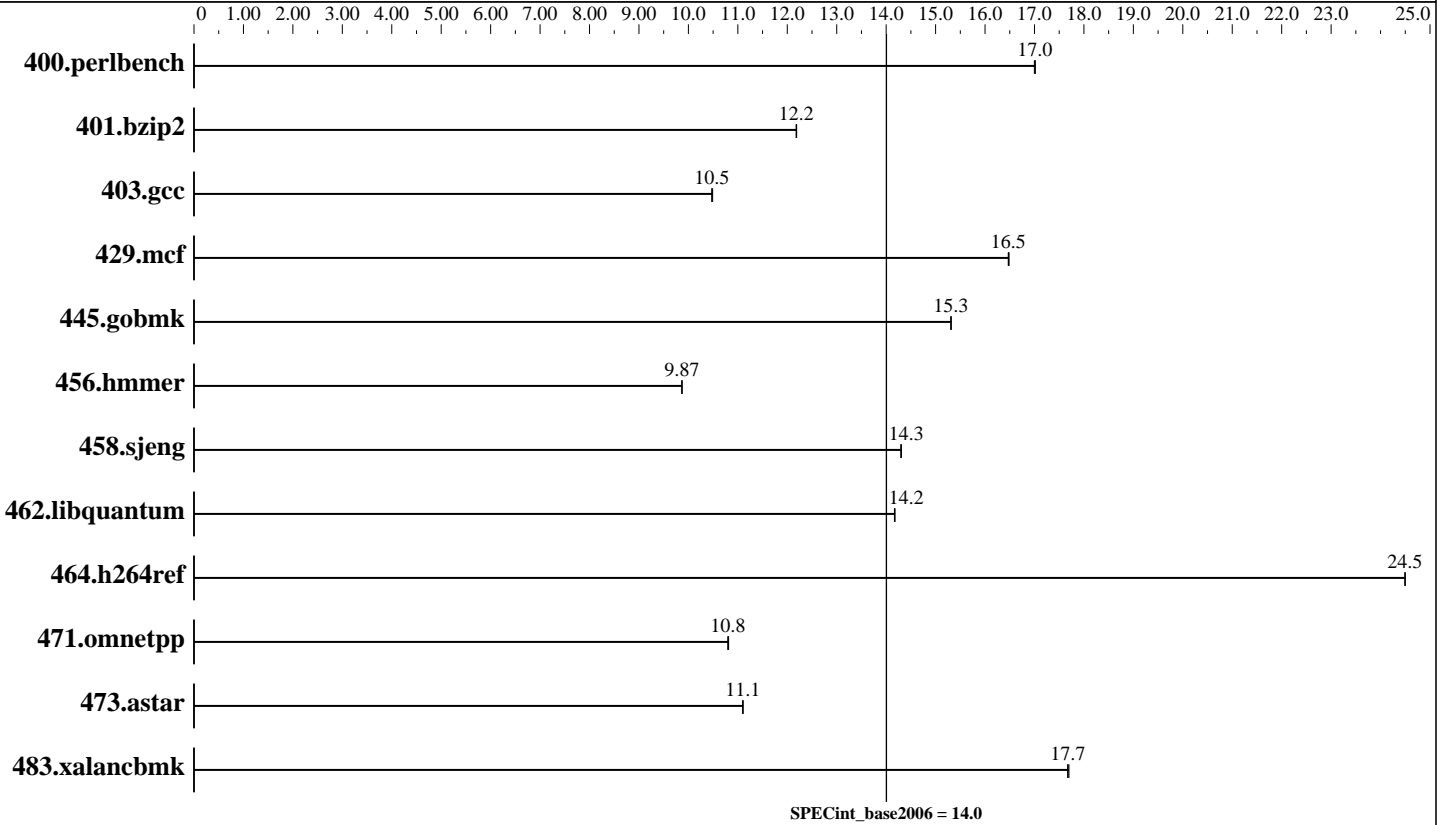
Test date: Jan-2007

Test sponsor: IBM Corporation

Hardware Availability: Feb-2007

Tested by: IBM Corporation

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon 5140
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 74 GB SAS, 1000 RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	574	17.0	<u>574</u>	<u>17.0</u>	575	17.0						
401.bzip2	792	12.2	792	12.2	<u>792</u>	<u>12.2</u>						
403.gcc	<u>768</u>	<u>10.5</u>	767	10.5	768	10.5						
429.mcf	<u>554</u>	<u>16.5</u>	554	16.5	553	16.5						
445.gobmk	<u>685</u>	<u>15.3</u>	685	15.3	685	15.3						
456.hammer	<u>945</u>	<u>9.87</u>	945	9.87	945	9.87						
458.sjeng	846	14.3	846	14.3	<u>846</u>	<u>14.3</u>						
462.libquantum	<u>1462</u>	<u>14.2</u>	1463	14.2	1461	14.2						
464.h264ref	903	24.5	<u>903</u>	<u>24.5</u>	903	24.5						
471.omnetpp	<u>579</u>	<u>10.8</u>	579	10.8	578	10.8						
473.astar	<u>632</u>	<u>11.1</u>	633	11.1	632	11.1						
483.xalancbmk	390	17.7	390	17.7	<u>390</u>	<u>17.7</u>						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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