



# SPEC® CINT2006 Result

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## IBM Corporation

## SPECint®\_rate2006 = Not Run

### IBM BladeCenter HS21 XM (Intel Xeon 5120)

## SPECint\_rate\_base2006 = 36.3

CPU2006 license: 11

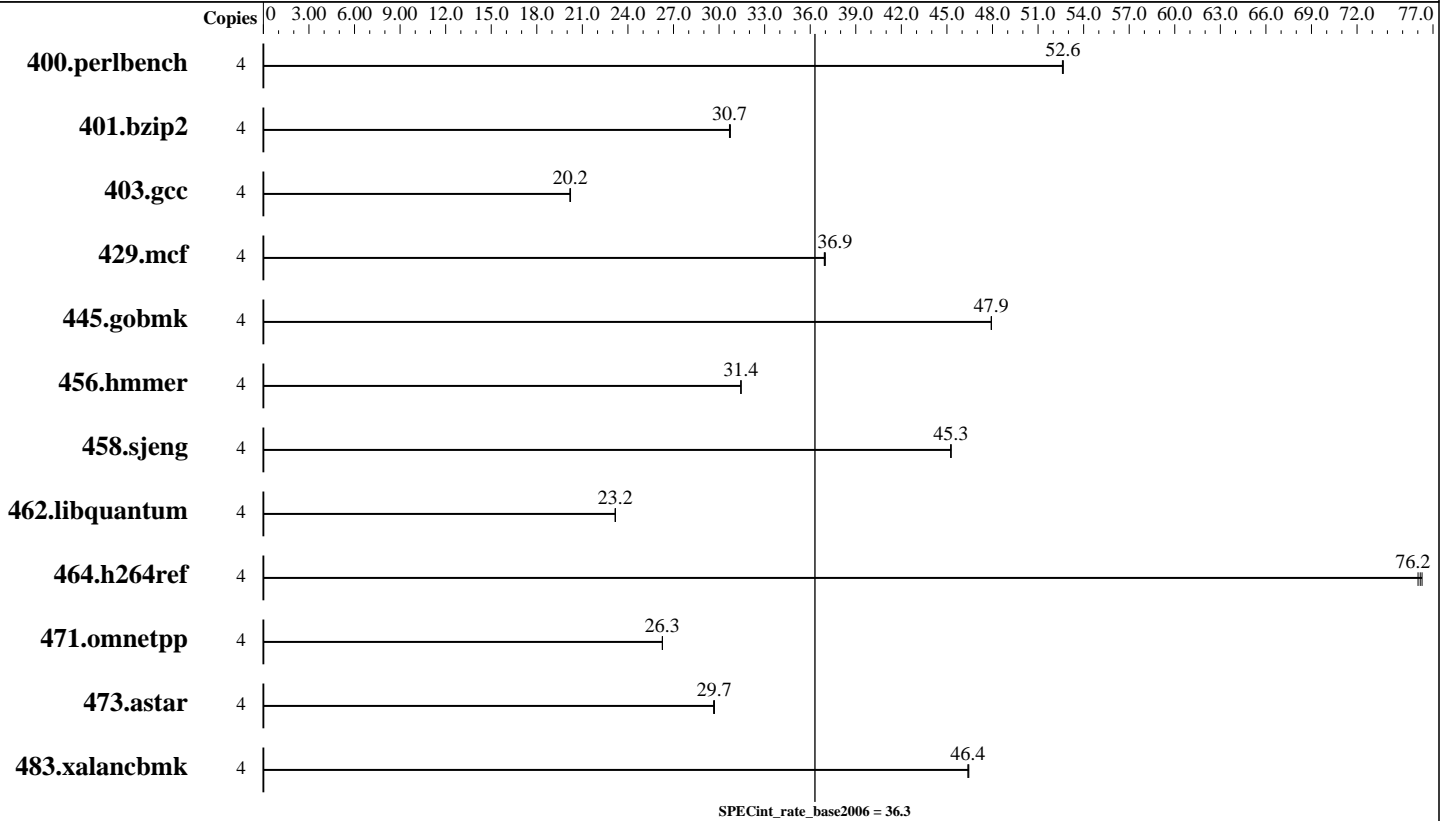
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2007

Hardware Availability: Feb-2007

Software Availability: Aug-2006



### Hardware

CPU Name: Intel Xeon 5120  
 CPU Characteristics: 1066MHz system bus  
 CPU MHz: 1866  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2GB DDR2-5300F ECC)  
 Disk Subsystem: 1 x 74 GB SAS, 1000 RPM  
 Other Hardware: None

### Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Build no 20060816  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Smart Heap Library, Version 8



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	743	52.6	742	52.7	<b>742</b>	<b>52.6</b>							
401.bzip2	4	1255	30.7	<b>1257</b>	<b>30.7</b>	1258	30.7							
403.gcc	4	1593	20.2	<b>1594</b>	<b>20.2</b>	1595	20.2							
429.mcf	4	<b>987</b>	<b>36.9</b>	986	37.0	988	36.9							
445.gobmk	4	876	47.9	875	47.9	<b>876</b>	<b>47.9</b>							
456.hammer	4	1187	31.4	1187	31.4	<b>1187</b>	<b>31.4</b>							
458.sjeng	4	1069	45.3	1070	45.3	<b>1069</b>	<b>45.3</b>							
462.libquantum	4	<b>3577</b>	<b>23.2</b>	3577	23.2	3578	23.2							
464.h264ref	4	<b>1162</b>	<b>76.2</b>	1165	76.0	1160	76.3							
471.omnetpp	4	952	26.3	952	26.3	<b>952</b>	<b>26.3</b>							
473.astar	4	946	29.7	947	29.6	<b>946</b>	<b>29.7</b>							
483.xalancbmk	4	<b>595</b>	<b>46.4</b>	594	46.4	595	46.4							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

## Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

## Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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