SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

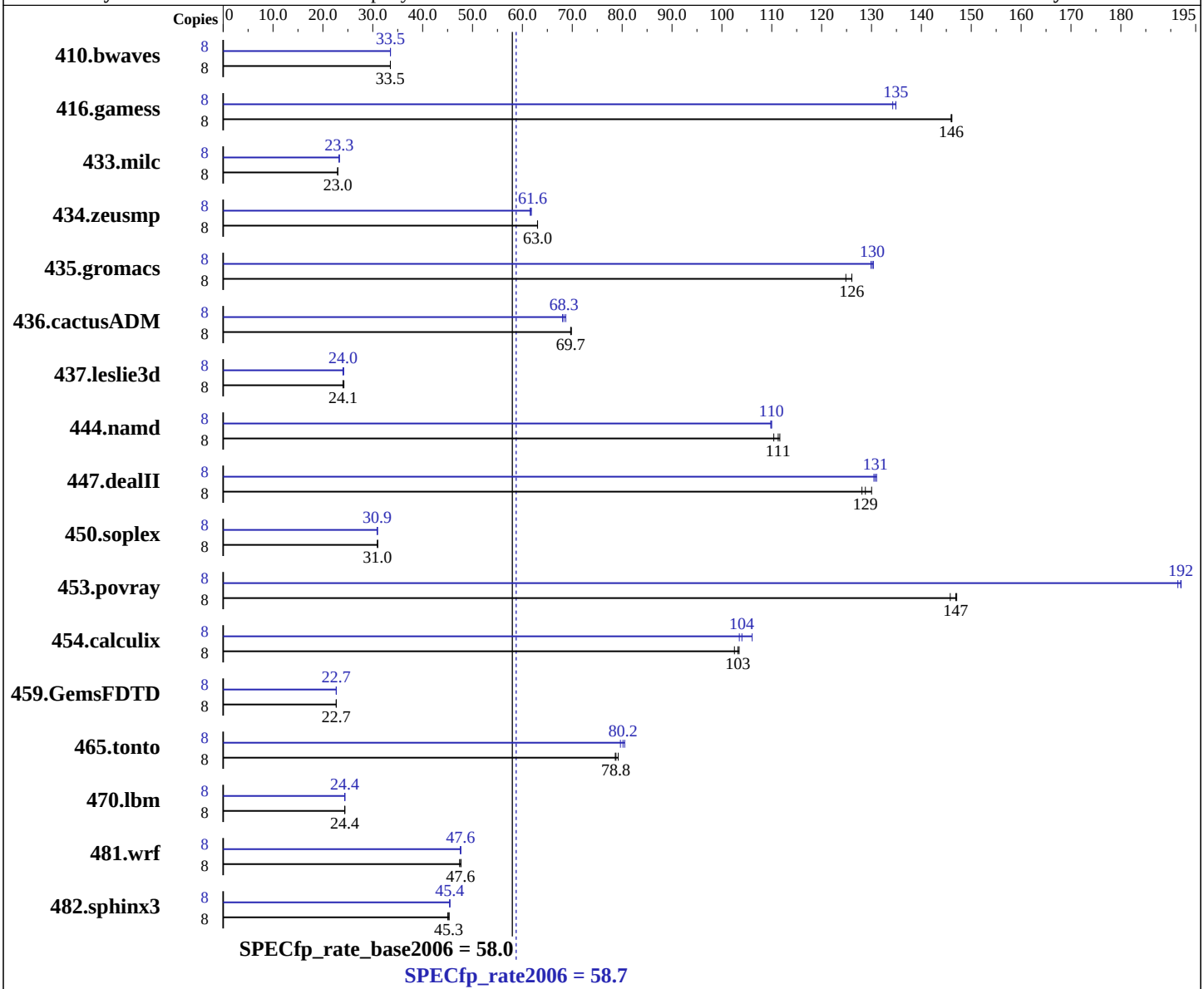
ProLiant BL20p G4
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®_rate2006 = 58.7

SPECfp_rate_base2006 = 58.0

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jan-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-default
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_cc_c_9.1.045 Build no 20061101 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel:	No

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant BL20p G4
(2.66 GHz, Intel Xeon processor X5355)

SPECfp_rate2006 = 58.7

SPECfp_rate_base2006 = 58.0

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jan-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006

L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300 CL5)	Base Pointers:	64-bit
Disk Subsystem:	2x72 GB SAS, 10 K RPM	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3242	33.5	**3243**	**33.5**	3243	33.5	8	**3241**	**33.5**	3242	33.5	3241	33.5
416.gamess	8	1072	146	**1073**	**146**	1073	146	8	1167	134	**1161**	**135**	1161	135
433.milc	8	3194	23.0	**3195**	**23.0**	3198	23.0	8	3157	23.3	**3155**	**23.3**	3154	23.3
434.zeusmp	8	**1155**	**63.0**	1155	63.0	1155	63.0	8	1178	61.8	**1181**	**61.6**	1183	61.6
435.gromacs	8	457	125	453	126	**453**	**126**	8	**439**	**130**	440	130	438	130
436.cactusADM	8	1369	69.8	**1372**	**69.7**	1372	69.7	8	**1399**	**68.3**	1405	68.0	1392	68.7
437.leslie3d	8	3105	24.2	3127	24.1	**3127**	**24.1**	8	3131	24.0	**3128**	**24.0**	3110	24.2
444.namd	8	581	110	**576**	**111**	575	112	8	584	110	**584**	**110**	584	110
447.dealII	8	704	130	715	128	**711**	**129**	8	698	131	**700**	**131**	701	130
450.soplex	8	2154	31.0	2156	31.0	**2155**	**31.0**	8	2156	30.9	**2158**	**30.9**	2160	30.9
453.povray	8	292	146	**290**	**147**	289	147	8	**222**	**192**	222	192	222	191
454.calculix	8	**639**	**103**	638	103	644	103	8	622	106	638	103	**635**	**104**
459.GemsFDTD	8	**3744**	**22.7**	3744	22.7	3742	22.7	8	3744	22.7	3740	22.7	**3744**	**22.7**
465.tonto	8	**999**	**78.8**	994	79.2	1001	78.6	8	**982**	**80.2**	988	79.7	978	80.5
470.lbm	8	4507	24.4	4508	24.4	**4507**	**24.4**	8	4506	24.4	**4506**	**24.4**	4505	24.4
481.wrf	8	**1876**	**47.6**	1874	47.7	1885	47.4	8	1875	47.6	1880	47.5	**1876**	**47.6**
482.sphinx3	8	3463	45.0	**3446**	**45.3**	3441	45.3	8	3430	45.5	3433	45.4	**3431**	**45.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
Environment stack size set to 'unlimited'
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Continued on next page

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:29:22 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 February 2007.