SPEC® CINT2006 Result

Hewlett-Packard Company

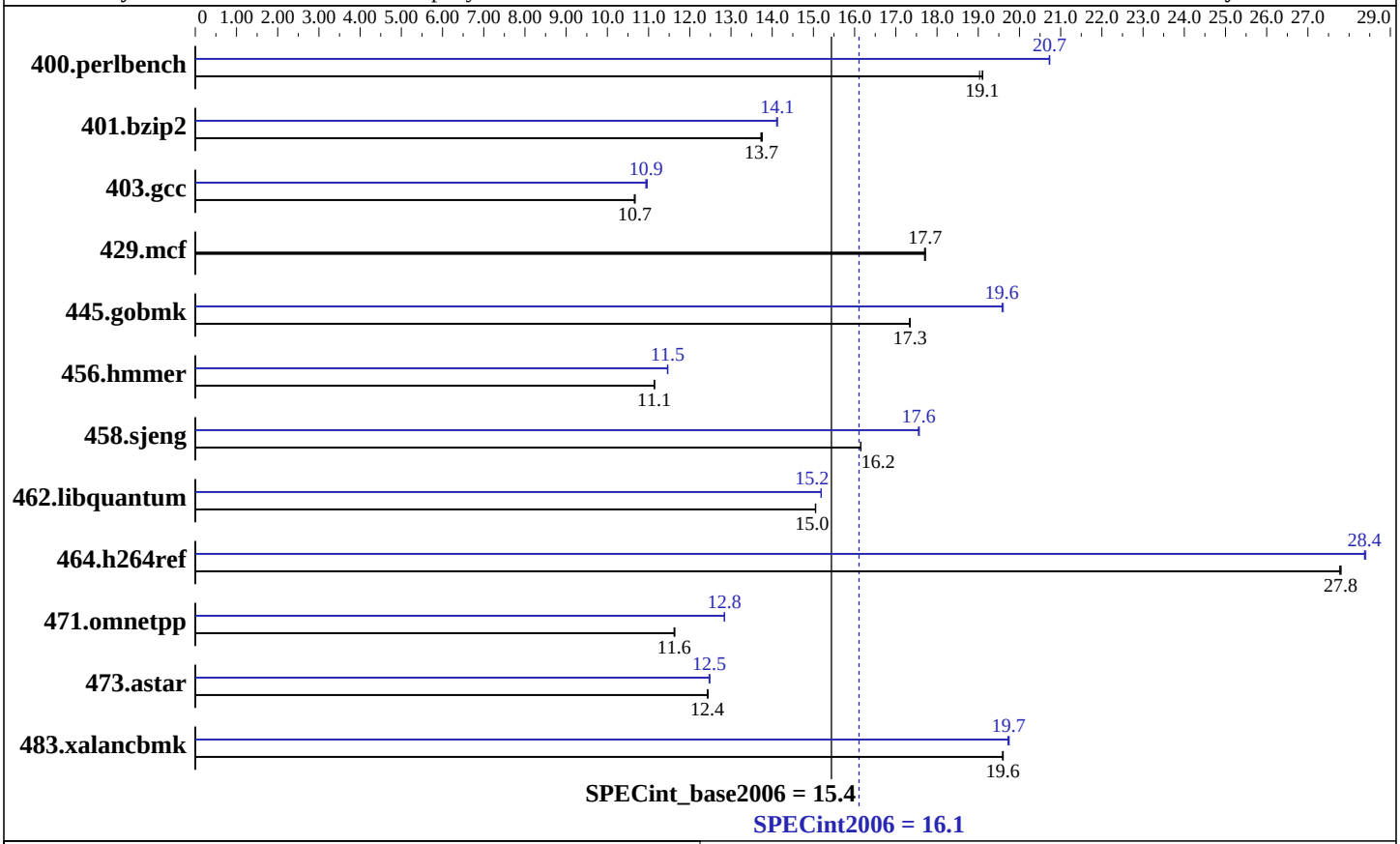
ProLiant BL480c
(2.66 GHz, Intel Xeon processor X5355)

SPECint®2006 = 16.1

SPECint_base2006 = 15.4

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66GHz, 4x2 MB L2 shared, 1333 MHz bus
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem:	1x72 GB 10 K SAS
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise X64 Edition
Compiler:	Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z Package ID: W_CC_P_9.1.020 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	513	19.0	**512**	**19.1**	511	19.1	**471**	**20.7**	471	20.7	472	20.7
401.bzip2	701	13.8	**702**	**13.7**	703	13.7	683	14.1	**683**	**14.1**	684	14.1
403.gcc	754	10.7	**755**	**10.7**	756	10.7	736	10.9	**736**	**10.9**	734	11.0
429.mcf	515	17.7	**515**	**17.7**	515	17.7	515	17.7	**515**	**17.7**	515	17.7
445.gobmk	605	17.3	**605**	**17.3**	605	17.3	536	19.6	**536**	**19.6**	535	19.6
456.hmmer	838	11.1	837	11.1	**838**	**11.1**	814	11.5	814	11.5	**814**	**11.5**
458.sjeng	**749**	**16.2**	749	16.1	749	16.2	689	17.6	**689**	**17.6**	690	17.5
462.libquantum	1376	15.1	1377	15.0	**1377**	**15.0**	1364	15.2	1365	15.2	**1364**	**15.2**
464.h264ref	796	27.8	**796**	**27.8**	797	27.8	**780**	**28.4**	779	28.4	780	28.4
471.omnetpp	538	11.6	537	11.6	**538**	**11.6**	487	12.8	**487**	**12.8**	487	12.8
473.astar	565	12.4	564	12.4	**565**	**12.4**	562	12.5	563	12.5	**563**	**12.5**
483.xalancbmk	352	19.6	352	19.6	**352**	**19.6**	350	19.7	**350**	**19.7**	349	19.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"start /b /wait /affinity" used to bind processes to CPU(s).
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

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Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

401.bzip2: `Same as 400.perlbench`

403.gcc: `Same as 400.perlbench`

429.mcf: `basepeak = yes`

445.gobmk: `Same as 400.perlbench`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 400.perlbench`

Continued on next page

Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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