SPEC® CINT2006 Result

Hewlett-Packard Company

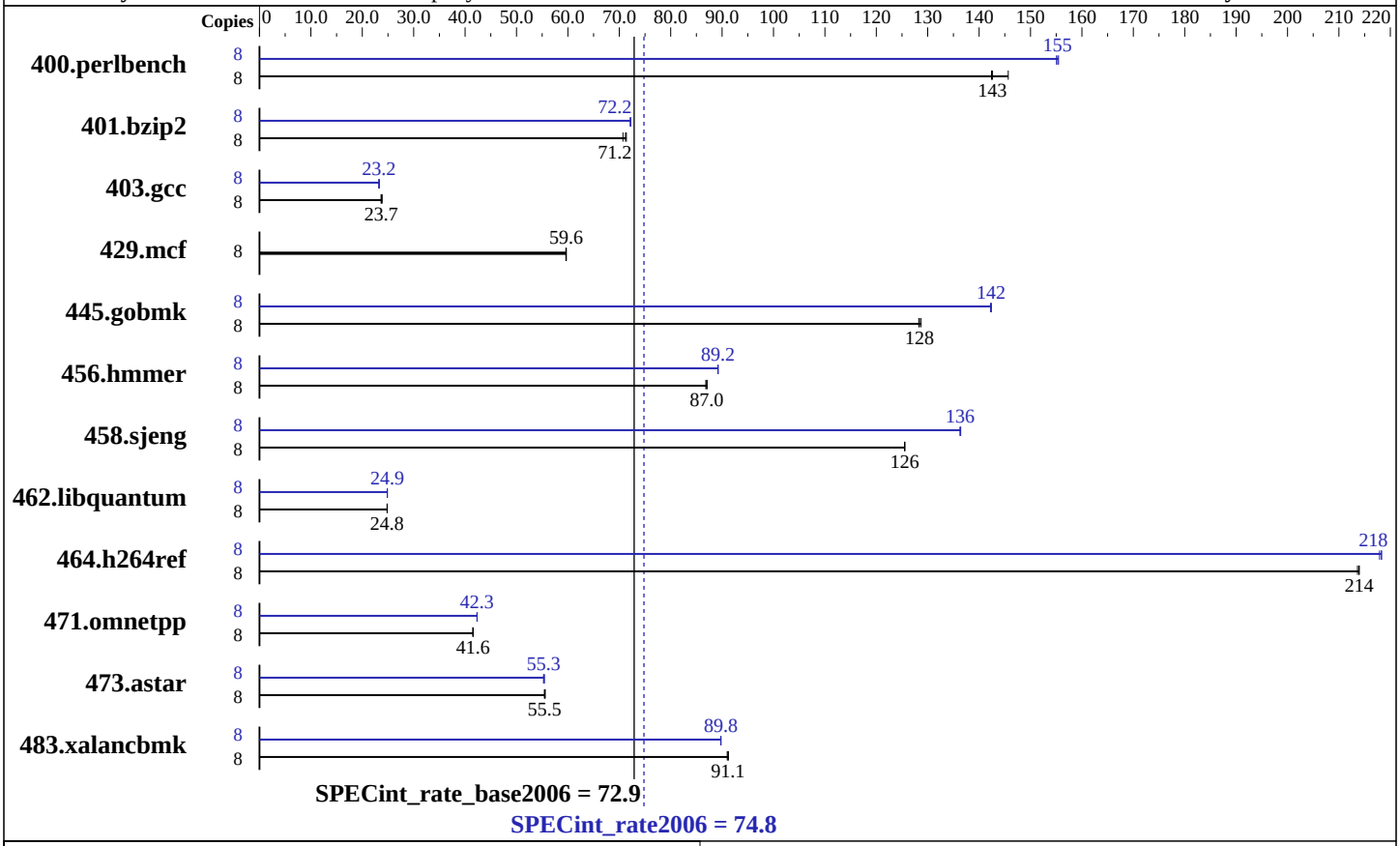
ProLiant ML350 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECint®_rate2006 = 74.8

SPECint_rate_base2006 = 72.9

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB PC2-5300F CL5)
Disk Subsystem:	4x36 GB 10 K SAS
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise X64 Edition
Compiler:	Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z Package ID: W_CC_P_9.1.020 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	537	146	**548**	**143**	549	142	8	504	155	**503**	**155**	503	155
401.bzip2	8	**1084**	**71.2**	1092	70.7	1082	71.4	8	1070	72.2	1069	72.2	**1070**	**72.2**
403.gcc	8	**2717**	**23.7**	2698	23.9	2721	23.7	8	**2772**	**23.2**	2774	23.2	2760	23.3
429.mcf	8	**1223**	**59.6**	1224	59.6	1222	59.7	8	**1223**	**59.6**	1224	59.6	1222	59.7
445.gobmk	8	652	129	**654**	**128**	654	128	8	589	142	**590**	**142**	590	142
456.hmmer	8	860	86.8	**858**	**87.0**	857	87.1	8	**837**	**89.2**	837	89.2	836	89.2
458.sjeng	8	771	125	771	126	**771**	**126**	8	**710**	**136**	710	136	710	136
462.libquantum	8	6676	24.8	6668	24.9	**6674**	**24.8**	8	6660	24.9	**6668**	**24.9**	6670	24.9
464.h264ref	8	829	214	828	214	**828**	**214**	8	**811**	**218**	813	218	811	218
471.omnetpp	8	1203	41.6	**1203**	**41.6**	1204	41.5	8	1181	42.3	**1181**	**42.3**	1182	42.3
473.astar	8	1013	55.4	1010	55.6	**1012**	**55.5**	8	**1015**	**55.3**	1013	55.4	1016	55.3
483.xalancbmk	8	605	91.2	607	91.0	**606**	**91.1**	8	**615**	**89.8**	615	89.8	615	89.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

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Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

401.bzip2: `Same as 400.perlbench`

403.gcc: `Same as 400.perlbench`

429.mcf: `basepeak = yes`

445.gobmk: `Same as 400.perlbench`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 400.perlbench`

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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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