SPEC® CFP2006 Result

Hewlett-Packard Company

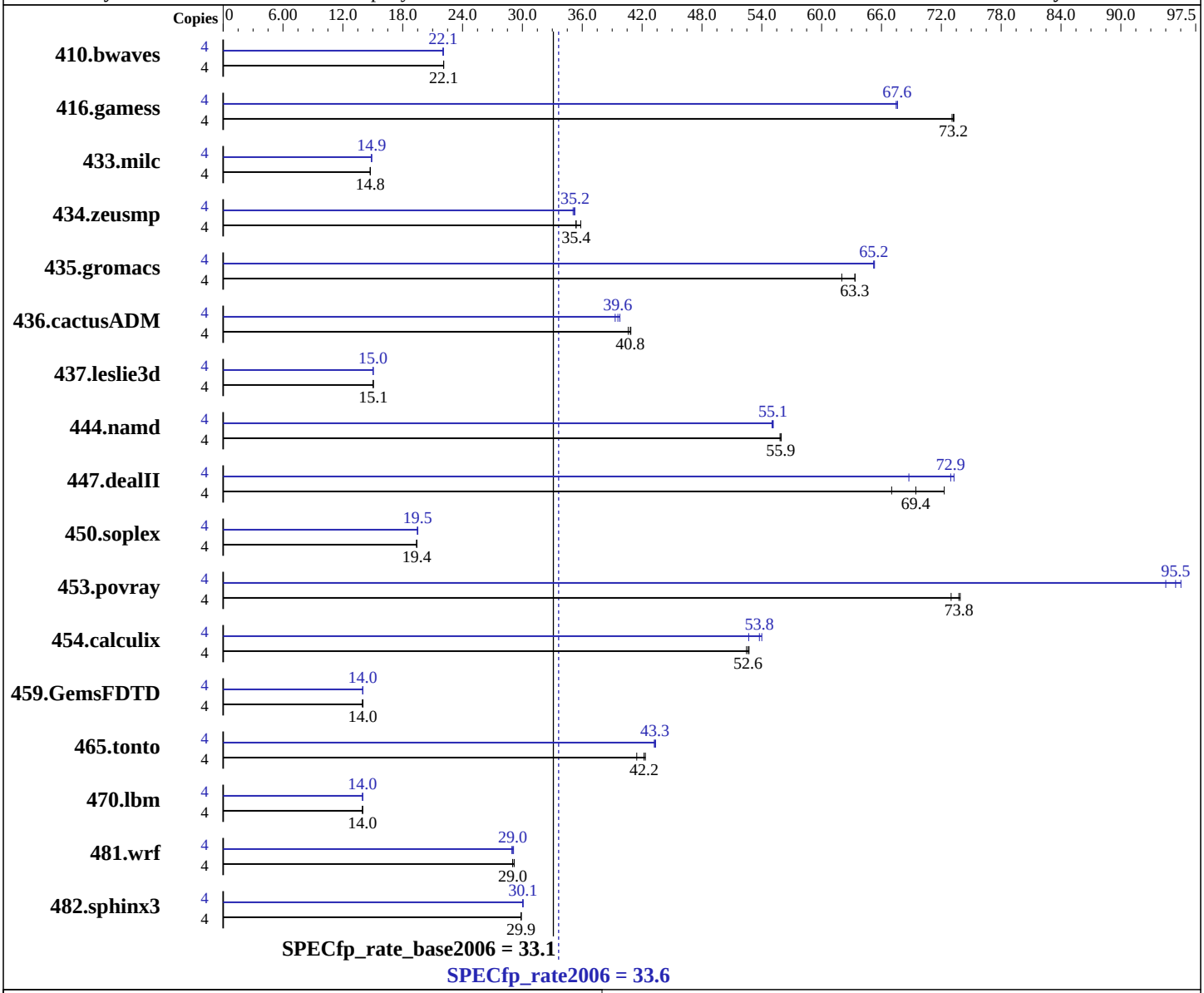
ProLiant ML350 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®_rate2006 = 33.6

SPECfp_rate_base2006 = 33.1

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	SuSE Linux Enterprise Server 10 EM64T kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_cc_c_9.1.045 Build no 20061101 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel:	No

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	8 GB (4x2 GB PC2-5300 CL5)	Base Pointers:	64-bit
Disk Subsystem:	4x36 GB 10 K SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	2460	22.1	2458	22.1	**2459**	**22.1**	4	2467	22.0	**2465**	**22.1**	2461	22.1
416.gamess	4	1069	73.3	1072	73.1	**1070**	**73.2**	4	1161	67.5	**1159**	**67.6**	1158	67.6
433.milc	4	**2489**	**14.8**	2491	14.7	2489	14.8	4	2471	14.9	**2467**	**14.9**	2466	14.9
434.zeusmp	4	**1029**	**35.4**	1015	35.9	1029	35.4	4	**1034**	**35.2**	1038	35.1	1032	35.3
435.gromacs	4	451	63.4	**451**	**63.3**	461	62.0	4	438	65.2	**438**	**65.2**	437	65.3
436.cactusADM	4	1177	40.6	1169	40.9	**1171**	**40.8**	4	1216	39.3	1202	39.8	**1208**	**39.6**
437.leslie3d	4	**2498**	**15.1**	2502	15.0	2497	15.1	4	2496	15.1	**2503**	**15.0**	2504	15.0
444.namd	4	**574**	**55.9**	575	55.8	573	55.9	4	582	55.2	**582**	**55.1**	583	55.0
447.dealII	4	683	67.0	**659**	**69.4**	633	72.3	4	**627**	**72.9**	666	68.7	625	73.3
450.soplex	4	1719	19.4	1719	19.4	**1719**	**19.4**	4	1709	19.5	**1711**	**19.5**	1713	19.5
453.povray	4	292	73.0	288	73.9	**289**	**73.8**	4	**223**	**95.5**	225	94.5	222	96.0
454.calculix	4	626	52.7	**627**	**52.6**	629	52.5	4	**614**	**53.8**	626	52.7	611	54.0
459.GemsFDTD	4	3027	14.0	**3030**	**14.0**	3041	14.0	4	3034	14.0	3029	14.0	**3032**	**14.0**
465.tonto	4	930	42.3	**933**	**42.2**	949	41.5	4	908	43.4	**909**	**43.3**	911	43.2
470.lbm	4	3931	14.0	**3933**	**14.0**	3933	14.0	4	3931	14.0	**3931**	**14.0**	3930	14.0
481.wrf	4	1540	29.0	1530	29.2	**1538**	**29.0**	4	1535	29.1	1544	28.9	**1539**	**29.0**
482.sphinx3	4	**2609**	**29.9**	2611	29.9	2608	29.9	4	**2594**	**30.1**	2592	30.1	2597	30.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
"ulimit -s unlimited" set
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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