



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

**SPECint®2006 = 16.0**

ProLiant BL460c  
(2.66 GHz, Intel Xeon processor X5355)

**SPECint\_base2006 = 15.4**

CPU2006 license: 3

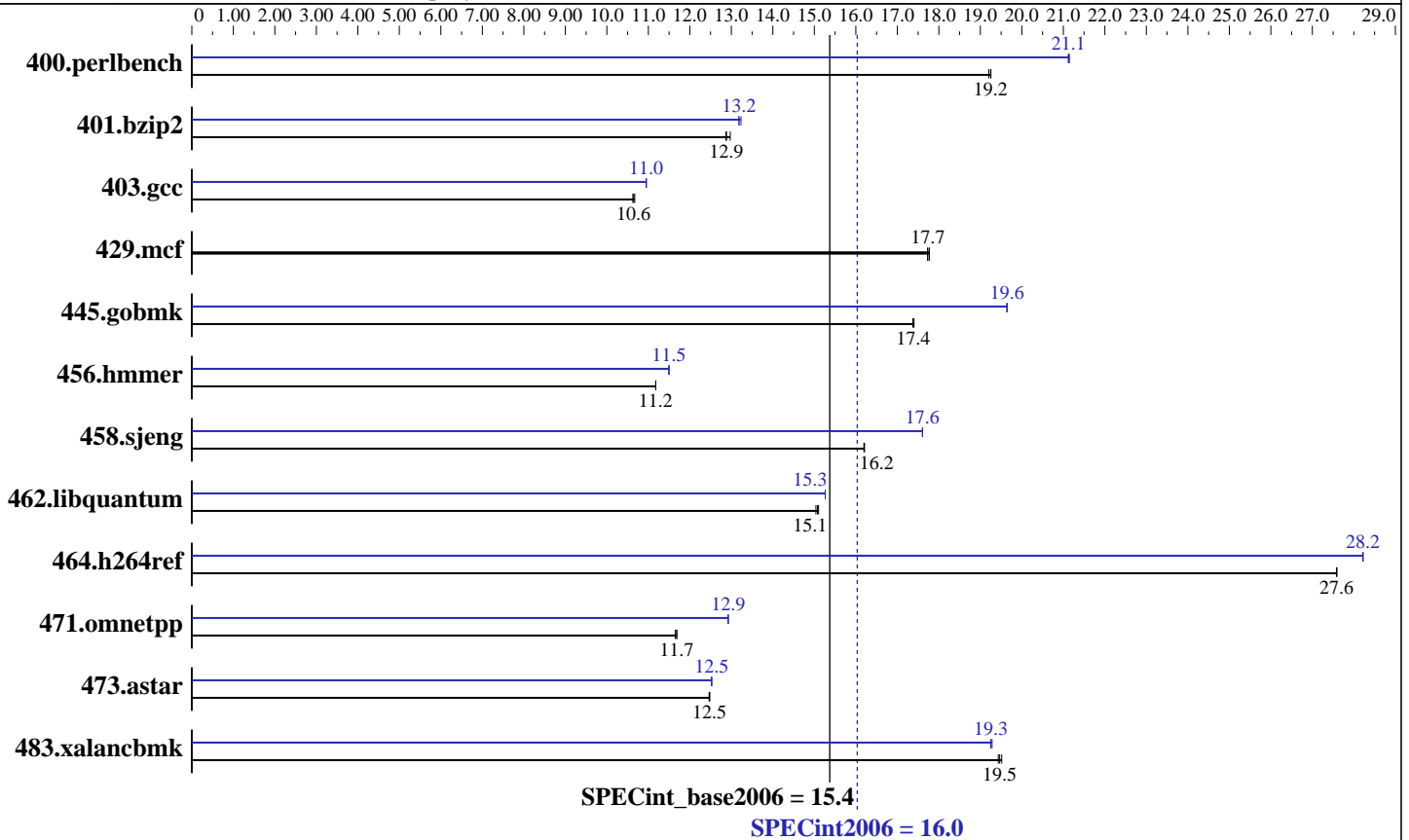
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



### Hardware

CPU Name: Intel Xeon X5355  
 CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus  
 CPU MHz: 2666  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F CL5)  
 Disk Subsystem: 2x72 GB 10 K SAS  
 Other Hardware: None

### Software

Operating System: Windows Server 2003 Enterprise X64 Edition  
 Compiler: Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z  
 Package ID: W\_CC\_P\_9.1.020  
 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: MicroQuill SmartHeap Library 8.0



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

ProLiant BL460c  
(2.66 GHz, Intel Xeon processor X5355)

SPECint2006 = **16.0**

SPECint\_base2006 = **15.4**

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Feb-2007

Hardware Availability: Jan-2007

Software Availability: Nov-2006

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	509	19.2	<b>508</b>	<b>19.2</b>	507	19.3	463	21.1	<b>462</b>	<b>21.1</b>	462	21.1
401.bzip2	<b>749</b>	<b>12.9</b>	750	12.9	744	13.0	<b>732</b>	<b>13.2</b>	729	13.2	732	13.2
403.gcc	758	10.6	<b>756</b>	<b>10.6</b>	754	10.7	735	10.9	735	11.0	<b>735</b>	<b>11.0</b>
429.mcf	<b>514</b>	<b>17.7</b>	514	17.7	513	17.8	<b>514</b>	<b>17.7</b>	514	17.7	513	17.8
445.gobmk	<b>604</b>	<b>17.4</b>	604	17.4	603	17.4	534	19.6	534	19.6	<b>534</b>	<b>19.6</b>
456.hammer	835	11.2	835	11.2	<b>835</b>	<b>11.2</b>	<b>812</b>	<b>11.5</b>	811	11.5	812	11.5
458.sjeng	747	16.2	<b>747</b>	<b>16.2</b>	747	16.2	687	17.6	687	17.6	<b>687</b>	<b>17.6</b>
462.libquantum	1378	15.0	<b>1374</b>	<b>15.1</b>	1372	15.1	1357	15.3	<b>1358</b>	<b>15.3</b>	1358	15.3
464.h264ref	<b>802</b>	<b>27.6</b>	802	27.6	802	27.6	<b>784</b>	<b>28.2</b>	784	28.2	784	28.2
471.omnetpp	537	11.6	<b>535</b>	<b>11.7</b>	535	11.7	<b>484</b>	<b>12.9</b>	484	12.9	484	12.9
473.astar	563	12.5	563	12.5	<b>563</b>	<b>12.5</b>	560	12.5	561	12.5	<b>560</b>	<b>12.5</b>
483.xalancbmk	355	19.4	354	19.5	<b>354</b>	<b>19.5</b>	358	19.2	358	19.3	<b>358</b>	<b>19.3</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.  
Adjacent Sector Prefetch disabled in BIOS.

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99  
C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Hewlett-Packard Company**

**SPECint2006 = 16.0**

ProLiant BL460c  
(2.66 GHz, Intel Xeon processor X5355)

**SPECint\_base2006 = 15.4**

**CPU2006 license:** 3

**Test date:** Feb-2007

**Test sponsor:** Hewlett-Packard Company

**Hardware Availability:** Jan-2007

**Tested by:** Hewlett-Packard Company

**Software Availability:** Nov-2006

## Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

## Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: Same as 400.perlbench
```

```
403.gcc: Same as 400.perlbench
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: Same as 400.perlbench
```

```
456.hmmmer: Same as 400.perlbench
```

```
458.sjeng: Same as 400.perlbench
```

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Hewlett-Packard Company**

**SPECint2006 = 16.0**

ProLiant BL460c  
(2.66 GHz, Intel Xeon processor X5355)

**SPECint\_base2006 = 15.4**

**CPU2006 license:** 3

**Test date:** Feb-2007

**Test sponsor:** Hewlett-Packard Company

**Hardware Availability:** Jan-2007

**Tested by:** Hewlett-Packard Company

**Software Availability:** Nov-2006

## Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

-Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx\_features  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 10:27:59 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 20 February 2007.