



SPEC® CINT2006 Result

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Fujitsu Siemens Computers
CELSIUS V840, AMD Opteron 2218

SPECint®2006 = 11.3

SPECint_base2006 = 10.7

CPU2006 license: 22

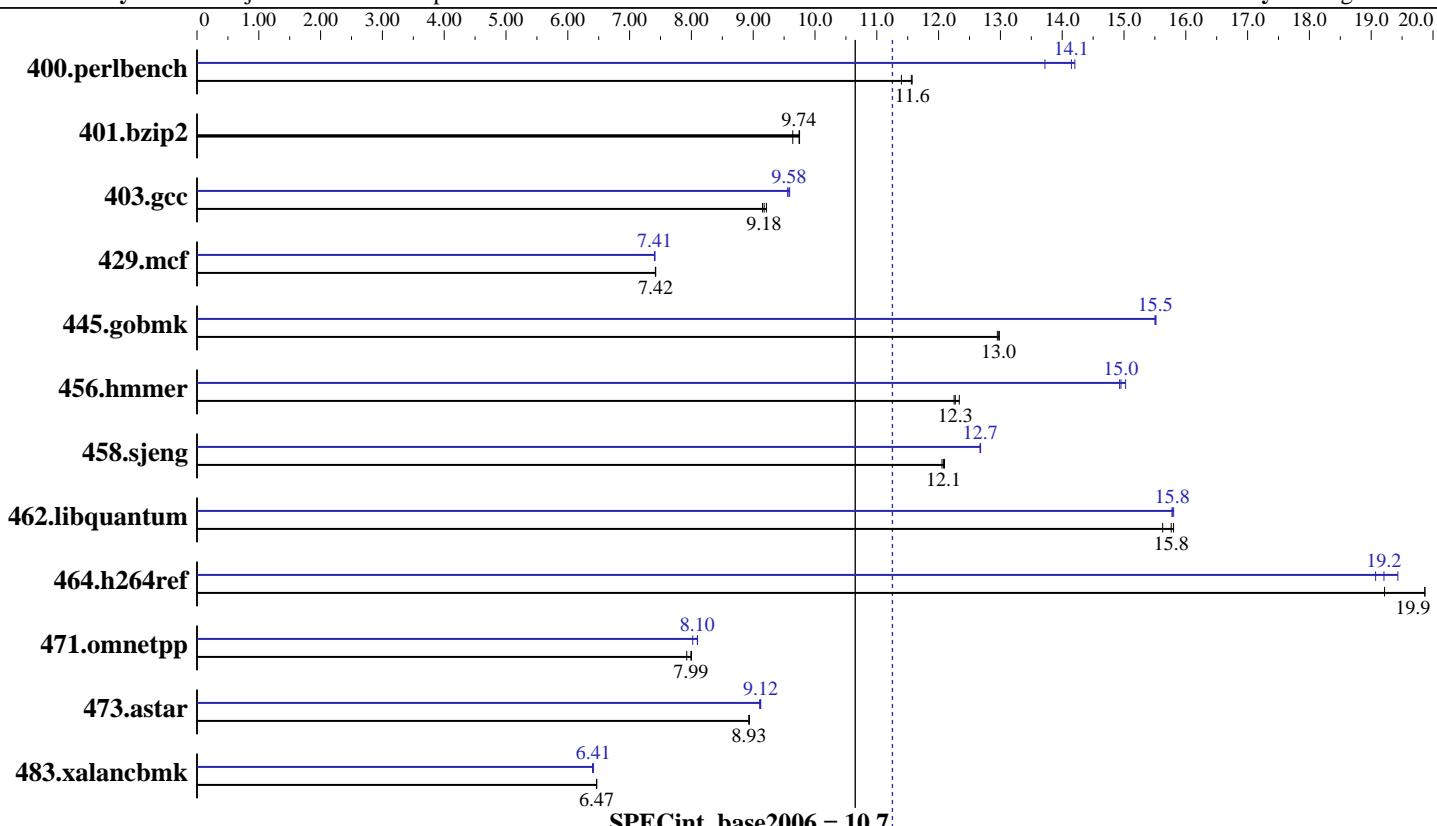
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jan-2007

Hardware Availability: Nov-2006

Software Availability: Aug-2006



Hardware

CPU Name:	AMD Opteron 2218
CPU Characteristics:	Dual Core, 2.6 GHz, 2 MB L2-cache
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2GB DDR2-667 CL5 dual rank ECC DIMMs registered)
Disk Subsystem:	SATA II, 80 GB
Other Hardware:	None

Software

Operating System:	SLES 10 for AMD64/EM64T
Compiler:	QLogic Pathscale Compiler Suite 2.5
Auto Parallel:	Yes
File System:	ext3
System State:	Multi-User SuSE Run Level 3
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other Software:	--



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	845	11.6	844	11.6	857	11.4	712	13.7	688	14.2	690	14.1
401.bzip2	1001	9.64	991	9.74	990	9.75	1001	9.64	991	9.74	990	9.75
403.gcc	877	9.18	880	9.15	874	9.21	839	9.59	841	9.58	842	9.56
429.mcf	1229	7.42	1229	7.42	1230	7.42	1231	7.41	1231	7.41	1232	7.40
445.gobmk	809	13.0	810	13.0	808	13.0	676	15.5	677	15.5	677	15.5
456.hmmer	756	12.3	760	12.3	762	12.3	625	14.9	621	15.0	624	15.0
458.sjeng	1004	12.1	1002	12.1	1000	12.1	955	12.7	954	12.7	955	12.7
462.libquantum	1311	15.8	1314	15.8	1326	15.6	1311	15.8	1313	15.8	1313	15.8
464.h264ref	1114	19.9	1114	19.9	1152	19.2	1139	19.4	1160	19.1	1152	19.2
471.omnetpp	782	7.99	789	7.92	781	8.00	772	8.10	772	8.10	779	8.02
473.astar	785	8.94	786	8.93	786	8.93	770	9.12	771	9.10	770	9.12
483.xalancbmk	1067	6.47	1067	6.47	1067	6.47	1076	6.41	1076	6.41	1078	6.40

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

powersave -f is applied to set CPU to maximum frequency prior to run
stacksize is set to unlimited prior to run

General Notes

Binaries are generated on an Opteron-based system
running Suse 9.3.

BIOS default settings with
Node memory interleave disabled, SRAT enabled

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Base Compiler Invocation

C benchmarks:
pathcc

C++ benchmarks:
pathCC



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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hammer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
-Ofast

C++ benchmarks:
-Ofast

Peak Compiler Invocation

C benchmarks:
pathcc

C++ benchmarks:
pathCC

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
  -IPA:plimit=525 -IPA:pu_reorder=1
```

```
401.bzip2: basepeak = yes
```

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Peak Optimization Flags (Continued)

403.gcc: -Ofast -apo

429.mcf: Same as 403.gcc

445.gobmk: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
-OPT:alias=disjoint -LNO:simd=0 -LNO:minvariant=off

456.hmmr: -O2 -OPT:alias=disjoint -WOPT:aggstr=0 -CG:cflow=0 -apo

458.sjeng: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
-IPA:callee_limit=5000 -IPA:linear=on

462.libquantum: -O3 -ipa -CG:local_fwd_sched=on -IPA:space=1000

464.h264ref: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
-IPA:plimit=20000 -OPT:alias=disjoint -LNO:prefetch=0 -apo

C++ benchmarks:

471.omnetpp: -Ofast -IPA:pu_reorder=2 -CG:gcm=off

473.astar: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast

483.xalancbmk: -Ofast -apo

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.14.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.14.xml

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