SPEC® CINT2006 Result
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Bull SAS

NovaScale B260 (Intel Xeon processor E5320,1.86GHz)

SPECint®_rate2006 =   55.4
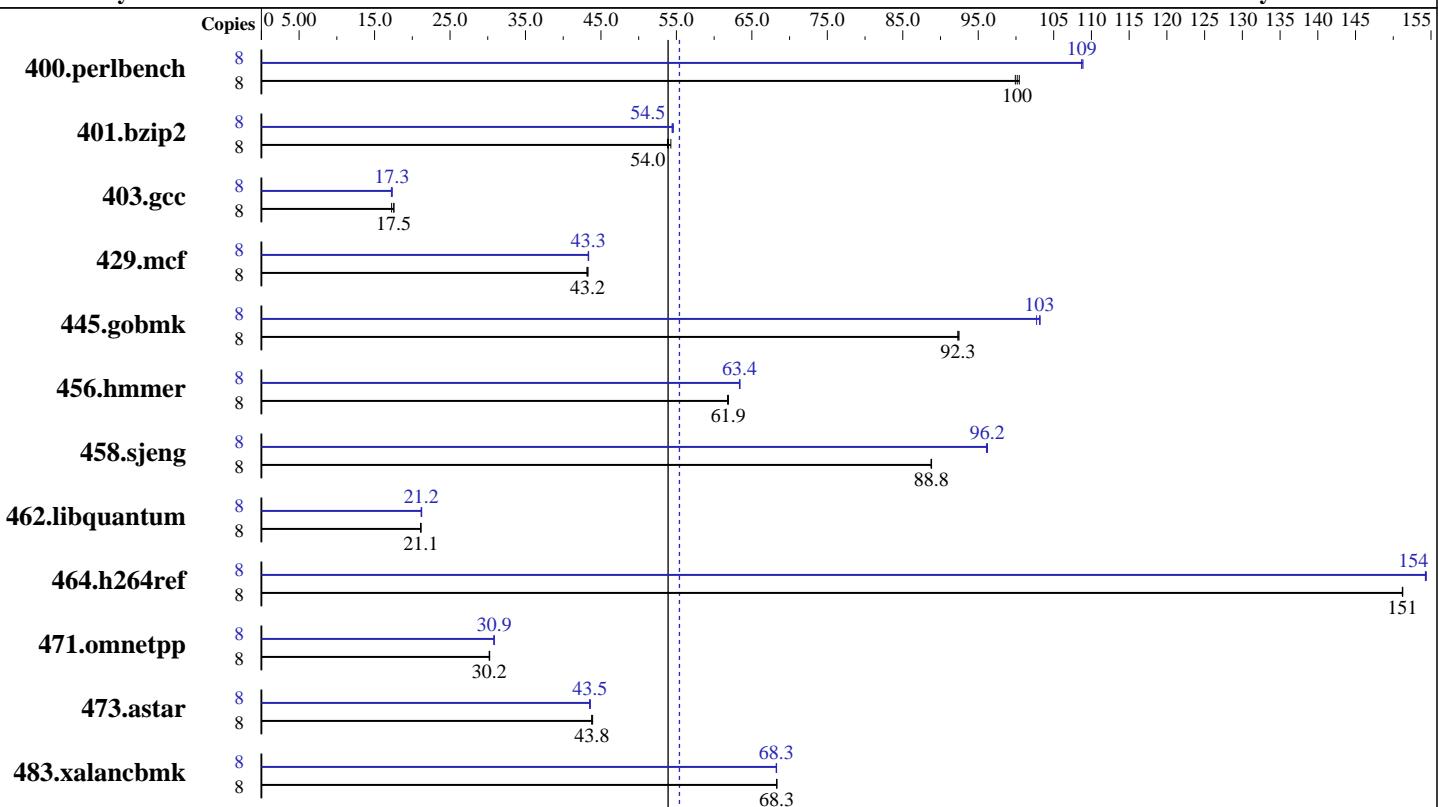
SPECint_rate_base2006 = 53.9

CPU2006 license: 20

Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Dec-2006

Hardware Availability: Jan-2007

Software Availability: Dec-2006

Copies 0 5.00 15.0 25.0 35.0 45.0 55.0 65.0 75.0 85.0 95.0 105 110 115 120 125 130 135 140 145 155

Benchmark	Copies	Value
400.perlbench	8	109
	8	100
401.bzip2	8	54.5
	8	54.0
403.gcc	8	17.3
	8	17.5
429.mcf	8	43.3
	8	43.2
445.gobmk	8	103
	8	92.3
456.hmmer	8	63.4
	8	61.9
458.sjeng	8	96.2
	8	88.8
462.libquantum	8	21.2
	8	21.1
464.h264ref	8	154
	8	151
471.omnetpp	8	30.9
	8	30.2
473.astar	8	43.5
	8	43.8
483.xalancbmk	8	68.3
	8	68.3

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Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1.86 GHz, 8MB L2, 1066MHz bus
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1 to 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (2GB DIMMx4, FB-DIMM PC2-5300F ECC CL5)
Disk Subsystem:	73 GB SAS, 10000RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition (32 bits) Service Pack1
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.033 Build no 20061103Z Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

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Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	8	782	99.9	**780**	**100**	778	100	8	718	109	**719**	**109**	719	109
401.bzip2	8	**1431**	**54.0**	1423	54.3	1434	53.8	8	**1417**	**54.5**	1418	54.4	1414	54.6
403.gcc	8	3731	17.3	3667	17.6	**3670**	**17.5**	8	3715	17.3	**3725**	**17.3**	3738	17.2
429.mcf	8	1691	43.1	**1687**	**43.2**	1686	43.3	8	**1683**	**43.3**	1682	43.4	1684	43.3
445.gobmk	8	909	92.3	**909**	**92.3**	908	92.5	8	813	103	**814**	**103**	817	103
456.hmmer	8	1208	61.8	1206	61.9	**1206**	**61.9**	8	**1178**	**63.4**	1178	63.4	1177	63.4
458.sjeng	8	1090	88.8	**1090**	**88.8**	1091	88.7	8	1006	96.2	**1006**	**96.2**	1007	96.1
462.libquantum	8	**7840**	**21.1**	7858	21.1	7838	21.1	8	7801	21.2	**7819**	**21.2**	7825	21.2
464.h264ref	8	**1171**	**151**	1171	151	1170	151	8	**1147**	**154**	1148	154	1147	154
471.omnetpp	8	1656	30.2	1652	30.3	**1655**	**30.2**	8	1623	30.8	1621	30.9	**1621**	**30.9**
473.astar	8	1279	43.9	**1282**	**43.8**	1283	43.8	8	1288	43.6	1291	43.5	**1290**	**43.5**
483.xalancbmk	8	808	68.3	808	68.3	**808**	**68.3**	8	809	68.2	808	68.3	**809**	**68.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

```
    403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
          -link /FORCE:MULTIPLE
```

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Test sponsor:	Bull SAS	**Hardware Availability:**	Jan-2007
Tested by:	Bull SAS	**Software Availability:**	Dec-2006

Base Other Flags

C benchmarks:

      403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
   `icl -Qvc7.1 -Qc99`

C++ benchmarks:
   `icl -Qvc7.1`

Peak Portability Flags

     403.gcc: `-DSPEC_CPU_WIN32`
    464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:
   `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib`
         `-link /FORCE:MULTIPLE`

C++ benchmarks:
   `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features`
   `/F512000000 shlw32m.lib`   `-link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

      403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/flags.20090715.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/flags.20090715.xml`

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For other inquiries, please contact webmaster@spec.org.

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