SPEC® CINT2006 Result
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Bull SAS

NovaScale B280 (Intel Xeon processor 5150,2.66GHz)

SPECint®2006 =   15.8

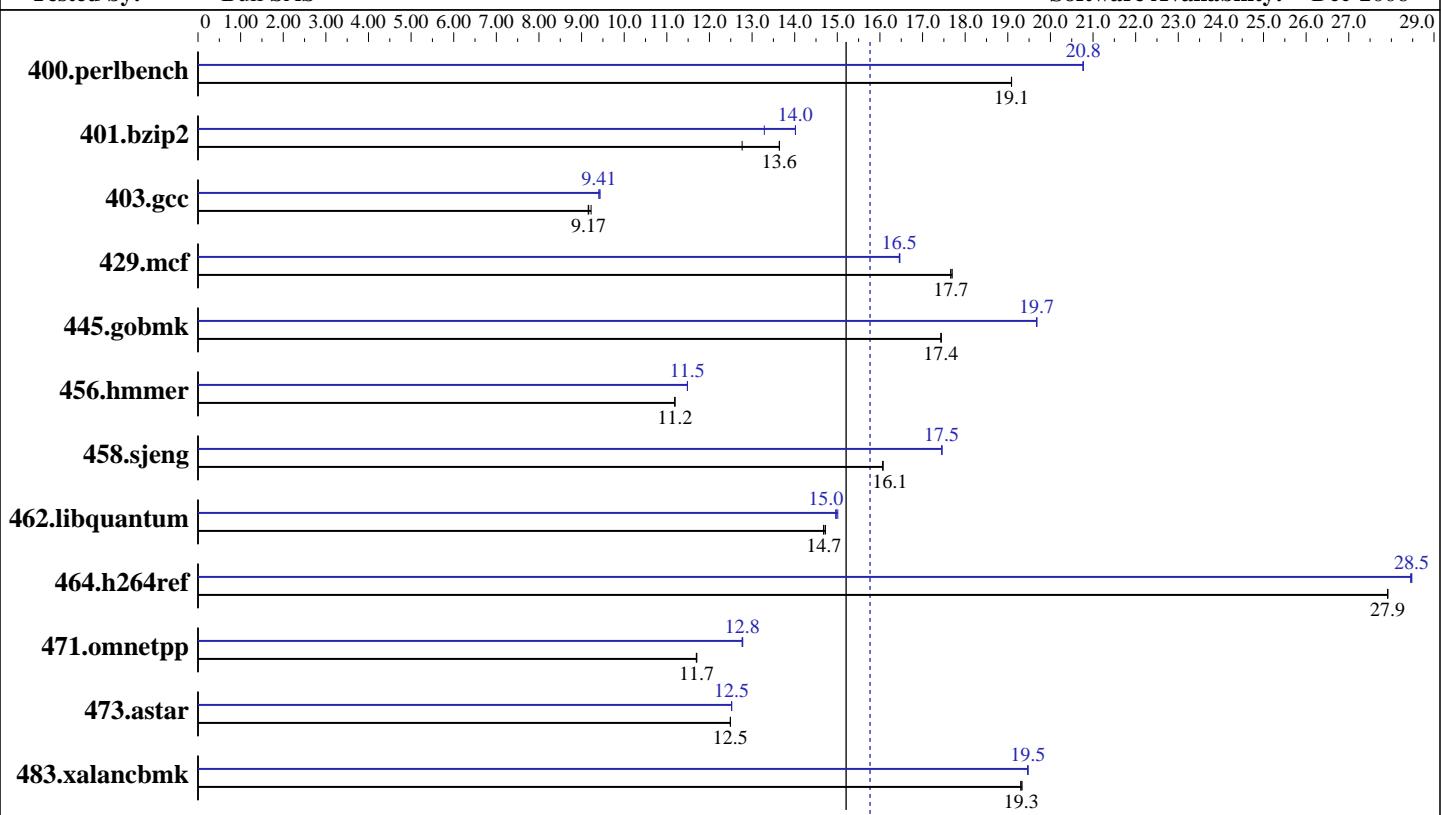SPECint_base2006 =   15.2

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Dec-2006
Hardware Availability: Jan-2007
Software Availability: Dec-2006

400.perlbench	20.8 / 19.1
401.bzip2	14.0 / 13.6
403.gcc	9.41 / 9.17
429.mcf	16.5 / 17.7
445.gobmk	19.7 / 17.4
456.hmmer	11.5 / 11.2
458.sjeng	17.5 / 16.1
462.libquantum	15.0 / 14.7
464.h264ref	28.5 / 27.9
471.omnetpp	12.8 / 11.7
473.astar	12.5 / 12.5
483.xalancbmk	19.5 / 19.3

SPECint_base2006 = 15.2
SPECint2006 = 15.8

Hardware

CPU Name: Intel Xeon 5150
CPU Characteristics: 2.66 GHz, 4MB L2, 1333MHz bus
CPU MHz: 2660
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 2 cores/chip
CPU(s) orderable: 1 to 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 8 GB (1GB DIMMx8, FB-DIMM PC2-5300F ECC CL5)
Disk Subsystem: 73 GB SAS, 10000RPM
Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition (32 bits) Service Pack1
Compiler: Intel C++ Compiler for IA32 version 9.1
Package ID W_CC_C_9.1.033 Build no 20061103Z
Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**512**	**19.1**	512	19.1	512	19.1	**470**	**20.8**	470	20.8	471	20.8
401.bzip2	**708**	**13.6**	756	12.8	707	13.6	688	14.0	726	13.3	**688**	**14.0**
403.gcc	873	9.22	879	9.16	**878**	**9.17**	853	9.43	**855**	**9.41**	856	9.41
429.mcf	515	17.7	516	17.7	**516**	**17.7**	554	16.5	**554**	**16.5**	554	16.5
445.gobmk	**602**	**17.4**	602	17.4	602	17.4	533	19.7	533	19.7	**533**	**19.7**
456.hmmer	**834**	**11.2**	834	11.2	834	11.2	812	11.5	812	11.5	**812**	**11.5**
458.sjeng	753	16.1	753	16.1	**753**	**16.1**	693	17.5	693	17.5	**693**	**17.5**
462.libquantum	**1411**	**14.7**	1412	14.7	1407	14.7	**1383**	**15.0**	1380	15.0	1385	15.0
464.h264ref	**793**	**27.9**	793	27.9	793	27.9	**777**	**28.5**	778	28.5	777	28.5
471.omnetpp	534	11.7	535	11.7	**534**	**11.7**	**489**	**12.8**	489	12.8	490	12.8
473.astar	562	12.5	562	12.5	**562**	**12.5**	561	12.5	**561**	**12.5**	561	12.5
483.xalancbmk	357	19.3	**357**	**19.3**	357	19.3	354	19.5	**354**	**19.5**	355	19.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

```
    403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
        -link /FORCE:MULTIPLE
```

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Base Other Flags

C benchmarks:

       403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
     icl -Qvc7.1 -Qc99

C++ benchmarks:
     icl -Qvc7.1

Peak Portability Flags

     403.gcc: -DSPEC_CPU_WIN32
    464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
     -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib
         -link /FORCE:MULTIPLE

C++ benchmarks:
     -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
     /F512000000 shlw32m.lib   -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

       403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/flags.20090715.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/flags.20090715.xml

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