



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint®_rate2006 = Not Run

IBM System x3550 (Intel Xeon E5335)

SPECint_rate_base2006 = 67.5

CPU2006 license: 11

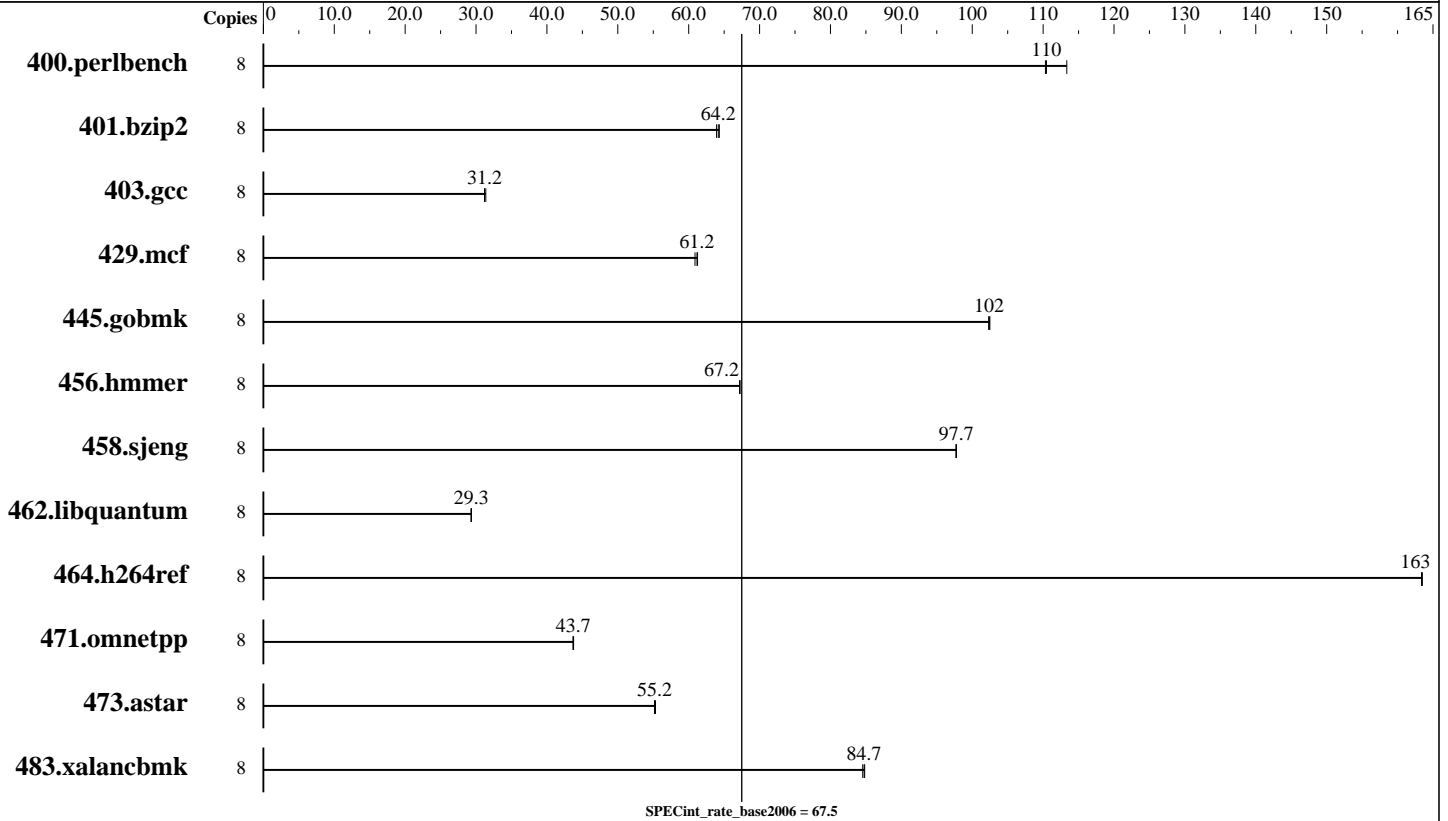
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2007

Hardware Availability: Feb-2007

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon E5335
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8 x 1GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 73 GB SAS, 1000 RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = Not Run

IBM System x3550 (Intel Xeon E5335)

SPECint_rate_base2006 = 67.5

CPU2006 license: 11

Test date: Jan-2007

Test sponsor: IBM Corporation

Hardware Availability: Feb-2007

Tested by: IBM Corporation

Software Availability: Aug-2006

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	690	113	708	110	708	110							
401.bzip2	8	1203	64.2	1200	64.3	1208	63.9							
403.gcc	8	2064	31.2	2064	31.2	2053	31.4							
429.mcf	8	1191	61.3	1193	61.2	1198	60.9							
445.gobmk	8	820	102	820	102	819	102							
456.hammer	8	1110	67.2	1111	67.2	1111	67.2							
458.sjeng	8	990	97.8	991	97.7	990	97.7							
462.libquantum	8	5653	29.3	5653	29.3	5652	29.3							
464.h264ref	8	1083	163	1083	163	1083	163							
471.omnetpp	8	1143	43.8	1145	43.7	1144	43.7							
473.astar	8	1018	55.2	1015	55.3	1017	55.2							
483.xalancbmk	8	651	84.7	651	84.8	653	84.5							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = Not Run

IBM System x3550 (Intel Xeon E5335)

SPECint_rate_base2006 = 67.5

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2007

Hardware Availability: Feb-2007

Software Availability: Aug-2006

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:13:34 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 February 2007.