



# SPEC® CINT2006 Result

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## IBM Corporation

## SPECint®\_rate2006 = Not Run

## IBM System x3950 (Intel Xeon 7140N)

## SPECint\_rate\_base2006 = 64.9

CPU2006 license: 11

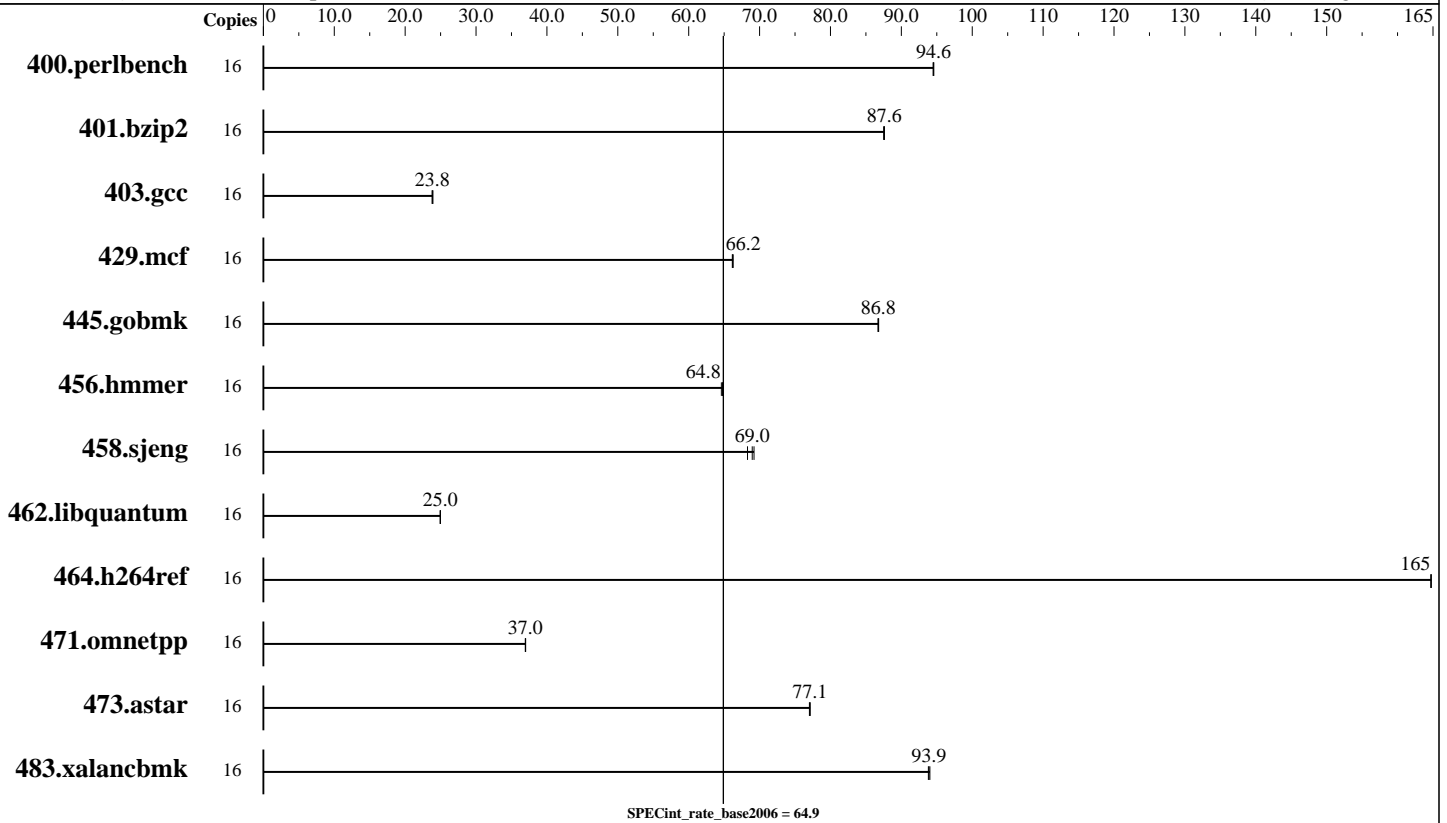
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2007

Hardware Availability: Oct-2006

Software Availability: Aug-2006



### Hardware

CPU Name: Intel Xeon 7140N  
 CPU Characteristics: 667 MHz bus  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,4 chips  
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 16 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (16 x 2048 MB ECC PC2-3200)  
 Disk Subsystem: 73 GB SAS, 10k RPM  
 Other Hardware: None

### Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Build no 20060816  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Smart Heap Library, Version 8



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	16	1655	94.5	1653	94.6	<b>1653</b>	<b>94.6</b>									
401.bzip2	16	<b>1763</b>	<b>87.6</b>	1762	87.6	1764	87.5									
403.gcc	16	5408	23.8	5385	23.9	<b>5405</b>	<b>23.8</b>									
429.mcf	16	2206	66.2	<b>2204</b>	<b>66.2</b>	2202	66.3									
445.gobmk	16	<b>1935</b>	<b>86.8</b>	1936	86.7	1934	86.8									
456.hammer	16	2311	64.6	2303	64.8	<b>2305</b>	<b>64.8</b>									
458.sjeng	16	2797	69.2	2835	68.3	<b>2807</b>	<b>69.0</b>									
462.libquantum	16	<b>13280</b>	<b>25.0</b>	13286	25.0	13277	25.0									
464.h264ref	16	2150	165	<b>2150</b>	<b>165</b>	2149	165									
471.omnetpp	16	2706	37.0	<b>2705</b>	<b>37.0</b>	2703	37.0									
473.astar	16	1458	77.0	<b>1457</b>	<b>77.1</b>	1456	77.1									
483.xalancbmk	16	<b>1176</b>	<b>93.9</b>	1174	94.0	1177	93.8									

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Bios Settings

Hardware Prefetch enabled

Memory Array set to High-Performance Memory Array

## Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32

464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib

-link /FORCE:MULTIPLE

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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