



SPEC[®] CFP2006 Result

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Fujitsu Siemens Computers

PRIMERGY RX300 S3, Intel Xeon processor 5160, 3.0 GHz

SPECfp[®]_rate2006 = 45.1

SPECfp_rate_base2006 = 44.1

CPU2006 license: 22

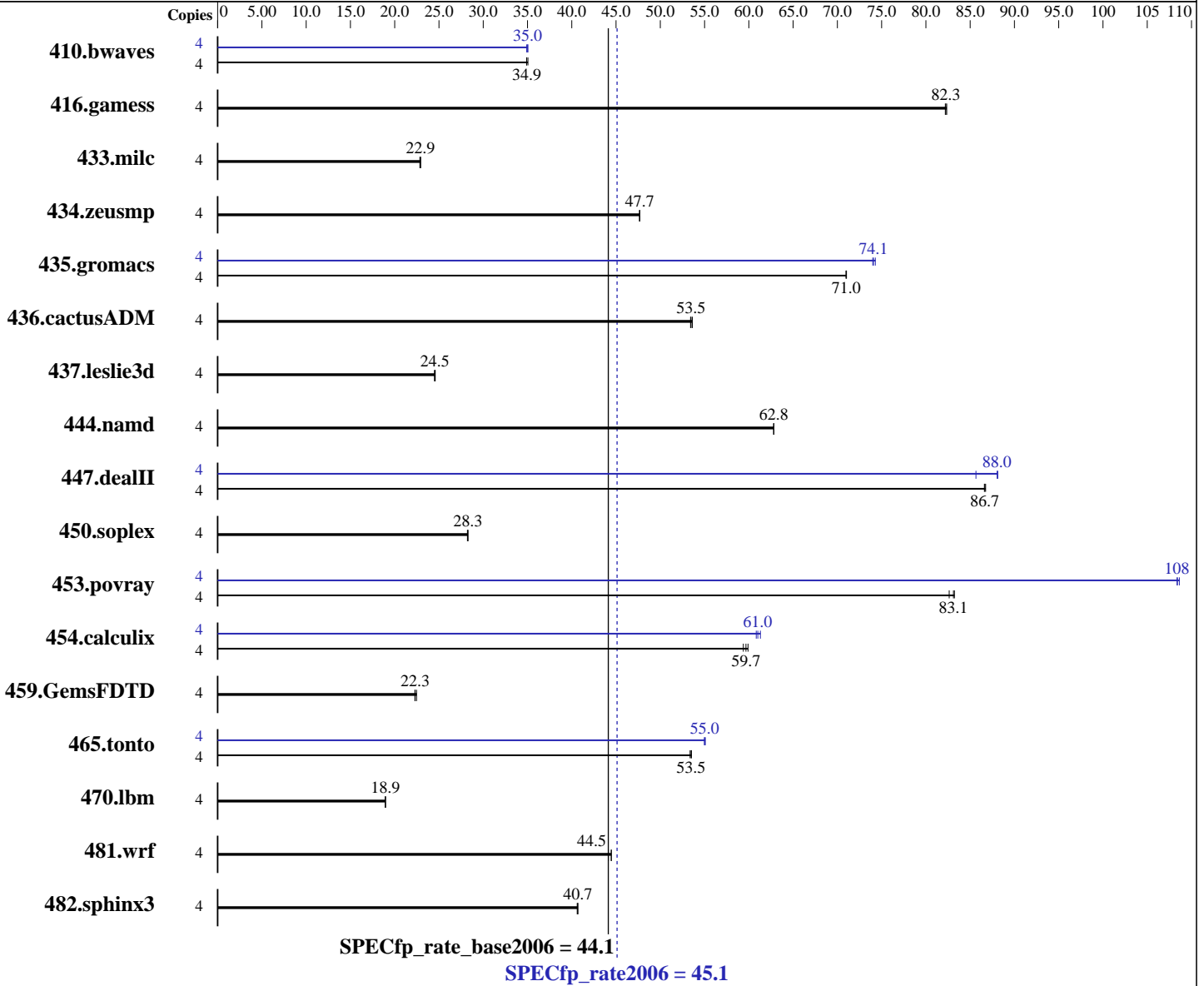
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jan-2007

Hardware Availability: Jul-2006

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon 5160
 CPU Characteristics: 5160
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip

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Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
 Compiler: Intel C++ Compiler for Linux64 EM64T version 9.1 Build 20061101, Package-ID l_cc_p_9.1.045
 Intel Fortran Compiler for Linux64 EM64T version 9.1 Build 20061101 Package ID: l_fc_p_9.1.040
 Auto Parallel: No
 File System: NTFS
 System State: Default

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: SAS (73GB 15400 rpm)
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1551	35.0	1558	34.9	1557	34.9	4	1551	35.0	1552	35.0	1557	34.9
416.gamess	4	953	82.2	951	82.4	952	82.3	4	953	82.2	951	82.4	952	82.3
433.milc	4	1603	22.9	1602	22.9	1608	22.8	4	1603	22.9	1602	22.9	1608	22.8
434.zeusmp	4	763	47.7	764	47.7	764	47.7	4	763	47.7	764	47.7	764	47.7
435.gromacs	4	402	71.0	402	71.0	402	71.0	4	384	74.3	386	74.0	386	74.1
436.cactusADM	4	891	53.6	895	53.4	894	53.5	4	891	53.6	895	53.4	894	53.5
437.leslie3d	4	1530	24.6	1535	24.5	1532	24.5	4	1530	24.6	1535	24.5	1532	24.5
444.namd	4	511	62.8	511	62.8	511	62.8	4	511	62.8	511	62.8	511	62.8
447.dealII	4	528	86.7	528	86.7	528	86.6	4	520	88.0	534	85.7	519	88.1
450.soplex	4	1182	28.2	1180	28.3	1180	28.3	4	1182	28.2	1180	28.3	1180	28.3
453.povray	4	256	83.2	258	82.6	256	83.1	4	196	108	196	108	196	109
454.calculix	4	553	59.7	551	59.9	556	59.4	4	541	61.0	542	60.8	538	61.3
459.GemsFDTD	4	1903	22.3	1889	22.5	1905	22.3	4	1903	22.3	1889	22.5	1905	22.3
465.tonto	4	735	53.5	736	53.5	738	53.4	4	716	55.0	715	55.1	715	55.0
470.lbm	4	2900	19.0	2900	18.9	2901	18.9	4	2900	19.0	2900	18.9	2901	18.9
481.wrf	4	1005	44.5	1005	44.5	1012	44.1	4	1005	44.5	1005	44.5	1012	44.1
482.sphinx3	4	1916	40.7	1920	40.6	1917	40.7	4	1916	40.7	1920	40.6	1917	40.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

The PRIMERGY RX300 S3 and the PRIMERGY TX300 S3 are electronically equivalent.

The system bus runs at 1333 MHz

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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast



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Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -prof_gen(pass 1) -prof_use(pass 2) -fast

450.soplex: basepeak = yes

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: -prof_gen(pass 1) -prof_use(pass 2) -fast

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

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Peak Optimization Flags (Continued)

459.GemsFDTD: basepeak = yes

465.tonto: Same as 410.bwaves

Benchmarks using both Fortran and C:

435.gromacs: -prof_gen(pass 1) -prof_use(pass 2) -fast

436.cactusADM: basepeak = yes

454.calculix: Same as 435.gromacs

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.10.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.10.xml

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