



SPEC® CINT2006 Result

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Bull SAS

SPECint®_rate2006 = 33.8

NovaScale B280 (Intel Xeon processor 5110,1.60GHz)

SPECint_rate_base2006 = 32.6

CPU2006 license: 20

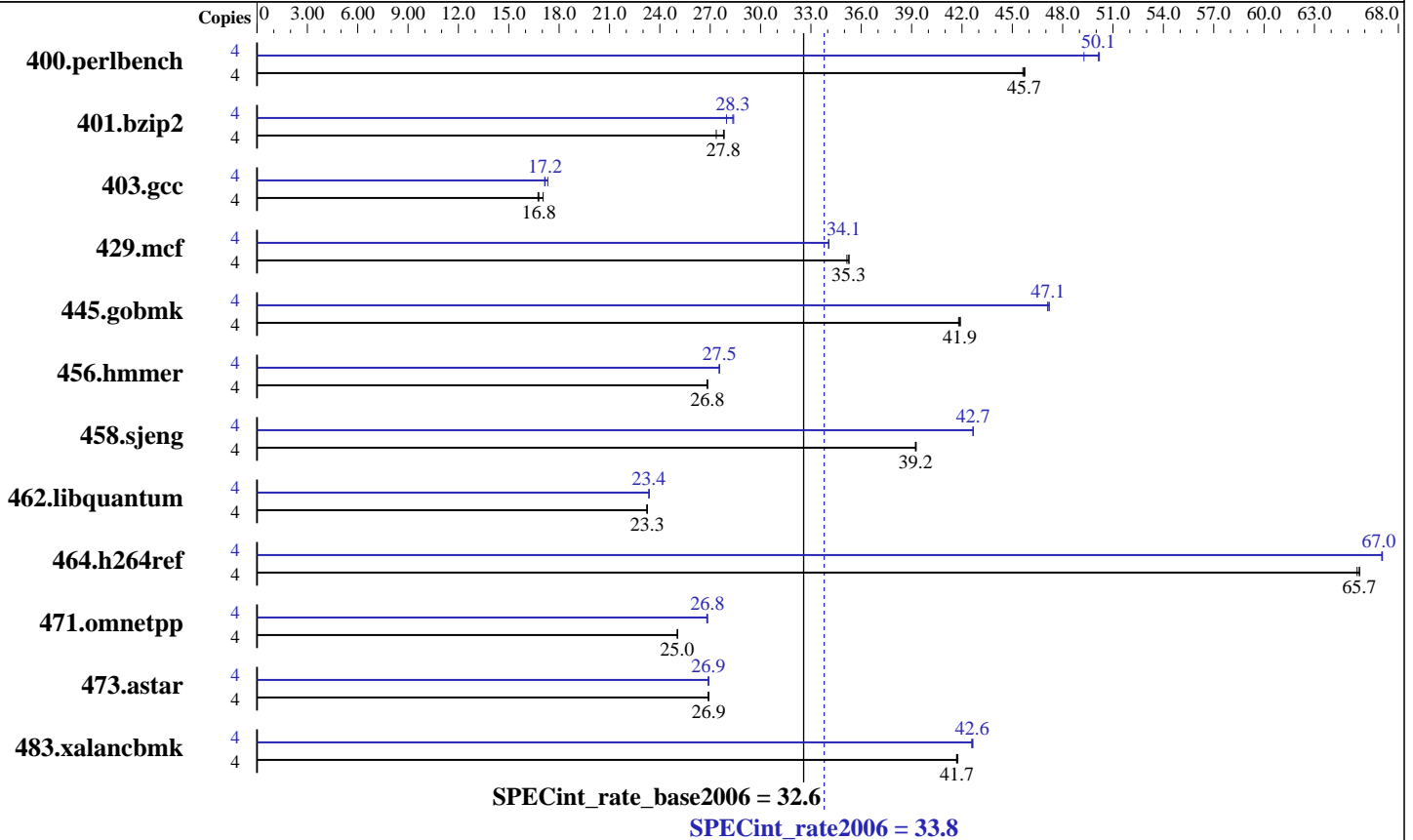
Test date: Dec-2006

Test sponsor: Bull SAS

Hardware Availability: Jan-2007

Tested by: Bull SAS

Software Availability: Dec-2006



Hardware

CPU Name: Intel Xeon 5110
 CPU Characteristics: 1.60 GHz, 4MB L2, 1066MHz bus
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1 to 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (1GB DIMMx8, FB-DIMM PC2-5300F ECC CL5)
 Disk Subsystem: 73 GB SAS, 10000RPM
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition (32 bits) Service Pack1
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.033 Build no 20061103Z
 Microsoft Visual Studio .NET 2003 (lib & linker)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0 (shIW32M.lib)



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	855	45.7	854	45.7	856	45.6	4	793	49.3	780	50.1	779	50.2
401.bzip2	4	1411	27.4	1387	27.8	1389	27.8	4	1380	28.0	1359	28.4	1362	28.3
403.gcc	4	1918	16.8	1923	16.7	1889	17.0	4	1858	17.3	1879	17.1	1876	17.2
429.mcf	4	1034	35.3	1038	35.1	1035	35.3	4	1071	34.1	1071	34.1	1071	34.1
445.gobmk	4	1001	41.9	1002	41.9	1004	41.8	4	889	47.2	891	47.1	890	47.1
456.hammer	4	1391	26.8	1391	26.8	1391	26.8	4	1355	27.5	1355	27.6	1355	27.5
458.sjeng	4	1232	39.3	1233	39.2	1234	39.2	4	1134	42.7	1135	42.7	1134	42.7
462.libquantum	4	3570	23.2	3563	23.3	3563	23.3	4	3549	23.4	3548	23.4	3548	23.4
464.h264ref	4	1351	65.5	1348	65.7	1348	65.7	4	1321	67.0	1320	67.1	1321	67.0
471.omnetpp	4	998	25.1	998	25.0	998	25.0	4	932	26.8	932	26.8	933	26.8
473.astar	4	1045	26.9	1043	26.9	1043	26.9	4	1044	26.9	1044	26.9	1044	26.9
483.xalancbmk	4	662	41.7	661	41.8	662	41.7	4	648	42.6	647	42.7	648	42.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags.20090714.00.xml>



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