



# SPEC® CINT2006 Result

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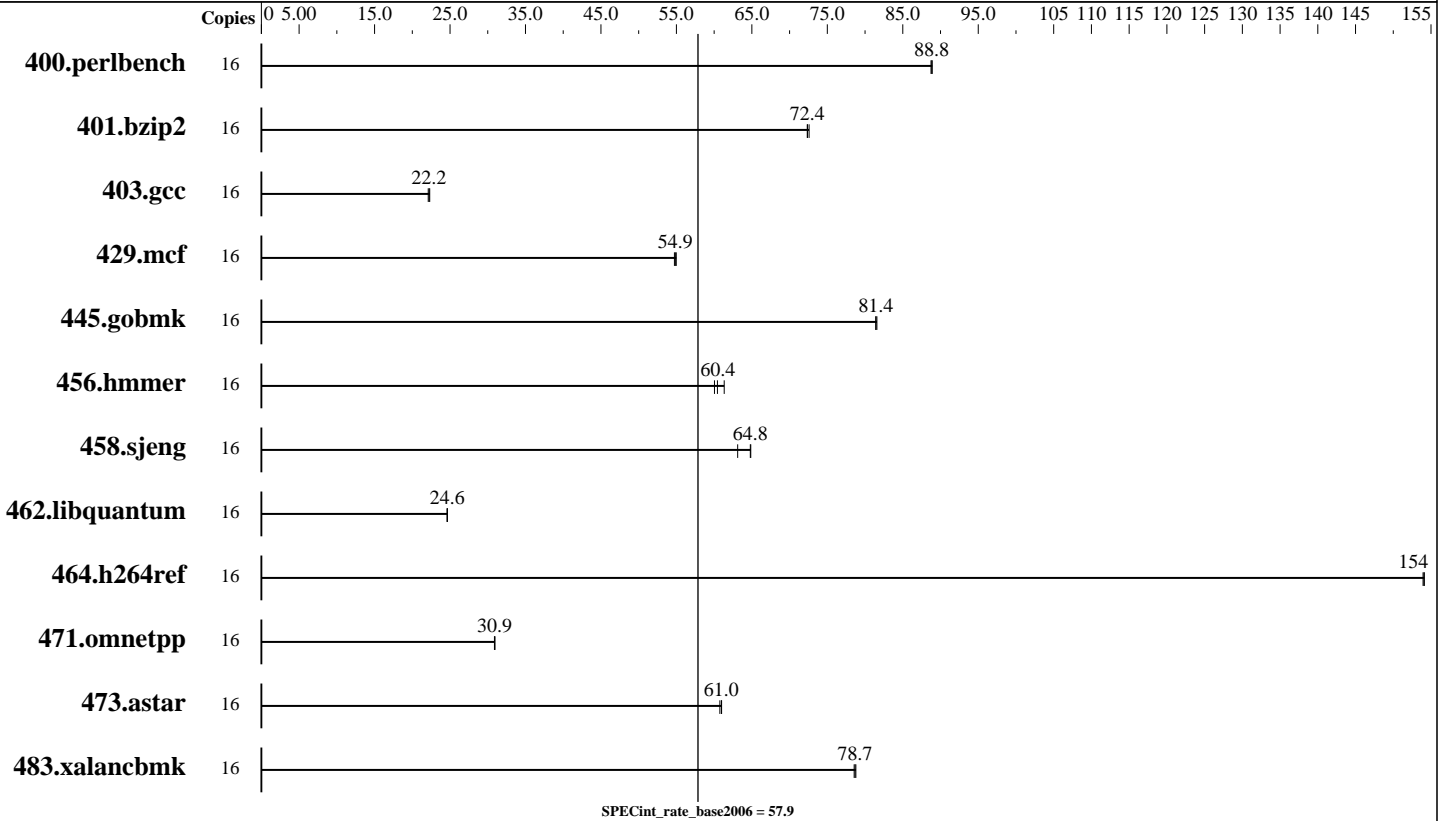
IBM Corporation  
IBM System x3950

SPECint®\_rate2006 = Not Run

SPECint\_rate\_base2006 = 57.9

CPU2006 license: 11  
Test sponsor: IBM Corporation  
Tested by: IBM Corporation

Test date: Dec-2006  
Hardware Availability: Oct-2006  
Software Availability: Aug-2006



## Hardware

CPU Name: Intel Xeon 7130N  
 CPU Characteristics: 667 MHz bus  
 CPU MHz: 3166  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,4 chips  
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (16 x 2048 MB ECC PC2-3200)  
 Disk Subsystem: 73 GB SAS, 10k RPM  
 Other Hardware: None

## Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Build no 20060816  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Smart Heap Library, Version 8



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	1762	88.7	<b>1760</b>	<b>88.8</b>	1758	88.9							
401.bzip2	16	2134	72.3	2127	72.6	<b>2133</b>	<b>72.4</b>							
403.gcc	16	5774	22.3	5825	22.1	<b>5794</b>	<b>22.2</b>							
429.mcf	16	<b>2660</b>	<b>54.9</b>	2666	54.7	2654	55.0							
445.gobmk	16	2058	81.6	<b>2061</b>	<b>81.4</b>	2061	81.4							
456.hammer	16	2486	60.0	2434	61.3	<b>2470</b>	<b>60.4</b>							
458.sjeng	16	3067	63.1	<b>2988</b>	<b>64.8</b>	2985	64.9							
462.libquantum	16	<b>13460</b>	<b>24.6</b>	13463	24.6	13450	24.6							
464.h264ref	16	<b>2298</b>	<b>154</b>	2300	154	2298	154							
471.omnetpp	16	3232	30.9	3238	30.9	<b>3233</b>	<b>30.9</b>							
473.astar	16	1849	60.8	<b>1842</b>	<b>61.0</b>	1842	61.0							
483.xalancbmk	16	<b>1403</b>	<b>78.7</b>	1401	78.8	1405	78.6							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Bios Settings  
Hardware Prefetch enabled  
Memory Array set to High-Performance Memory Array

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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## Base Optimization Flags (Continued)

C++ benchmarks:  
-fast -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at  
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html>

You can also download the XML flags source by saving the following link:  
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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