



# SPEC® CFP2006 Result

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Fujitsu Siemens Computers

CELSIUS R640, Intel Xeon 5160 processor

**SPECfp®\_rate2006 = 42.5**

CPU2006 license: 22

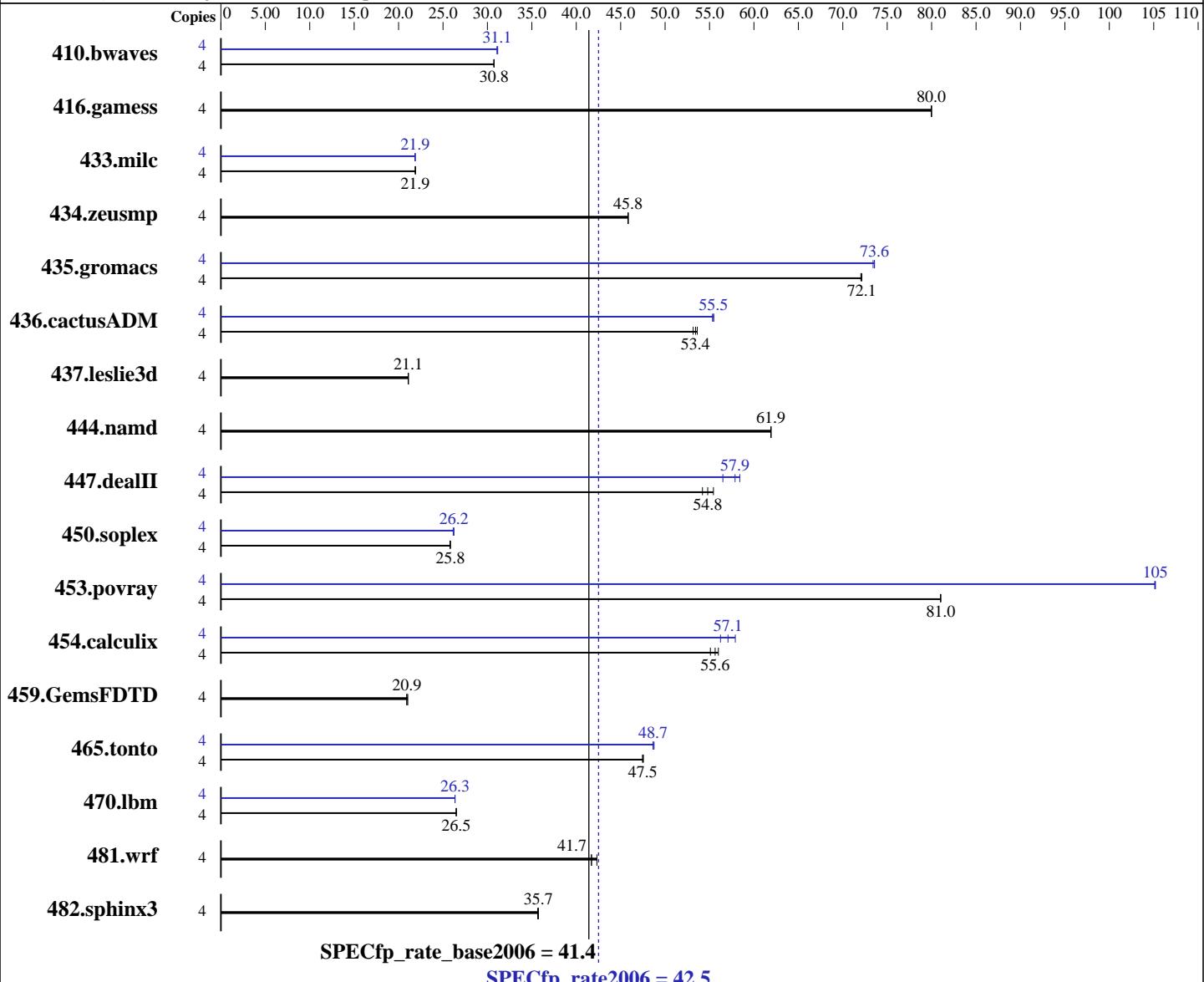
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

**Test date:** Nov-2006

**Hardware Availability:** Sep-2006

**Software Availability:** Nov-2006



<b>Hardware</b>		<b>Software</b>	
CPU Name:	Intel Xeon 5160	Operating System:	Windows XP Professional x64 Edition
CPU Characteristics:	Dual Core, 3.0 GHz	Compiler:	Intel C++ Compiler for EM64T version 9.1 - Build 20061104, Package-ID W_CC_C_9.1.033
CPU MHz:	3000		Intel Fortran Compiler for EM64T version 9.1 - Build 20061104, Package-ID W_FC_C_9.1.033
FPU:	Integrated		Microsoft Visual Studio 2005 (libr. & linker)
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip	Auto Parallel:	No
CPU(s) orderable:	1,2 chips	File System:	NTFS
Primary Cache:	32 KB I + 32 KB D on chip per core	System State:	Default
Secondary Cache:	4 MB I+D on chip per chip		Continued on next page

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L3 Cache: None

Base Pointers: 64-bit

Other Cache: None

Peak Pointers: 64-bit

Memory: 8 GB (8x1 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)

Other Software: Smart Heap Library, Version 8

Disk Subsystem: SATA II 7200 rpm

Other Hardware: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1767	30.8	<b>1767</b>	<b>30.8</b>	1770	30.7	4	1744	31.2	1748	31.1	<b>1748</b>	<b>31.1</b>
416.gamess	4	<b>979</b>	<b>80.0</b>	980	80.0	979	80.0	4	<b>979</b>	<b>80.0</b>	980	80.0	979	80.0
433.milc	4	1678	21.9	<b>1678</b>	<b>21.9</b>	1677	21.9	4	1679	21.9	<b>1679</b>	<b>21.9</b>	1681	21.8
434.zeusmp	4	794	45.8	<b>794</b>	<b>45.8</b>	794	45.9	4	794	45.8	<b>794</b>	<b>45.8</b>	794	45.9
435.gromacs	4	396	72.1	<b>396</b>	<b>72.1</b>	396	72.0	4	389	73.4	388	73.6	<b>388</b>	<b>73.6</b>
436.cactusADM	4	892	53.6	<b>895</b>	<b>53.4</b>	899	53.2	4	<b>862</b>	<b>55.5</b>	864	55.3	862	55.5
437.leslie3d	4	1782	21.1	<b>1783</b>	<b>21.1</b>	1783	21.1	4	1782	21.1	<b>1783</b>	<b>21.1</b>	1783	21.1
444.namd	4	<b>518</b>	<b>61.9</b>	518	61.9	518	61.9	4	<b>518</b>	<b>61.9</b>	518	61.9	518	61.9
447.dealII	4	844	54.2	<b>835</b>	<b>54.8</b>	825	55.4	4	<b>791</b>	<b>57.9</b>	783	58.4	810	56.5
450.soplex	4	1294	25.8	<b>1292</b>	<b>25.8</b>	1291	25.8	4	1276	26.1	1272	26.2	<b>1272</b>	<b>26.2</b>
453.povray	4	263	81.0	<b>263</b>	<b>81.0</b>	263	81.0	4	202	105	202	105	<b>202</b>	<b>105</b>
454.calculix	4	589	56.0	599	55.1	<b>593</b>	<b>55.6</b>	4	570	57.9	<b>578</b>	<b>57.1</b>	587	56.2
459.GemsFDTD	4	2018	21.0	<b>2028</b>	<b>20.9</b>	2028	20.9	4	2018	21.0	<b>2028</b>	<b>20.9</b>	2028	20.9
465.tonto	4	828	47.6	829	47.5	<b>829</b>	<b>47.5</b>	4	807	48.8	809	48.6	<b>809</b>	<b>48.7</b>
470.lbm	4	2074	26.5	<b>2074</b>	<b>26.5</b>	2075	26.5	4	2085	26.4	2086	26.3	<b>2086</b>	<b>26.3</b>
481.wrf	4	<b>1071</b>	<b>41.7</b>	1071	41.7	1056	42.3	4	<b>1071</b>	<b>41.7</b>	1071	41.7	1056	42.3
482.sphinx3	4	2186	35.7	2182	35.7	<b>2182</b>	<b>35.7</b>	4	2186	35.7	2182	35.7	<b>2182</b>	<b>35.7</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

For information about Fujitsu Siemens Computers in your country please see:  
<http://www.fujitsu-siemens.com/countries>

## Base Compiler Invocation

C benchmarks:

  icl -Qvc8 -Qc99

C++ benchmarks:

  icl -Qvc8

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Fujitsu Siemens Computers CELSIUS R640, Intel Xeon 5160 processor	<b>SPECfp_rate2006 = 42.5</b>
	<b>SPECfp_rate_base2006 = 41.4</b>
CPU2006 license: 22	Test date: Nov-2006
Test sponsor: Fujitsu Siemens Computers	Hardware Availability: Sep-2006
Tested by: Fujitsu Siemens Computers	Software Availability: Nov-2006

## Base Compiler Invocation (Continued)

## Fortran benchmarks: ifort

Benchmarks using both Fortran and C:  
  `icl -Ovc8 -Oc99 ifort`

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
    433.milc: -D_Complex= -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -D_Complex= -DSPEC_CPU_P64
436.cactusADM: -D_Complex= -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
    444.namd: -DSPEC_CPU_P64 /TP
    447.dealII: -D_Complex= -DSPEC_CPU_P64 -DBOOST_NO_INTRINSIC_WCHAR_T
                  -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -D_Complex= -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER
                  -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
    465.tonto: -DSPEC_CPU_P64
        470.lbm: -D_Complex= -DSPEC_CPU_P64
        481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -D Complex= -DSPEC_CPU_P64
```

# Base Optimization Flags

C benchmarks:

-fast -F9500000000 shlw32M.lib

## C++ benchmarks:

-fast -Qcxx-features -F950000000 shlw32M.lib

## Fortran benchmarks:

-fast -F9500000000 shlw32M.lib

Benchmarks using both Fortran and C:



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## Peak Compiler Invocation

C benchmarks:

  icl -Qvc8 -Qc99

C++ benchmarks:

  icl -Qvc8

Fortran benchmarks:

  ifort

Benchmarks using both Fortran and C:

  icl -Qvc8 -Qc99 ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

  433.milc: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -F950000000  
    shlw32M.lib

  470.lbm: Same as 433.milc

  482.sphinx3: basepeak = yes

C++ benchmarks:

  444.namd: basepeak = yes

  447.dealII: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx-features  
    -F950000000 shlw32M.lib

  450.soplex: Same as 447.dealII

  453.povray: Same as 447.dealII

Fortran benchmarks:

  410.bwaves: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -F950000000  
    shlw32M.lib

  416.gamess: basepeak = yes

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## Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: Same as 410.bwaves

Benchmarks using both Fortran and C:

435.gromacs: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -F950000000  
shlw32M.lib

436.cactusADM: Same as 435.gromacs

454.calculix: Same as 435.gromacs

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090715.12.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.12.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090715.12.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.12.xml)

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.

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