SPEC® CINT2006 Result

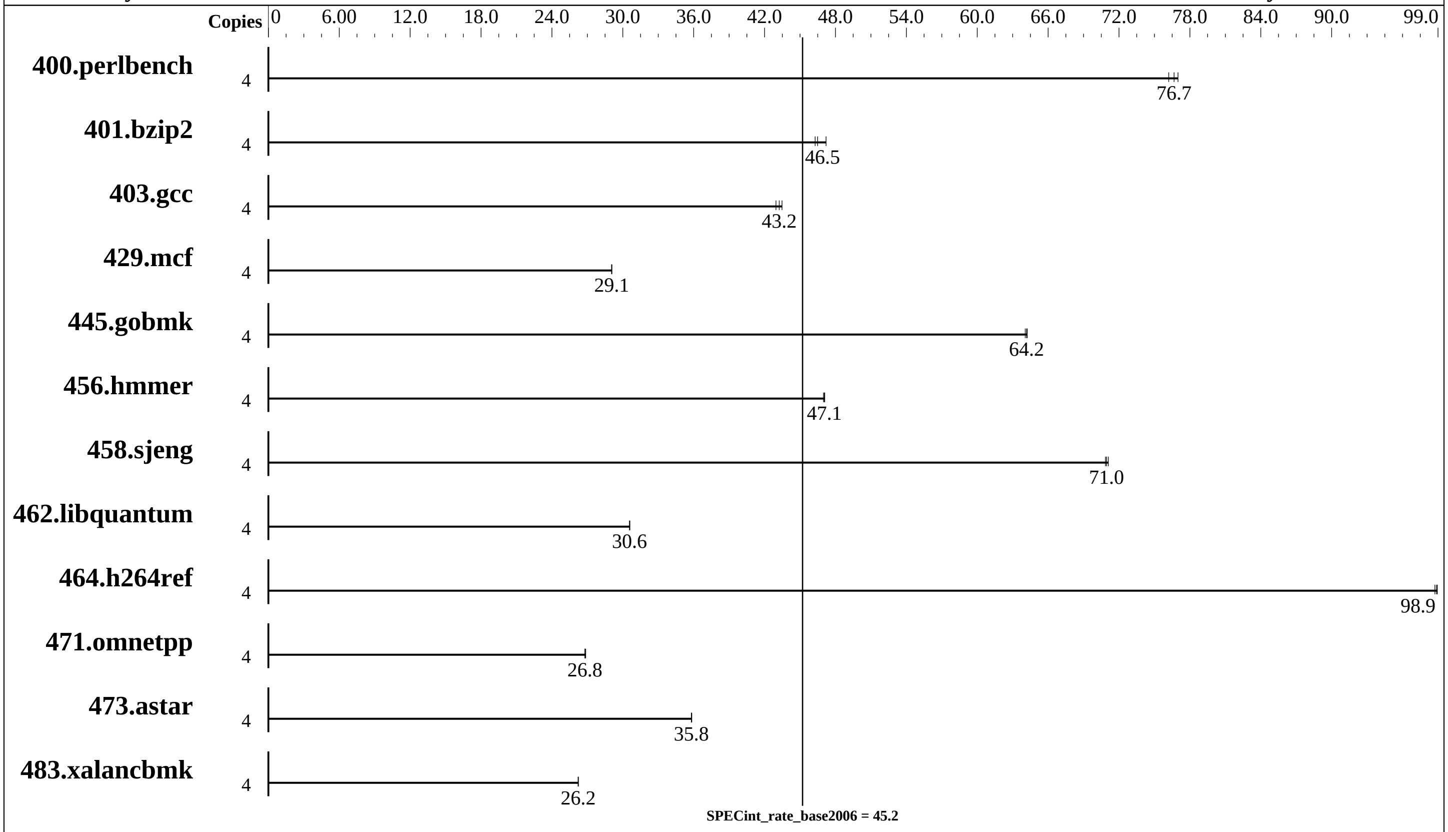
Supermicro
(Test Sponsor: Advanced Micro Devices)

Supermicro X7DBE (Intel Xeon 5160)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 45.2

CPU2006 license: 49		**Test date:**	Apr-2006
Test sponsor:	Advanced Micro Devices	**Hardware Availability:**	Jul-2006
Tested by:	Advanced Micro Devices	**Software Availability:**	Jul-2006



Hardware

CPU Name:	Intel Xeon 5160
CPU Characteristics:	3.0GHz 1333MHz System Bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8 x 1 GB DDR2-667 CL5 ECC FB)
Disk Subsystem:	250GB SATA
Other Hardware:	None

Software

Operating System:	SUSE Linux 10.1 (for x86_64)
Compiler:	SUSE gcc, g++ 4.1 (for x86_64)
Auto Parallel:	No
File System:	ext3
System State:	runlevel 3
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other Software:	None

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Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	4	508	77.0	513	76.2	**510**	**76.7**							
401.bzip2	4	818	47.2	834	46.3	**830**	**46.5**							
403.gcc	4	741	43.5	749	43.0	**745**	**43.2**							
429.mcf	4	**1255**	**29.1**	1255	29.1	1256	29.0							
445.gobmk	4	655	64.1	653	64.2	**654**	**64.2**							
456.hmmer	4	**793**	**47.1**	792	47.1	794	47.0							
458.sjeng	4	**682**	**71.0**	681	71.1	683	70.9							
462.libquantum	4	2709	30.6	2711	30.6	**2711**	**30.6**							
464.h264ref	4	897	98.7	**895**	**98.9**	895	98.9							
471.omnetpp	4	931	26.9	933	26.8	**933**	**26.8**							
473.astar	4	783	35.8	**784**	**35.8**	784	35.8							
483.xalancbmk	4	**1052**	**26.2**	1052	26.2	1053	26.2							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
 The system can be assembled using an SSI EEB form-factor case,
 and a Zippy/Emacs 700 watt PSL-6701P Power Supply.
Other Configuration Notes
 Taskset utility used to bind process to cores
```

Base Compiler Invocation

C benchmarks:
`gcc`

C++ benchmarks:
`g++`

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Continued on next page

Base Portability Flags (Continued)

464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-O3 -fno-inline-functions -funroll-loops`

C++ benchmarks:
`-O3 -funroll-loops`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.xml

Tested with SPEC CPU2006 v1.0.
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