



SPEC® CINT2006 Result

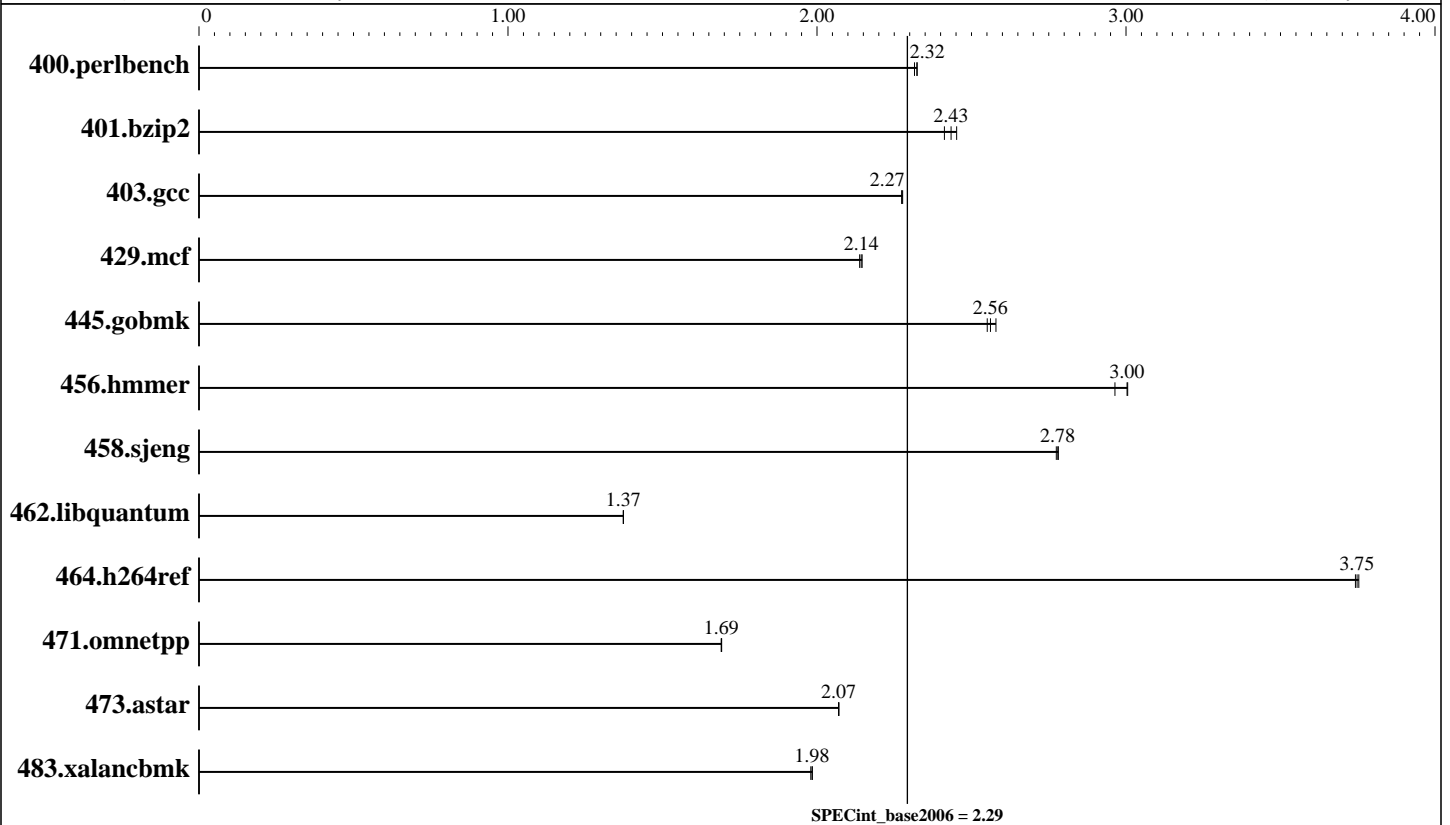
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Sun Microsystems Sun Fire 3800

SPECint®2006 = --
SPECint_base2006 = 2.29

CPU2006 license: 6
Test sponsor: Sun Microsystems
Tested by: Sun Microsystems

Test date: Mar-2006
Hardware Availability: Mar-2001
Software Availability: May-2006



Hardware

CPU Name: UltraSPARC III Cu
 CPU Characteristics: 750
 CPU MHz: Integrated
 FPU: 1 core, 1 chip, 1 core/chip
 CPU(s) enabled: 2,4,6,8 chips
 CPU(s) orderable: 32 KB I + 64 KB D on chip per chip
 Primary Cache: 8 MB I+D off chip per chip
 Secondary Cache: None
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (8 x 256 MB DIMM, 4-way interleaved)
 Disk Subsystem: 1x18GB 10,000RPM SCSI for SPEC
 2x18GB 10,000RPM SCSI (RAID 1) for system
 Other Hardware: None

Software

Operating System: Solaris 10 1/06
 Compiler: Sun Studio 11 with patch 120760-03
 Auto Parallel: No
 File System: ufs
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: None



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4219	2.32	4204	2.32	<u>4205</u>	<u>2.32</u>						
401.bzip2	4000	2.41	<u>3965</u>	<u>2.43</u>	3936	2.45						
403.gcc	<u>3539</u>	<u>2.27</u>	3539	2.27	3535	2.28						
429.mcf	4266	2.14	<u>4256</u>	<u>2.14</u>	4250	2.15						
445.gobmk	4067	2.58	4112	2.55	<u>4095</u>	<u>2.56</u>						
456.hammer	3104	3.01	3147	2.96	<u>3106</u>	<u>3.00</u>						
458.sjeng	4351	2.78	4361	2.77	<u>4354</u>	<u>2.78</u>						
462.libquantum	15086	1.37	15087	1.37	<u>15087</u>	<u>1.37</u>						
464.h264ref	5896	3.75	<u>5905</u>	<u>3.75</u>	5913	3.74						
471.omnetpp	<u>3697</u>	<u>1.69</u>	3695	1.69	3698	1.69						
473.astar	3391	2.07	3391	2.07	<u>3391</u>	<u>2.07</u>						
483.xalancbmk	3485	1.98	<u>3478</u>	<u>1.98</u>	3476	1.99						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack limit set to 131072

Platform Notes

Only 1 CPU enabled by the system controller
(prior to boot)

Base Compiler Invocation

C benchmarks:
cc

C++ benchmarks:
CC

Base Portability Flags

400.perlbench: -DSPEC_CPU_SOLARIS_SPARC
403.gcc: -DSPEC_CPU_SOLARIS
462.libquantum: -DSPEC_CPU_SOLARIS
483.xalancbmk: -DSPEC_CPU_SOLARIS



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Base Optimization Flags

C benchmarks:
-g -fast -xpagesize=4M
C++ benchmarks:
-g0 -library=stlport4 -fast -xpagesize=4M

Base Other Flags

C benchmarks:
-V
C++ benchmarks:
-verbose=version

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/sun-studio.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/sun-studio.xml>

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For other inquiries, please contact webmaster@spec.org.

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