SPEC® CINT2006 Result

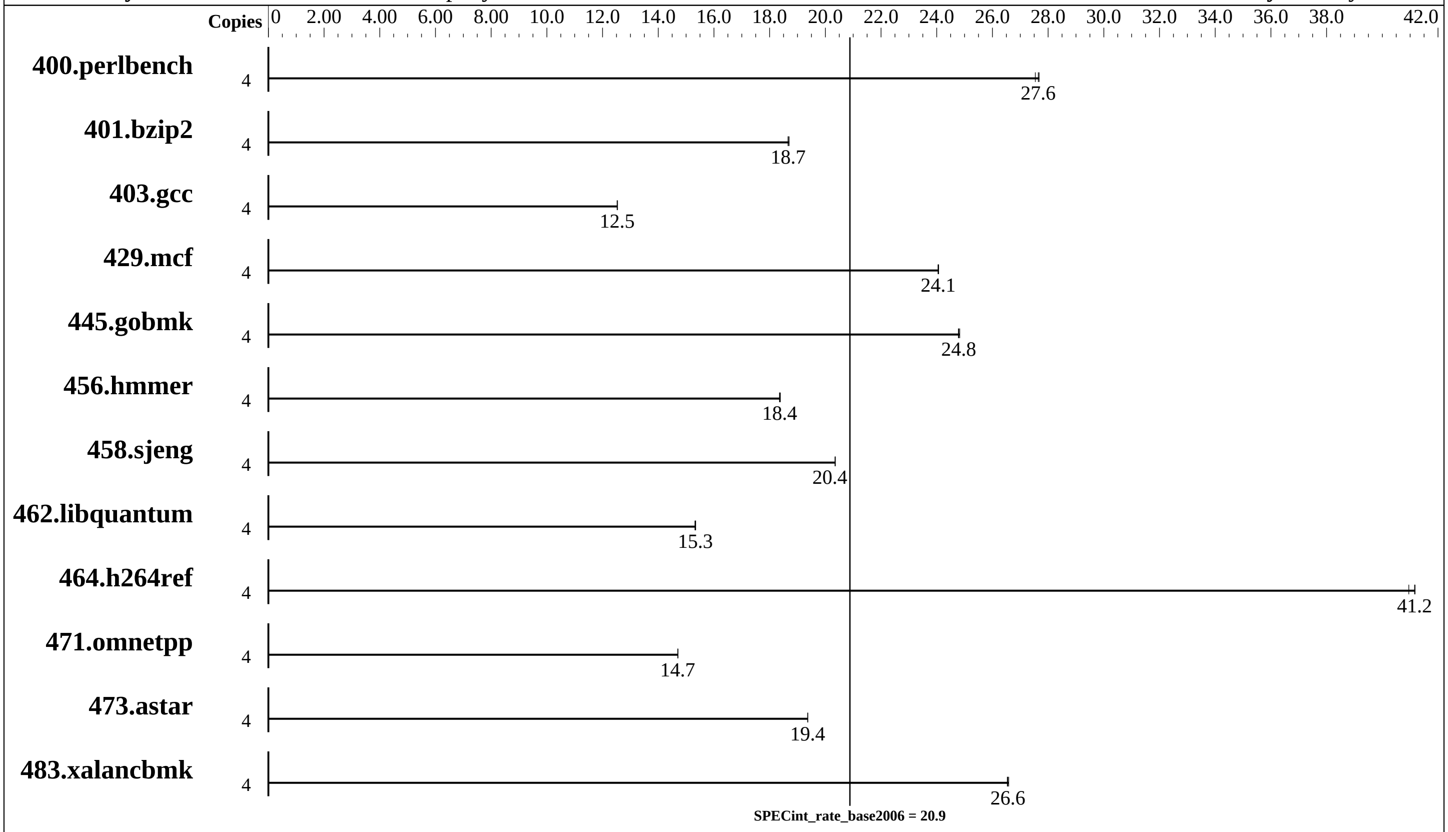
Hewlett-Packard Company

ProLiant DL380 G4 (3.8GHz, Intel Xeon Processor)

SPECint®_rate2006 = --

SPECint_rate_base2006 = 20.9

CPU2006 license:	3	**Test date:**	Apr-2006
Test sponsor:	Hewlett-Packard Company	**Hardware Availability:**	Sep-2005
Tested by:	Hewlett-Packard Company	**Software Availability:**	May-2006



Hardware

CPU Name:	Intel Xeon
CPU Characteristics:	3.8GHz, 2MB L2, 800MHz bus
CPU MHz:	3800
FPU:	Integrated
CPU(s) enabled:	2 cores, 2 chips, 1 core/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per chip
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB PC2-3200)
Disk Subsystem:	146GB 10k U320 SCSI
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Ent, SP1
Compiler:	Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z Package ID: W_CC_P_9.1.020 Microsoft Visual Studio .NET 2003 (for libraries) MicroQuill SmartHeap Library 7.4
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	None

Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	4	1419	27.5	1412	27.7	**1414**	**27.6**							
401.bzip2	4	2064	18.7	2068	18.7	**2068**	**18.7**							
403.gcc	4	2569	12.5	**2571**	**12.5**	2571	12.5							
429.mcf	4	1516	24.1	1517	24.0	**1516**	**24.1**							
445.gobmk	4	**1692**	**24.8**	1694	24.8	1690	24.8							
456.hmmer	4	**2033**	**18.4**	2030	18.4	2033	18.4							
458.sjeng	4	**2378**	**20.4**	2379	20.3	2377	20.4							
462.libquantum	4	5401	15.3	**5406**	**15.3**	5411	15.3							
464.h264ref	4	2162	41.0	**2151**	**41.2**	2150	41.2							
471.omnetpp	4	1701	14.7	1700	14.7	**1701**	**14.7**							
473.astar	4	1450	19.4	**1450**	**19.4**	1450	19.4							
483.xalancbmk	4	1040	26.5	**1039**	**26.6**	1038	26.6							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
           -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.01.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v90.
Report generated on Tue Jul 22 09:59:27 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 August 2006.