SPEC® OMPG2012 Result

Lenovo Global Technology

ThinkSystem SR655 V3 (AMD EPYC 9654P)

SPECompG_peak2012 =   48.4

SPECompG_base2012 =   47.5

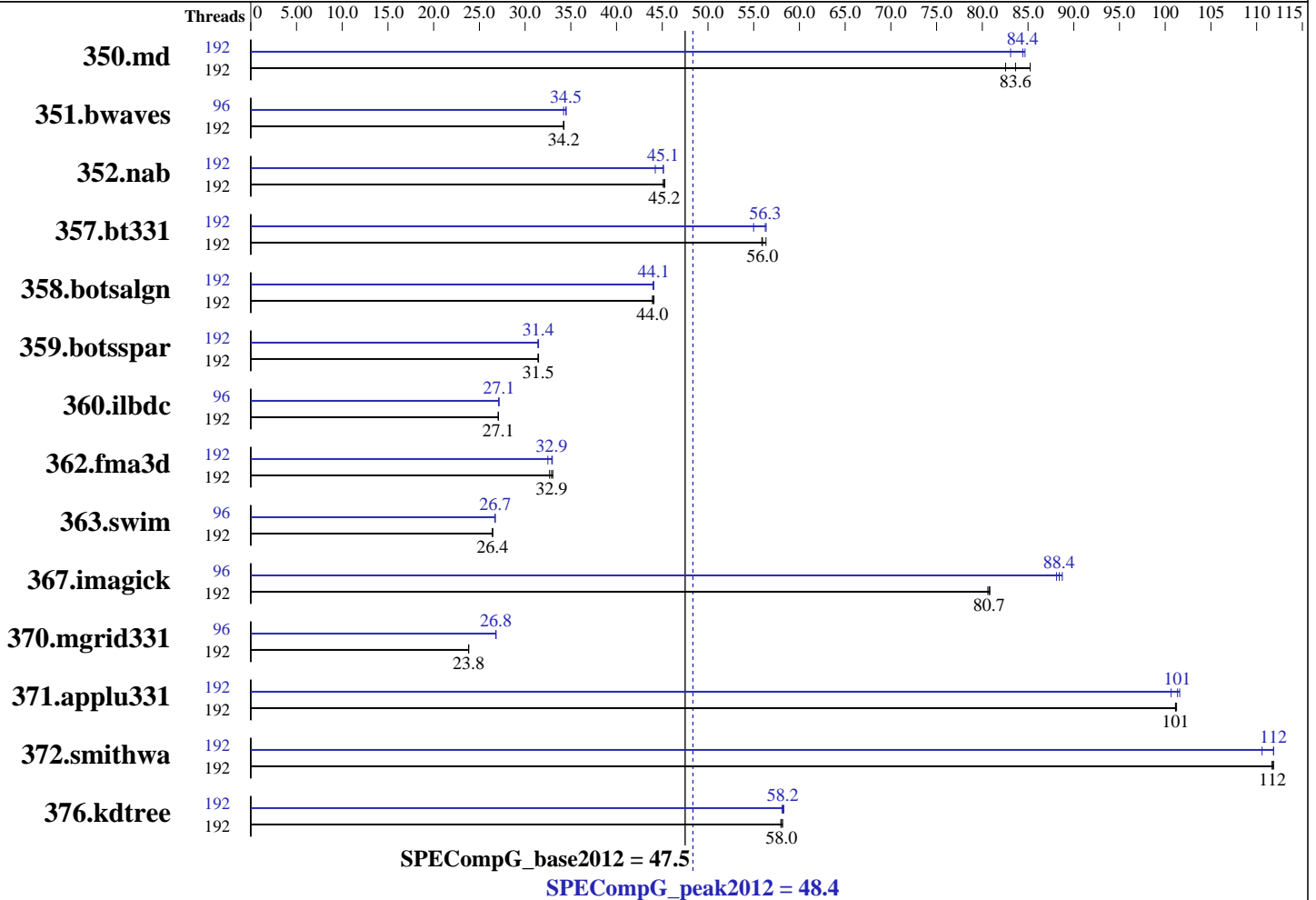**OMP2012 license:** 28

Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Feb-2023

Hardware Availability: Mar-2023

Software Availability: Mar-2023

Threads | Results

- 350.md — 192 (peak): 84.4; 192 (base): 83.6
- 351.bwaves — 96 (peak): 34.5; 192 (base): 34.2
- 352.nab — 192 (peak): 45.1; 192 (base): 45.2
- 357.bt331 — 192 (peak): 56.3; 192 (base): 56.0
- 358.botsalgn — 192 (peak): 44.1; 192 (base): 44.0
- 359.botsspar — 192 (peak): 31.4; 192 (base): 31.5
- 360.ilbdc — 96 (peak): 27.1; 192 (base): 27.1
- 362.fma3d — 192 (peak): 32.9; 192 (base): 32.9
- 363.swim — 96 (peak): 26.7; 192 (base): 26.4
- 367.imagick — 96 (peak): 88.4; 192 (base): 80.7
- 370.mgrid331 — 96 (peak): 26.8; 192 (base): 23.8
- 371.applu331 — 192 (peak): 101; 192 (base): 101
- 372.smithwa — 192 (peak): 112; 192 (base): 112
- 376.kdtree — 192 (peak): 58.2; 192 (base): 58.0

SPECompG_base2012 = 47.5

SPECompG_peak2012 = 48.4

Hardware

CPU Name:	AMD EPYC 9654P CPU
CPU Characteristics:	Max Boost Clock up to 3.7 GHz
CPU MHz:	2400
CPU MHz Maximum:	3700
FPU:	Integrated
CPU(s) enabled:	192 cores, 1 chip, 96 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	384 MB I+D on chip per chip
Other Cache:	None
Memory:	768 GB (12 x 64 GB 2Rx4 PC5-4800B-R)
Disk Subsystem:	1 x 1 TB SATA Hard Drive
Other Hardware:	None
Base Threads Run:	192
Minimum Peak Threads:	96

Software

Operating System:	Red Hat Enterprise Linux (x86_64) , Kernel 4.18.0-425.3.1.el8.x86_64
Compiler:	C/C++/Fortran: Version 2022.2.0.191 of Intel oneAPI DPC/C++
Auto Parallel:	No
File System:	xfs
System State:	Multi-user, run level 3
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other Software:	None

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Maximum Peak Threads: 192

Results Table

Benchmark	Base Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
350.md	192	**55.4**	**83.6**	54.3	85.2	56.1	82.5	192	54.7	84.7	55.7	83.1	**54.8**	**84.4**
351.bwaves	192	**132**	**34.2**	132	34.3	133	34.2	96	131	34.5	**131**	**34.5**	132	34.2
352.nab	192	**86.0**	**45.2**	86.2	45.1	85.9	45.3	192	86.2	45.1	**86.3**	**45.1**	87.9	44.2
357.bt331	192	84.8	55.9	84.1	56.3	**84.7**	**56.0**	192	84.1	56.4	86.2	55.0	**84.2**	**56.3**
358.botsalgn	192	98.7	44.1	99.0	43.9	**99.0**	**44.0**	192	**98.7**	**44.1**	98.9	44.0	98.7	44.1
359.botsspar	192	167	31.5	**167**	**31.5**	167	31.4	192	167	31.5	**167**	**31.4**	167	31.4
360.ilbdc	192	132	27.1	131	27.1	**132**	**27.1**	96	131	27.1	**131**	**27.1**	131	27.1
362.fma3d	192	**116**	**32.9**	116	32.7	115	33.0	192	**115**	**32.9**	117	32.5	115	33.0
363.swim	192	171	26.4	171	26.4	**171**	**26.4**	96	170	26.7	**169**	**26.7**	169	26.7
367.imagick	192	87.0	80.8	**87.1**	**80.7**	87.2	80.6	96	79.2	88.8	**79.5**	**88.4**	79.8	88.1
370.mgrid331	192	**186**	**23.8**	185	23.8	186	23.8	96	165	26.8	165	26.8	**165**	**26.8**
371.applu331	192	59.9	101	**59.9**	**101**	59.9	101	192	**59.8**	**101**	60.2	101	59.7	102
372.smithwa	192	47.9	112	**48.0**	**112**	48.0	112	192	47.9	112	48.5	111	**47.9**	**112**
376.kdtree	192	**77.5**	**58.0**	77.6	58.0	77.4	58.2	192	77.4	58.1	77.2	58.3	**77.3**	**58.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Sysinfo program /home/omp2012/Docs/sysinfo
Revision 563 of 2016-06-10 (097295389cf6073d8c3b03fa376740a5)
running on Kahoolawe_OMP Wed Feb  8 16:23:12 2023

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/omp2012/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : AMD EPYC 9654P 96-Core Processor
      1 "physical id"s (chips)
      192 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 96
      siblings  : 192
      physical 0: cores 0 1 2 3 4 5 6 7 10 11 12 13 14 15 16 17 18 19 20 21 22
      23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
      48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72
      73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95
   cache size : 1024 KB
```

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Lenovo Global Technology

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OMP2012 license: 28

Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Feb-2023

Hardware Availability: Mar-2023

Software Availability: Mar-2023

Platform Notes (Continued)

```
From /proc/meminfo
   MemTotal:        791987648 kB
   HugePages_Total:         0
   Hugepagesize:         2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux"
      VERSION="8.7 (Ootpa)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="8.7"
      PLATFORM_ID="platform:el8"
      PRETTY_NAME="Red Hat Enterprise Linux 8.7 (Ootpa)"
      ANSI_COLOR="0;31"
   redhat-release: Red Hat Enterprise Linux release 8.7 (Ootpa)
   system-release: Red Hat Enterprise Linux release 8.7 (Ootpa)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:8::baseos

uname -a:
   Linux Kahoolawe_OMP 4.18.0-425.3.1.el8.x86_64 #1 SMP Fri Sep 30 11:45:06 EDT
   2022 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 8 06:33

SPEC is set to: /home/omp2012
   Filesystem             Type  Size  Used Avail Use% Mounted on
   /dev/mapper/rhel-home xfs    819G   29G  790G   4% /home
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS Lenovo KAE107C-1.30 01/16/2023
   Memory:
   8x SK Hynix HMCG94AEBQA109N 64 GB 2 rank 4800 MT/s
   4x SK Hynix HMCG94AEBRA102N 64 GB 2 rank 4800 MT/s

(End of data from sysinfo program)
```

General Notes

```
==============================================================================
General OMP Library Settings
   OMP_DYNAMIC  = FALSE
   OMP_NUM_THREADS  = 192
   KMP_SCHEDULE = static
   KMP_LIBRARY  = turnaround
   KMP_STACKSIZE = 768M
```

Lenovo Global Technology

$SPECompG\_peak2012 = 48.4$

ThinkSystem SR655 V3 (AMD EPYC 9654P)

$SPECompG\_base2012 = 47.5$

OMP2012 license: 28
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Test date: Feb-2023
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Software Availability: Mar-2023

General Notes (Continued)

```
    KMP_BLOCKTIME = infinite
    KMP_AFFINITY = granularity=fine,proclist=[0-7,8-15,16-23,24-31,32-39,40-47,
    48-55,56-63,64-71,72-79,80-87,88-95,96-103,104-111,112-119,120-127,128-135,
    136-143,144-151,152-159,160-167,168-175,176-183,184-191],explicit
==============================================================================
 uEFI Setting notes:
  Choose "Maximum Performance" operating mode and changed to "Custom" operating mode. Below items also configured:
   - CPPC = Disabled
   - NUMA Nodes per Socket = NPS1
   - ACPI SRAT L3 Cache as NUMA Domain = Enabled
   - DRAM Scrub Time = Disabled
   - Memory Power Down enable = Disabled
   - DF C-state = Disabled
   - P-state 1 = Disabled

===============================================
 Yes: The test sponsor attests, as of date of publication, the CVE-2017-5754 (Meltdown)
 is mitigated in the system as tested and documented.

 Yes: The test sponsor attests, as of date of publication, the CVE-2017-5753 (Spectre variant 1)
 is mitigated in the system as tested and documented.

 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Spectre variant 2)
 is mitigated in the system as tested and documented.
===============================================
 OS tunning:
 ulimit -s unlimited
```

Base Compiler Invocation

C benchmarks:
```
    icx
```

C++ benchmarks:
```
    icpx
```

Fortran benchmarks:
```
    ifx
```

Base Portability Flags

```
    350.md: -FR
  357.bt331: -mcmodel=medium
   363.swim: -mcmodel=medium
 367.imagick: -std=c99
```

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OMP2012 license:28
Test sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test date: Feb-2023
Hardware Availability: Mar-2023
Software Availability: Mar-2023

Base Optimization Flags

C benchmarks:
```
-Ofast -march=core-avx2 -fopenmp -ipo  -fma -ansi-alias
-fp-model fast=2 -qno-opt-multiple-gather-scatter-by-shuffles
-no-prec-sqrt -ffast-math -fstrict-enums -fstrict-vtable-pointers
-fvirtual-function-elimination
```

C++ benchmarks:
```
-Ofast -march=core-avx2 -fopenmp -ipo  -fma -ansi-alias
-fp-model fast=2 -qno-opt-multiple-gather-scatter-by-shuffles
-no-prec-sqrt -ffast-math -fstrict-enums -fstrict-vtable-pointers
```

Fortran benchmarks:
```
-Ofast -march=core-avx2 -fopenmp -ipo  -fma -ansi-alias
-fp-model fast=2 -qno-opt-multiple-gather-scatter-by-shuffles
-no-prec-sqrt -align array128byte -ffinite-math-only
-fno-omit-frame-pointer -m64 -ipo1
```

Peak Compiler Invocation

C benchmarks:
```
icx
```

C++ benchmarks:
```
icpx
```

Fortran benchmarks:
```
ifx
```

Peak Portability Flags

```
    350.md: -FR
 357.bt331: -mcmodel=medium
  363.swim: -mcmodel=medium
367.imagick: -std=c99
```

Peak Optimization Flags

C benchmarks:

```
    352.nab: -Ofast -march=core-avx2 -fopenmp -ipo  -fma -ansi-alias
             -fp-model fast=2 -qno-opt-multiple-gather-scatter-by-shuffles
             -no-prec-sqrt -ffast-math -fstrict-enums
             -fstrict-vtable-pointers -fvirtual-function-elimination
             -fno-signed-zeros
```

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Lenovo Global Technology

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Peak Optimization Flags (Continued)

```
358.botsalgn: -Ofast -march=core-avx2 -fopenmp -ipo  -fma -ansi-alias
              -fp-model fast=2 -qno-opt-multiple-gather-scatter-by-shuffles
              -no-prec-sqrt -ffast-math -fstrict-enums
              -fstrict-vtable-pointers -fvirtual-function-elimination

359.botsspar: Same as 358.botsalgn

367.imagick: Same as 358.botsalgn

372.smithwa: Same as 358.botsalgn
```

C++ benchmarks:
```
  -Ofast -march=core-avx2 -fopenmp -ipo  -fma -ansi-alias
  -fp-model fast=2 -qno-opt-multiple-gather-scatter-by-shuffles
  -no-prec-sqrt -ffast-math -fstrict-enums -fstrict-vtable-pointers
```

Fortran benchmarks:
```
    350.md: -Ofast -march=core-avx2 -fopenmp -ipo  -fma -ansi-alias
            -fp-model fast=2 -qno-opt-multiple-gather-scatter-by-shuffles
            -no-prec-sqrt -align array128byte -ffinite-math-only
            -fno-omit-frame-pointer -m64 -ipo1

351.bwaves: Same as 350.md

357.bt331: -Ofast -march=core-avx2 -fopenmp -ipo  -fma -ansi-alias
           -fp-model fast=2 -qno-opt-multiple-gather-scatter-by-shuffles
           -no-prec-sqrt -align array128byte -ffinite-math-only
           -fno-omit-frame-pointer -m64 -ipo1 -norecursive

360.ilbdc: Same as 350.md

362.fma3d: Same as 350.md

363.swim: Same as 350.md

370.mgrid331: Same as 350.md

371.applu331: Same as 350.md
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/omp2012/flags/lenovo-omp2012-oneAPI.20230222.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/omp2012/flags/lenovo-omp2012-oneAPI.20230222.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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