



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR635 V3  
(2.40 GHz, AMD EPYC 9745)

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

CPU2017 License: 9017

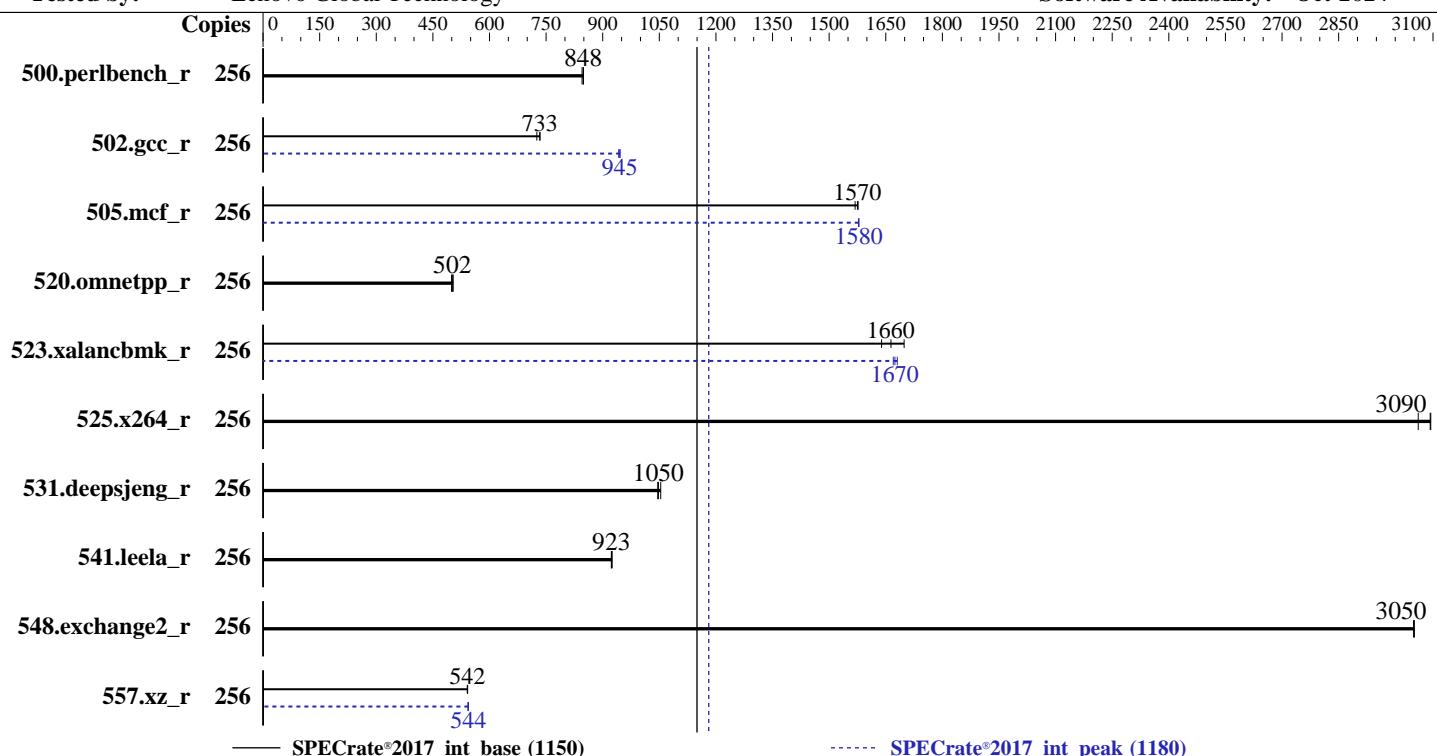
Test Date: Mar-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024



### Hardware

CPU Name: AMD EPYC 9745  
Max MHz: 3700  
Nominal: 2400  
Enabled: 128 cores, 1 chip, 2 threads/core  
Orderable: 1 chip  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 256 MB I+D on chip per chip,  
32 MB shared / 16 cores  
Other: None  
Memory: 768 GB (12 x 64 GB 2Rx4 PC5-6400B-R, running at 6000)  
Storage: 1 x 480 GB SATA SSD  
Other: CPU Cooling: Air

### Software

OS: Red Hat Enterprise Linux 9.4 (Plow)  
Compiler: Kernel 5.14.0-427.13.1.el9\_4.x86\_64  
Parallel: C/C++/Fortran: Version 5.0.0 of AOCC  
Firmware: No  
File System: Lenovo BIOS Version KAE13II 5.30 released Dec-2024  
System State: xfs  
Base Pointers: Run level 3 (multi-user)  
Peak Pointers: 64-bit  
Other: 32/64-bit  
Power Management: None  
BIOS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR635 V3  
(2.40 GHz, AMD EPYC 9745)

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

CPU2017 License: 9017

Test Date: Mar-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	256	<b>480</b>	<b>848</b>	480	849	482	845	256	<b>480</b>	<b>848</b>	480	849	482	845	482	845
502.gcc_r	256	500	725	494	734	<b>494</b>	<b>733</b>	256	<b>384</b>	<b>945</b>	383	946	385	942	385	942
505.mcf_r	256	264	1570	262	1580	<b>263</b>	<b>1570</b>	256	<b>262</b>	<b>1580</b>	262	1580	262	1580	262	1580
520.omnetpp_r	256	<b>670</b>	<b>502</b>	667	504	673	499	256	<b>670</b>	<b>502</b>	667	504	673	499	673	499
523.xalancbmk_r	256	<b>162</b>	<b>1660</b>	165	1640	159	1700	256	<b>161</b>	<b>1670</b>	162	1670	161	1680	161	1680
525.x264_r	256	146	3060	145	3090	<b>145</b>	<b>3090</b>	256	146	3060	145	3090	<b>145</b>	<b>3090</b>	<b>145</b>	<b>3090</b>
531.deepsjeng_r	256	281	1050	278	1050	<b>280</b>	<b>1050</b>	256	281	1050	278	1050	<b>280</b>	<b>1050</b>	<b>280</b>	<b>1050</b>
541.leela_r	256	458	925	<b>459</b>	<b>923</b>	459	923	256	458	925	<b>459</b>	<b>923</b>	459	923	459	923
548.exchange2_r	256	220	3050	220	3050	<b>220</b>	<b>3050</b>	256	220	3050	220	3050	<b>220</b>	<b>3050</b>	<b>220</b>	<b>3050</b>
557.xz_r	256	<b>511</b>	<b>542</b>	511	541	510	542	256	508	544	<b>508</b>	<b>544</b>	509	543	509	543

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations:  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR635 V3  
(2.40 GHz, AMD EPYC 9745)

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

CPU2017 License: 9017

Test Date: Mar-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017-1.1.9-amd-
    -aocc500_znver5_A1.2/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

Environment variables set by runcpu during the 523.xalancbmk\_r peak run:

```
MALLOC_CONF = "thp:always"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance and then set it to Custom Mode  
NUMA Nodes per Socket set to NPS4

```
Sysinfo program /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Sat Mar 15 16:16:05 2025
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-32.el9\_4)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR635 V3  
(2.40 GHz, AMD EPYC 9745)

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

CPU2017 License: 9017

Test Date: Mar-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Platform Notes (Continued)

22. dmidecode  
23. BIOS

1. uname -a  
Linux localhost.localdomain 5.14.0-427.13.1.el9\_4.x86\_64 #1 SMP PREEMPT\_DYNAMIC Wed Apr 10 10:29:16 EDT 2024 x86\_64 x86\_64 x86\_64 GNU/Linux

2. w  
16:16:05 up 3 min, 0 users, load average: 0.22, 0.17, 0.08  
USER TTY LOGIN@ IDLE JCPU PCPU WHAT

3. Username  
From environment variable \$USER: root

4. ulimit -a  
real-time non-blocking time (microseconds, -R) unlimited  
core file size (blocks, -c) 0  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (-i) 3094012  
max locked memory (kbytes, -l) 2097152  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 3094012  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited

5. sysinfo process ancestry  
/usr/lib/systemd/systemd --switched-root --system --deserialize 31  
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups  
sshd: root [priv]  
sshd: root@notty  
/bin/bash ./02.remote\_local\_SPECCpu\_1.01.sh  
/bin/bash ./Run025-compliant-amd-rateint.sh  
python3 ./run\_amd\_rate\_aocc500\_znver5\_A1.py  
/bin/bash ./amd\_rate\_aocc500\_znver5\_A1.sh  
runcpu --config amd\_rate\_aocc500\_znver5\_A1.cfg --tune all --reportable --iterations 3 intrate  
runcpu --configfile amd\_rate\_aocc500\_znver5\_A1.cfg --tune all --reportable --iterations 3 --nopower  
--runmode rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile  
\$SPEC/tmp/CPU2017.418/templogs/preenv.intrate.418.0.log --lognum 418.0 --from\_runcpu 2  
specperl \$SPEC/bin/sysinfo  
\$SPEC = /home/cpu2017-1.1.9-amd-aocc500\_znver5\_A1.2

6. /proc/cpuinfo  
model name : AMD EPYC 9745 128-Core Processor  
vendor\_id : AuthenticAMD  
cpu family : 26

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR635 V3  
(2.40 GHz, AMD EPYC 9745)

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

CPU2017 License: 9017

Test Date: Mar-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Platform Notes (Continued)

```
model      : 17
stepping   : 0
microcode  : 0xb101025
bugs       : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size   : 192 4K pages
cpu cores  : 128
siblings   : 256
1 physical ids (chips)
256 processors (hardware threads)
physical id 0: core ids 0-127
physical id 0: apicids 0-255
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

-----  
7. lscpu

From lscpu from util-linux 2.37.4:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                256
On-line CPU(s) list:  0-255
Vendor ID:             AuthenticAMD
BIOS Vendor ID:       Advanced Micro Devices, Inc.
Model name:            AMD EPYC 9745 128-Core Processor
BIOS Model name:      AMD EPYC 9745 128-Core Processor
CPU family:            26
Model:                 17
Thread(s) per core:   2
Core(s) per socket:   128
Socket(s):             1
Stepping:              0
BogoMIPS:              4792.82
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                      clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp
                      lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid
                      aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2
                      x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm
                      extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit
                      wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
                      cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp
                      ibrs_enhanced vmmcall fsgsbase tsc_adjust bmil avx2 smep bmi2 erms
                      invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                      clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                      xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local
                      avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin
                      cpc_arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyaid
                      decodeassist pausefilter pfthreshold avic v_vmsave_vmlload vgif x2avic
                      v_spec_ctrl vnni avx512vbmi umip pkru ospte avx512_vbmi2 gfn vaes
                      vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid
                      bus_lock_detect movdiri movdir64b overflow_recov succor smca fsrm
                      avx512_vp2intersect flush_lld debug_swap
Virtualization:        AMD-V
L1d cache:             6 MiB (128 instances)
L1i cache:             4 MiB (128 instances)
L2 cache:              128 MiB (128 instances)
L3 cache:              256 MiB (8 instances)
NUMA node(s):          4
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR635 V3  
(2.40 GHz, AMD EPYC 9745)

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

CPU2017 License: 9017

Test Date: Mar-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Platform Notes (Continued)

NUMA node0 CPU(s):	0-31,128-159
NUMA node1 CPU(s):	32-63,160-191
NUMA node2 CPU(s):	64-95,192-223
NUMA node3 CPU(s):	96-127,224-255
Vulnerability Gather data sampling:	Not affected
Vulnerability Itlb multihit:	Not affected
Vulnerability Llftf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec rstack overflow:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS, IBPB conditional, STIBP always-on, RSB filling, PBRSB-eIBRS Not affected
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	6M	12	Data	1	64	1	64
L1i	32K	4M	8	Instruction	1	64	1	64
L2	1M	128M	16	Unified	2	1024	1	64
L3	32M	256M	16	Unified	3	32768	1	64

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

node 0 cpus: 0-31,128-159

node 0 size: 193098 MB

node 0 free: 191747 MB

node 1 cpus: 32-63,160-191

node 1 size: 193519 MB

node 1 free: 192561 MB

node 2 cpus: 64-95,192-223

node 2 size: 193519 MB

node 2 free: 192580 MB

node 3 cpus: 96-127,224-255

node 3 size: 193403 MB

node 3 free: 192361 MB

node distances:

node 0 1 2 3

0: 10 12 12 12

1: 12 10 12 12

2: 12 12 10 12

3: 12 12 12 10

-----  
9. /proc/meminfo

MemTotal: 792107200 kB

-----  
10. who -r

run-level 3 Mar 15 16:13

-----  
11. Systemd service manager version: systemd 252 (252-32.el9\_4)

Default Target Status

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR635 V3  
(2.40 GHz, AMD EPYC 9745)

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

CPU2017 License: 9017

Test Date: Mar-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Platform Notes (Continued)

multi-user degraded

```
-----  
12. Failed units, from systemctl list-units --state=failed  
      UNIT          LOAD  ACTIVE SUB   DESCRIPTION  
* NetworkManager-wait-online.service loaded failed failed Network Manager Wait Online  
  
-----  
13. Services, from systemctl list-unit-files  
    STATE          UNIT FILES  
  enabled        NetworkManager NetworkManager-dispatcher NetworkManager-wait-online audited chronyd crond  
                dbus-broker firewalld getty@ insights-client-boot irqbalance kdump low-memory-monitor  
                mdmonitor microcode nis-domainname rhsmcertd rsyslog rtkit-daemon selinux-autorelabel-mark  
                sshd sssd systemd-boot-update systemd-network-generator udisks2 upower  
  enabled-runtime  systemd-remount-fs  
  disabled       canberra-system-bootup canberra-system-shutdown canberra-system-shutdown-reboot  
                chrony-wait chronyrd-restricted console-getty cpupower debug-shell dnf-system-upgrade  
                hwloc-dump-hwdata kvm_stat man-db-restart-cache-update nftables pesign rdisc rhcd rhsm  
                rhsm-facts rpmdb-rebuild selinux-check-proper-disable serial-getty@ sshd-keygen@  
                systemd-boot-check-no-failures systemd-pstore systemd-sysext  
  indirect        sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate  
                systemd-sysupdate-reboot  
  
-----  
14. Linux kernel boot-time arguments, from /proc/cmdline  
  BOOT_IMAGE=(hd3,gpt3)/boot/vmlinuz-5.14.0-427.13.1.el9_4.x86_64  
  root=UUID=273c79f3-13a9-4ae6-9c36-e82799318ef8  
  ro  
  resume=UUID=19c8733f-555f-4142-8f61-7554d0dc64ae  
  
-----  
15. cpupower frequency-info  
analyzing CPU 145:  
  Unable to determine current policy  
  boost state support:  
    Supported: yes  
    Active: yes  
    Boost States: 0  
    Total States: 3  
    Pstate-P0: 44800MHz  
  
-----  
16. sysctl  
  kernel.numa_balancing          1  
  kernel.randomize_va_space      0  
  vm.compaction_proactiveness   20  
  vm.dirty_background_bytes      0  
  vm.dirty_background_ratio     10  
  vm.dirty_bytes                 0  
  vm.dirty_expire_centisecs    3000  
  vm.dirty_ratio                 8  
  vm.dirty_writeback_centisecs  500  
  vm.dirtytime_expire_seconds   43200  
  vm.extfrag_threshold          500  
  vm.min_unmapped_ratio         1  
  vm.nr_hugepages               0  
  vm.nr_hugepages_mempolicy     0  
  vm.nr_overcommit_hugepages    0  
  vm.swappiness                  1  
  vm.watermark_boost_factor     15000
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR635 V3  
(2.40 GHz, AMD EPYC 9745)

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

CPU2017 License: 9017

Test Date: Mar-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Platform Notes (Continued)

vm.watermark\_scale\_factor 10  
vm.zone\_reclaim\_mode 1

-----  
17. /sys/kernel/mm/transparent\_hugepage  
defrag [always] defer defer+madvise madvise never  
enabled [always] madvise never  
hpage\_pmd\_size 2097152  
shmem\_enabled always within\_size advise [never] deny force

-----  
18. /sys/kernel/mm/transparent\_hugepage/khugepaged  
alloc\_sleep\_millisecs 60000  
defrag 1  
max\_ptes\_none 511  
max\_ptes\_shared 256  
max\_ptes\_swap 64  
pages\_to\_scan 4096  
scan\_sleep\_millisecs 10000

-----  
19. OS release  
From /etc/\*-release /etc/\*-version  
os-release Red Hat Enterprise Linux 9.4 (Plow)  
redhat-release Red Hat Enterprise Linux release 9.4 (Plow)  
system-release Red Hat Enterprise Linux release 9.4 (Plow)

-----  
20. Disk information  
SPEC is set to: /home/cpu2017-1.1.9-amd-aocc500\_znver5\_A1.2  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda4 xfs 372G 107G 265G 29% /home

-----  
21. /sys/devices/virtual/dmi/id  
Vendor: Lenovo  
Product: ThinkSystem SR635V3  
Product Family: ThinkSystem  
Serial: 1234567890

-----  
22. dmidecode  
Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
5x Samsung M321R8GA0PB2-CCPKC 64 GB 2 rank 6400, configured at 6000  
5x Samsung M321R8GA0PB2-CCPPC 64 GB 2 rank 6400, configured at 6000  
2x Samsung M321R8GA0PB2-CCPWC 64 GB 2 rank 6400, configured at 6000

-----  
23. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Lenovo  
BIOS Version: KAE131I-5.30  
BIOS Date: 12/17/2024  
BIOS Revision: 5.30  
Firmware Revision: 54.6



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR635 V3  
(2.40 GHz, AMD EPYC 9745)

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

CPU2017 License: 9017

Test Date: Mar-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Compiler Version Notes

```
=====
C      | 502.gcc_r(peak)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----
```

```
=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----
```

```
=====
C      | 502.gcc_r(peak)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----
```

```
=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----
```

```
=====
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
      | 541.leela_r(base, peak)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----
```

```
=====
Fortran | 548.exchange2_r(base, peak)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----
```



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR635 V3  
(2.40 GHz, AMD EPYC 9745)

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

CPU2017 License: 9017

Test Date: Mar-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather  
-Wl,-mllvm -Wl,-extra-inliner -z muldefs -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang  
-lamdalloc-ext -ldl

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-do-block-reorder=advanced -z muldefs -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto -mllvm -unroll-threshold=100  
-mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -zopt -fno-PIE -no-pie

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR635 V3  
(2.40 GHz, AMD EPYC 9745)

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

CPU2017 License: 9017

Test Date: Mar-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-fvirtual-function-elimination -fvisibility=hidden  
-mllvm -do-block-reorder=advanced -lamdlibm -lflang -lamdalloc-ext  
-ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop  
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto  
-fepilog-vectorization-of-inductions -mllvm -optimize-strided-mem-cost  
-floop-transform -mllvm -unroll-aggressive -mllvm -unroll-threshold=500  
-lamdlibm -lflang -lamdalloc -ldl
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR635 V3  
(2.40 GHz, AMD EPYC 9745)

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

CPU2017 License: 9017

Test Date: Mar-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

500.perlbench\_r: basepeak = yes  
  
502.gcc\_r: -m32 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand  
-fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner  
-z muldefs -Ofast -march=znver5 -fveclib=AMDLIBM  
-ffast-math -fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline  
-lamdalloc  
  
505.mcf\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7  
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lflang -lamdalloc-ext -ldl

525.x264\_r: basepeak = yes

557.xz\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-ldist-scalar-expand  
-fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner  
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR635 V3  
(2.40 GHz, AMD EPYC 9745)

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

CPU2017 License: 9017

Test Date: Mar-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Peak Optimization Flags (Continued)

557.xz\_r (continued):

```
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc-ext -ldl
```

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: -m64 -std=c++14

```
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fsto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lflang
-lamdalloc-ext -ldl
```

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

## Peak Other Flags

C benchmarks (except as noted below):

-Wno-unused-command-line-argument

502.gcc\_r: -L/usr/lib32 -Wno-unused-command-line-argument

-L/home/work/cpu2017/v119/aocc5/1316/amd\_rate\_aocc500\_znver5\_A\_lib/lib32

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR635 V3  
(2.40 GHz, AMD EPYC 9745)

SPECrate®2017\_int\_base = 1150

SPECrate®2017\_int\_peak = 1180

CPU2017 License: 9017

Test Date: Mar-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-E.html>  
<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-E.xml>  
<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2025-03-15 04:16:04-0400.

Report generated on 2025-04-09 14:59:17 by CPU2017 PDF formatter v6716.

Originally published on 2025-04-09.