



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

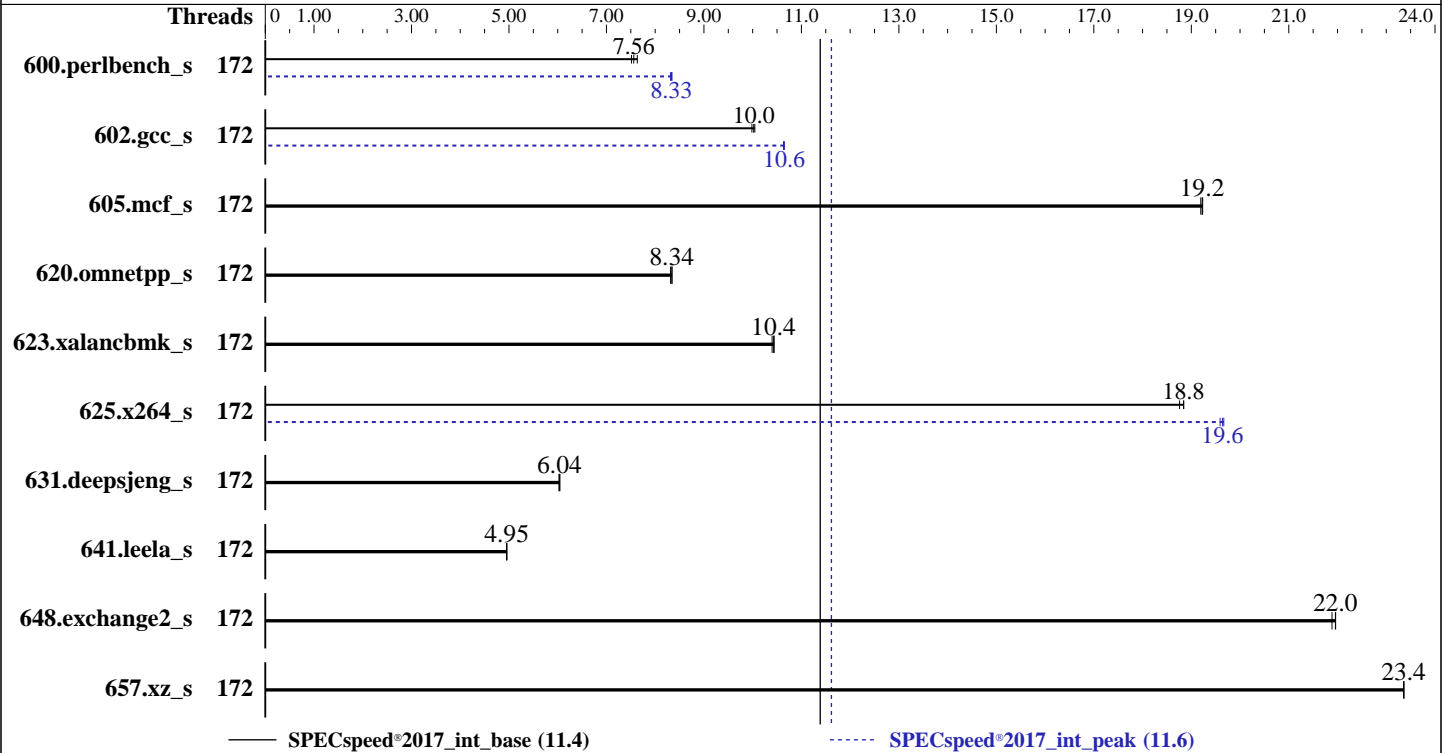
WIO SuperServer SYS-112B-WR
(X14SBW-F , Intel Xeon 6787P)

SPECspeed®2017_int_base = 11.4

SPECspeed®2017_int_peak = 11.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jan-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024



Hardware

CPU Name: Intel Xeon 6787P
Max MHz: 3800
Nominal: 2000
Enabled: 86 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 64 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 336 MB I+D on chip per chip
Other: None
Memory: 512 GB (8 x 64 GB 2Rx4 PC5-8800B-R, running at 8000)
Storage: 1 x 256 GB NVMe SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6
Kernel 6.4.0-150600.21-default
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel: Yes
Firmware: Version 1.2 released Jan-2025
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

WIO SuperServer SYS-112B-WR
(X14SBW-F , Intel Xeon 6787P)

SPECspeed®2017_int_base = 11.4

SPECspeed®2017_int_peak = 11.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jan-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	172	236	7.51	<u>235</u>	<u>7.56</u>	233	7.63	172	213	8.35	<u>213</u>	<u>8.33</u>	214	8.31
602.gcc_s	172	396	10.0	<u>397</u>	<u>10.0</u>	399	9.98	172	<u>374</u>	<u>10.6</u>	374	10.6	375	10.6
605.mcf_s	172	246	19.2	<u>246</u>	<u>19.2</u>	245	19.2	172	246	19.2	<u>246</u>	<u>19.2</u>	245	19.2
620.omnetpp_s	172	195	8.35	196	8.32	<u>196</u>	<u>8.34</u>	172	195	8.35	196	8.32	<u>196</u>	<u>8.34</u>
623.xalancbmk_s	172	<u>136</u>	<u>10.4</u>	136	10.4	136	10.4	172	<u>136</u>	<u>10.4</u>	136	10.4	136	10.4
625.x264_s	172	93.6	18.8	<u>93.6</u>	<u>18.8</u>	94.0	18.8	172	89.7	19.7	<u>89.8</u>	<u>19.6</u>	90.0	19.6
631.deepsjeng_s	172	237	6.04	238	6.03	<u>237</u>	<u>6.04</u>	172	237	6.04	238	6.03	<u>237</u>	<u>6.04</u>
641.leela_s	172	<u>344</u>	<u>4.95</u>	344	4.95	344	4.95	172	<u>344</u>	<u>4.95</u>	344	4.95	344	4.95
648.exchange2_s	172	134	22.0	<u>134</u>	<u>22.0</u>	134	21.9	172	134	22.0	<u>134</u>	<u>22.0</u>	134	21.9
657.xz_s	172	<u>265</u>	<u>23.4</u>	265	23.4	265	23.4	172	<u>265</u>	<u>23.4</u>	265	23.4	265	23.4

SPECspeed®2017_int_base = 11.4

SPECspeed®2017_int_peak = 11.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

WIO SuperServer SYS-112B-WR
(X14SBW-F , Intel Xeon 6787P)

SPECspeed®2017_int_base = 11.4

SPECspeed®2017_int_peak = 11.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jan-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024

Platform Notes

BIOS Configuration:
Workload Profile = HPC
SNC = Enable
LLC Dead Line Alloc = Disable
KTI Prefetch = Enable
Stale AtoS = Disable
Patrol Scrub = Disable

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Tue Jan 28 19:54:17 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

```
1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
19:54:17 up 19:09, 1 user, load average: 15.64, 72.06, 119.48
USER      TTY      FROM          LOGIN@      IDLE        JCPU   PCPU WHAT
root     tty1      -              01:16      18:35m    1.02s   0.00s -bash
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

WIO SuperServer SYS-112B-WR
(X14SBW-F, Intel Xeon 6787P)

SPECspeed®2017_int_base = 11.4

SPECspeed®2017_int_peak = 11.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jan-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024

Platform Notes (Continued)

```

data seg size      (kbytes, -d) unlimited
scheduling priority (-e) 0
file size          (blocks, -f) unlimited
pending signals    (-i) 2062077
max locked memory  (kbytes, -l) 8192
max memory size    (kbytes, -m) unlimited
open files         (-n) 1024
pipe size          (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size         (kbytes, -s) unlimited
cpu time           (seconds, -t) unlimited
max user processes (-u) 2062077
virtual memory     (kbytes, -v) unlimited
file locks         (-x) unlimited

```

```

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags -c
  ic2024.1-lin-core-avx512-speed-20240308.cfg --define cores=86 --tune base,peak -o all --define
  intspeedaffinity --define smt-on --define drop_caches intspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
  ic2024.1-lin-core-avx512-speed-20240308.cfg --define cores=86 --tune base,peak --output_format all
  --define intspeedaffinity --define smt-on --define drop_caches --nopower --runmode speed --tune base:peak
  --size refspeed intspeed --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.007/templogs/preenv.intspeed.007.0.log --lognum 007.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

```

```

-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) 6787P
vendor_id      : GenuineIntel
cpu family     : 6
model          : 173
stepping       : 1
microcode      : 0x1000380
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores      : 86
siblings       : 172
1 physical ids (chips)
172 processors (hardware threads)
physical id 0: core ids 0-42,64-106
physical id 0: apicids 0-85,128-213
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

```

-----
7. lscpu

From lscpu from util-linux 2.39.3:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                172

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

WIO SuperServer SYS-112B-WR
(X14SBW-F, Intel Xeon 6787P)

SPECspeed®2017_int_base = 11.4

SPECspeed®2017_int_peak = 11.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jan-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024

Platform Notes (Continued)

```

On-line CPU(s) list:          0-171
Vendor ID:                   GenuineIntel
BIOS Vendor ID:              Intel(R) Corporation
Model name:                   Intel(R) Xeon(R) 6787P
BIOS Model name:              Intel(R) Xeon(R) 6787P  CPU @ 2.0GHz
BIOS CPU family:              179
CPU family:                   6
Model:                        173
Thread(s) per core:           2
Core(s) per socket:           86
Socket(s):                     1
Stepping:                     1
CPU(s) scaling MHz:           26%
CPU max MHz:                  3800.0000
CPU min MHz:                  800.0000
BogoMIPS:                     4000.00
Flags:                        fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                             pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                             pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                             nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
                             pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
                             xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
                             tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                             3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
                             ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
                             vpid ept_ad fsgsbase tsc_adjust bmil hle avx2 smep bmi2 erms invpcid
                             rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
                             clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                             xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                             split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
                             arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req vnni avx512vbmi
                             umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
                             avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                             cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk
                             pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_l1d
                             arch_capabilities
Virtualization:               VT-x
L1d cache:                    4 MiB (86 instances)
L1i cache:                    5.4 MiB (86 instances)
L2 cache:                     172 MiB (86 instances)
L3 cache:                     336 MiB (1 instance)
NUMA node(s):                 2
NUMA node0 CPU(s):            0-42,86-128
NUMA node1 CPU(s):            43-85,129-171
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:  Not affected
Vulnerability L1tf:           Not affected
Vulnerability Mds:             Not affected
Vulnerability Meltdown:       Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed:       Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:      Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:      Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
                             PBRSE-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds:           Not affected
Vulnerability Tsx async abort: Not affected

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

WIO SuperServer SYS-112B-WR
(X14SBW-F , Intel Xeon 6787P)

SPECspeed®2017_int_base = 11.4

SPECspeed®2017_int_peak = 11.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jan-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024

Platform Notes (Continued)

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	4M	12	Data	1	64	1	64
L1i	64K	5.4M	16	Instruction	1	64	1	64
L2	2M	172M	16	Unified	2	2048	1	64
L3	336M	336M	16	Unified	3	344064	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0-42,86-128
node 0 size: 257603 MB
node 0 free: 206221 MB
node 1 cpus: 43-85,129-171
node 1 size: 257944 MB
node 1 free: 212351 MB
node distances:
node  0  1
  0: 10 12
  1: 12 10

```

9. /proc/meminfo

MemTotal: 527921704 kB

10. who -r

run-level 3 Jan 28 00:46

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

```

Default Target Status
multi-user      running

```

12. Services, from systemctl list-unit-files

```

STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron
display-manager getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nscd
nvmeefc-boot-connections nvme-autoconnect postfix purge-kernels rollback rsyslog smartd
sshd systemd-pstore tuned wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled accounts-daemon autofs autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl
ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables
exchange-bmc-os-info firewallld fsidd gpm grub2-once haveged ipmi ipmievd
issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb
ostree-remount rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@
smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd systemd-boot-check-no-failures
systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync
systemd-timesyncd udisks2 update-system-flatpaks upower vncserver@
generated jexec
indirect systemd-userdbd wickedd

```

13. Linux kernel boot-time arguments, from /proc/cmdline

```

BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=61c5e10f-e2b6-4af8-baf4-1fad022c671
splash=silent
mitigations=auto

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

WIO SuperServer SYS-112B-WR
(X14SBW-F , Intel Xeon 6787P)

SPECspeed®2017_int_base = 11.4

SPECspeed®2017_int_peak = 11.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jan-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024

Platform Notes (Continued)

```
quiet
security=apparmor
```

```
-----
14. cpupower frequency-info
analyzing CPU 95:
  current policy: frequency should be within 800 MHz and 3.80 GHz.
                   The governor "performance" may decide which speed to use
                   within this range.
  boost state support:
    Supported: yes
    Active: yes
```

```
-----
15. tuned-adm active
  Current active profile: throughput-performance
```

```
-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space     2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   10
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0
```

```
-----
17. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force
```

```
-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

```
-----
19. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP6
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

WIO SuperServer SYS-112B-WR
(X14SBW-F, Intel Xeon 6787P)

SPECspeed®2017_int_base = 11.4

SPECspeed®2017_int_peak = 11.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jan-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024

Platform Notes (Continued)

20. Disk information

SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p2 btrfs 236G 125G 109G 54% /home

21. /sys/devices/virtual/dmi/id

Vendor: Supermicro
Product: Super Server
Product Family: Family
Serial: 0123456789

22. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
2x Micron Technology MTC40F2046S1HC88XD1 WCCCC 64 GB 2 rank 8800, configured at 8000
6x Micron Technology MTC40F2046S1HC88XD1 WFFFG 64 GB 2 rank 8800, configured at 8000

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 1.2
BIOS Date: 01/24/2025
BIOS Revision: 5.35

Compiler Version Notes

=====
C | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
| 657.xz_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
C++ | 620.omnetpp_s(base, peak) 623.xalancbnk_s(base, peak) 631.deepsjeng_s(base, peak)
| 641.leela_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
Fortran | 648.exchange2_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

WIO SuperServer SYS-112B-WR
(X14SBW-F, Intel Xeon 6787P)

SPECspeed®2017_int_base = 11.4

SPECspeed®2017_int_peak = 11.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jan-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

WIO SuperServer SYS-112B-WR
(X14SBW-F , Intel Xeon 6787P)

SPECspeed®2017_int_base = 11.4

SPECspeed®2017_int_peak = 11.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jan-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```

600.perlbench_s: -w -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fiopenmp -DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -w -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fiopenmp -DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

WIO SuperServer SYS-112B-WR
(X14SBW-F , Intel Xeon 6787P)

SPECspeed®2017_int_base = 11.4

SPECspeed®2017_int_peak = 11.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jan-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024

Peak Optimization Flags (Continued)

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-GNR-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-GNR-revB.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-01-28 06:54:17-0500.
Report generated on 2025-02-25 19:09:03 by CPU2017 PDF formatter v6716.
Originally published on 2025-02-25.