



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Cisco Systems

Cisco UCS X215c M8 (AMD EPYC 9734  
2.20 GHz Processor)

SPECrate®2017\_int\_base = 1780

SPECrate®2017\_int\_peak = 1840

CPU2017 License: 9019

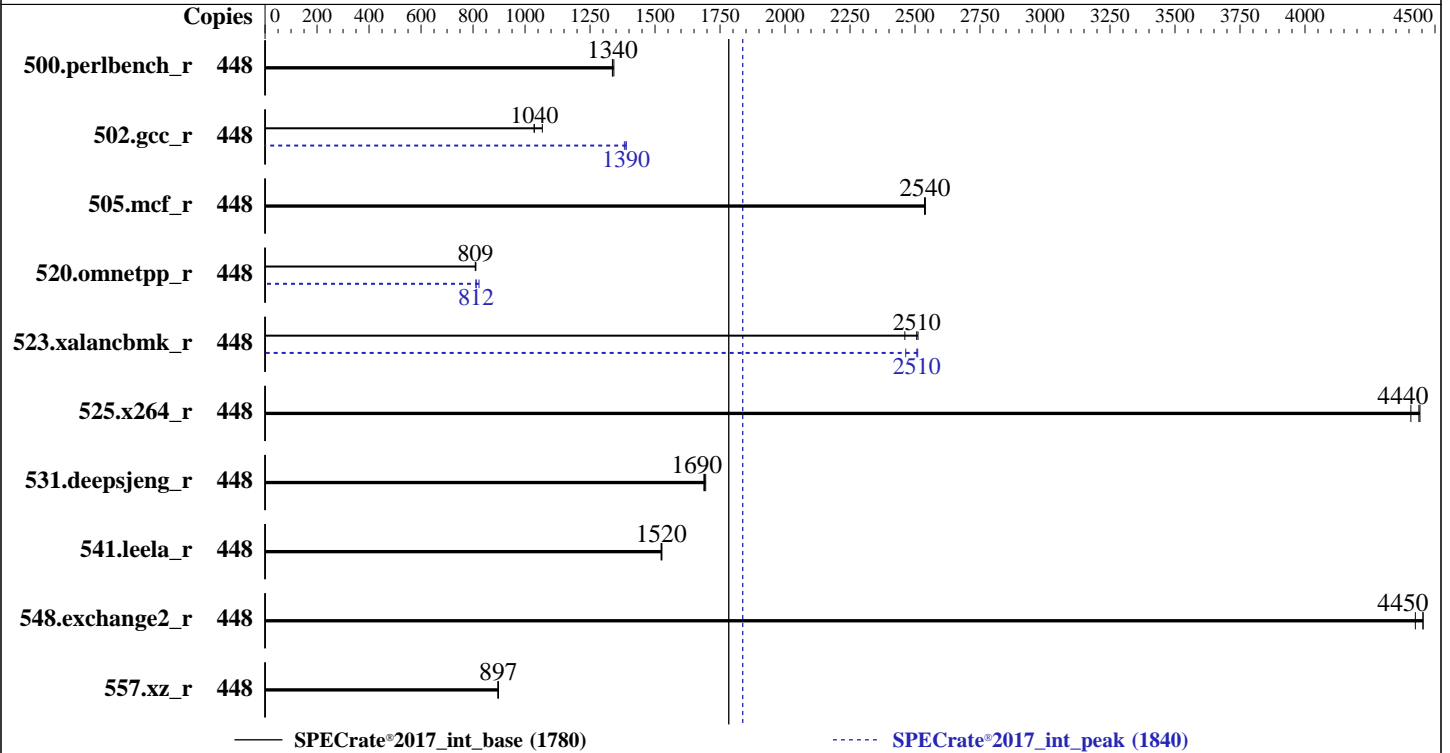
Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jan-2025

Hardware Availability: Oct-2024

Software Availability: Sep-2024



### Hardware

CPU Name: AMD EPYC 9734  
 Max MHz: 3000  
 Nominal: 2200  
 Enabled: 224 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 256 MB I+D on chip per chip, 16 MB shared / 7 cores  
 Other: None  
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-5600B-R, running at 4800)  
 Storage: 1 x 960 GB NVMe SSD  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP6  
 kernel version 6.4.0-150600.21-default  
 C/C++/Fortran: Version 5.0.0 of AOCC  
 Compiler: No  
 Parallel: Version 4.3.5a released Sep-2024  
 Firmware: btrfs  
 File System: Run level 3 (multi-user)  
 System State: 64-bit  
 Base Pointers: 32/64-bit  
 Peak Pointers: None  
 Other: BIOS and OS set to prefer performance at the cost of additional power usage.  
 Power Management:



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	448	532	1340	<b>534</b>	<b>1340</b>	534	1340	448	532	1340	<b>534</b>	<b>1340</b>	534	1340
502.gcc_r	448	595	1070	613	1030	<b>612</b>	<b>1040</b>	448	457	1390	459	1380	<b>457</b>	<b>1390</b>
505.mcf_r	448	285	2540	285	2540	<b>285</b>	<b>2540</b>	448	285	2540	285	2540	<b>285</b>	<b>2540</b>
520.omnetpp_r	448	<b>727</b>	<b>809</b>	727	809	725	811	448	714	823	<b>724</b>	<b>812</b>	725	811
523.xalancbmk_r	448	192	2460	<b>189</b>	<b>2510</b>	188	2510	448	192	2460	188	2510	<b>189</b>	<b>2510</b>
525.x264_r	448	177	4440	<b>177</b>	<b>4440</b>	178	4410	448	177	4440	<b>177</b>	<b>4440</b>	178	4410
531.deepsjeng_r	448	303	1690	<b>304</b>	<b>1690</b>	304	1690	448	303	1690	<b>304</b>	<b>1690</b>	304	1690
541.leela_r	448	486	1530	487	1520	<b>487</b>	<b>1520</b>	448	486	1530	487	1520	<b>487</b>	<b>1520</b>
548.exchange2_r	448	<b>264</b>	<b>4450</b>	264	4450	265	4420	448	<b>264</b>	<b>4450</b>	264	4450	265	4420
557.xz_r	448	540	895	539	898	<b>539</b>	<b>897</b>	448	540	895	539	898	<b>539</b>	<b>897</b>

SPECrate®2017\_int\_base = **1780**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) only on request for base runs,  
'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled' run as root.  
To enable THP for all allocations for peak runs,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.



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## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

Environment variables set by runcpu during the 523.xalancbmk\_r peak run:

```
MALLOC_CONF = "thp:always"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS settings:

NUMA nodes per socket set to NPS4

Determinism Slider set to Power

DF C-States set to Disabled

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Tue Jan 28 04:51:00 2025
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information

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### Platform Notes (Continued)

20. /sys/devices/virtual/dmi/id  
21. dmidecode  
22. BIOS

1. uname -a  
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT\_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36cle09)  
x86\_64 x86\_64 x86\_64 GNU/Linux

2. w  
04:51:00 up 3 min, 2 users, load average: 0.28, 0.12, 0.04  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root ttyl - 04:49 36.00s 1.75s 0.20s /bin/bash ./amd\_rate\_aocc500\_znver5\_A1.sh

3. Username  
From environment variable \$USER: root

4. ulimit -a  
core file size (blocks, -c) unlimited  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (-i) 6190425  
max locked memory (kbytes, -l) 2097152  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 6190425  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited

5. sysinfo process ancestry  
/usr/lib/systemd/systemd --switched-root --system --deserialize=42  
login -- root  
-bash  
python3 ./run\_amd\_rate\_aocc500\_znver5\_A1.py -b intrate  
/bin/bash ./amd\_rate\_aocc500\_znver5\_A1.sh  
runcpu --config amd\_rate\_aocc500\_znver5\_A1.cfg --tune all --reportable --iterations 3 intrate  
runcpu --configfile amd\_rate\_aocc500\_znver5\_A1.cfg --tune all --reportable --iterations 3 --nopower  
--runmode rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile  
\$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from\_runcpu 2  
specperl \$SPEC/bin/sysinfo  
\$SPEC = /home/cpu2017

6. /proc/cpuinfo  
model name : AMD EPYC 9734 112-Core Processor  
vendor\_id : AuthenticAMD  
cpu family : 25  
model : 160  
stepping : 2

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### Platform Notes (Continued)

```

microcode      : 0xaa00215
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
TLB size      : 3584 4K pages
cpu cores     : 112
siblings      : 224
2 physical ids (chips)
448 processors (hardware threads)
physical id 0: core ids
0-6,8-14,16-22,24-30,32-38,40-46,48-54,56-62,64-70,72-78,80-86,88-94,96-102,104-110,112-118,120-126
physical id 1: core ids
0-6,8-14,16-22,24-30,32-38,40-46,48-54,56-62,64-70,72-78,80-86,88-94,96-102,104-110,112-118,120-126
physical id 0: apicids
0-13,16-29,32-45,48-61,64-77,80-93,96-109,112-125,128-141,144-157,160-173,176-189,192-205,208-221,224-237,240-253
physical id 1: apicids
256-269,272-285,288-301,304-317,320-333,336-349,352-365,368-381,384-397,400-413,416-429,432-445,448-461,464-477,480-493,496-509

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

#### 7. lscpu

From lscpu from util-linux 2.39.3:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                448
On-line CPU(s) list:  0-447
Vendor ID:             AuthenticAMD
BIOS Vendor ID:       Advanced Micro Devices, Inc.
Model name:            AMD EPYC 9734 112-Core Processor
BIOS Model name:      AMD EPYC 9734 112-Core Processor
BIOS CPU family:      107
CPU family:            25
Model:                 160
Thread(s) per core:   2
Core(s) per socket:   112
Socket(s):             2
Stepping:              2
Frequency boost:      enabled
CPU(s) scaling MHz:   74%
CPU max MHz:          2999.2180
CPU min MHz:          1500.0000
BogoMIPS:              4393.19
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase bmi1 avx2 smep bmi2
erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
user_shstk avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd
amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean

```

(Continued on next page)



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### Platform Notes (Continued)

```
flushbyasid decodeassists pausefilter pfthreshold avic
v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pku
ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_l1d
debug_swap
```

#### Virtualization:

```
AMD-V
L1d cache: 7 MiB (224 instances)
L1i cache: 7 MiB (224 instances)
L2 cache: 224 MiB (224 instances)
L3 cache: 512 MiB (32 instances)
NUMA node(s): 8
NUMA node0 CPU(s): 0-27,224-251
NUMA node1 CPU(s): 28-55,252-279
NUMA node2 CPU(s): 56-83,280-307
NUMA node3 CPU(s): 84-111,308-335
NUMA node4 CPU(s): 112-139,336-363
NUMA node5 CPU(s): 140-167,364-391
NUMA node6 CPU(s): 168-195,392-419
NUMA node7 CPU(s): 196-223,420-447
```

```
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Mitigation; Safe RET
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
always-on; RSB filling; PBRBS-eIBRS Not affected; BHI Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected
```

#### From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	7M	8	Data	1	64	1	64
L1i	32K	7M	8	Instruction	1	64	1	64
L2	1M	224M	8	Unified	2	2048	1	64
L3	16M	512M	16	Unified	3	16384	1	64

#### 8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 8 nodes (0-7)
node 0 cpus: 0-27,224-251
node 0 size: 193154 MB
node 0 free: 192522 MB
node 1 cpus: 28-55,252-279
node 1 size: 193482 MB
node 1 free: 192952 MB
node 2 cpus: 56-83,280-307
node 2 size: 193521 MB
node 2 free: 192980 MB
node 3 cpus: 84-111,308-335
node 3 size: 193521 MB
node 3 free: 192996 MB
node 4 cpus: 112-139,336-363
node 4 size: 193521 MB
```

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### Platform Notes (Continued)

```

node 4 free: 192926 MB
node 5 cpus: 140-167,364-391
node 5 size: 193521 MB
node 5 free: 192963 MB
node 6 cpus: 168-195,392-419
node 6 size: 193521 MB
node 6 free: 192912 MB
node 7 cpus: 196-223,420-447
node 7 size: 193389 MB
node 7 free: 192866 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10 12 12 12 32 32 32 32
1:  12 10 12 12 32 32 32 32
2:  12 12 10 12 32 32 32 32
3:  12 12 12 10 32 32 32 32
4:  32 32 32 32 10 12 12 12
5:  32 32 32 32 12 10 12 12
6:  32 32 32 32 12 12 10 12
7:  32 32 32 32 12 12 12 10

```

```

9. /proc/meminfo
MemTotal:      1584776240 kB

```

```

10. who -r
run-level 3 Jan 28 04:47

```

```

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
Default Target Status
multi-user      running

```

```

12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance iscsi
issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections
nvmf-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore
virtqemu-wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info
firewalld fsidd gpm grub2-once haveged hv_fcopy_daemon hv_kv_daemon hv_vss_daemon
hwloc-dump-hwdata ipmi ipmievd iscsi-init iscsid issue-add-ssh-keys kexec-load ksm
kvm_stat libvirt-guests lunmask man-db-create multipathd nfs nfs-blkmap nfs-server
nfs-server rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd-generate_opts
snmpd snmptrapd strongswan strongswan-starter svnserv systemd-boot-check-no-failures
systemd-confext systemd-network-generator systemd-nsd systemd-sysext
systemd-time-wait-sync systemd-timesyncd tcsd udisks2 virtinterfaced virtlockd virtlogd
virtnetworkd virtnodedevd virtwfilterd virtsecretd virtstoraged
indirect pcsd systemd-userdbd tftpd wickedd

```

```

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=5d106d92-ac35-417e-b9aa-58e20fc2854f
splash=silent
mitigations=auto

```

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## Platform Notes (Continued)

```
quiet
security=apparmor
```

```
-----
14. cpupower frequency-info
analyzing CPU 27:
  current policy: frequency should be within 1.50 GHz and 2.20 GHz.
                   The governor "performance" may decide which speed to use
                   within this range.
  boost state support:
    Supported: yes
    Active: yes
```

```
-----
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space     0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio     10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs  500
vm.dirtytime_expire_seconds   43200
vm.extfrag_threshold          500
vm.min_unmapped_ratio         1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy     0
vm.nr_overcommit_hugepages    0
vm.swappiness                  1
vm.watermark_boost_factor     15000
vm.watermark_scale_factor     10
vm.zone_reclaim_mode          1
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag          [always] defer defer+madvise madvise never
enabled         [always] madvise never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap           64
pages_to_scan          4096
scan_sleep_millisecs  10000
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
```

```
-----
19. Disk information
SPEC is set to: /home/cpu2017
```

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### Platform Notes (Continued)

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/nvme0n1p2	btrfs	371G	11G	356G	3%	/home

```

20. /sys/devices/virtual/dmi/id
Vendor:      Cisco Systems Inc
Product:     UCSX-215C-M8
Serial:      FCH282172B4

```

```

21. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
15x 0xCE00 M321R8GA0PB0-CWMCH 64 GB 2 rank 5600, configured at 4800
2x 0xCE00 M321R8GA0PB0-CWMJH 64 GB 2 rank 5600, configured at 4800
7x 0xCE00 M321R8GA0PB0-CWMKJ 64 GB 2 rank 5600, configured at 4800

```

```

22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      Cisco Systems, Inc.
BIOS Version:     X215M8.4.3.5a.0.0904241153
BIOS Date:        09/04/2024
BIOS Revision:    5.27

```

### Compiler Version Notes

C | 502.gcc\_r(peak)

```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

```

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak) 557.xz\_r(base, peak)

```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

```

C | 502.gcc\_r(peak)

```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

```

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# SPEC CPU®2017 Integer Rate Result

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## Cisco Systems

Cisco UCS X215c M8 (AMD EPYC 9734  
2.20 GHz Processor)

SPECrate®2017\_int\_base = 1780

SPECrate®2017\_int\_peak = 1840

**CPU2017 License:** 9019

**Test Sponsor:** Cisco Systems

**Tested by:** Cisco Systems

**Test Date:** Jan-2025

**Hardware Availability:** Oct-2024

**Software Availability:** Sep-2024

## Compiler Version Notes (Continued)

-----  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
557.xz\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

-----  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
541.leela\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

-----  
Fortran | 548.exchange2\_r(base, peak)  
-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64

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## Base Portability Flags (Continued)

541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-Wl,-mllvm -Wl,-extra-inliner -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc-ext -ldl
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -fno-PIE -no-pie
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lflang -lamdalloc-ext
-ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto
-fepilog-vectorization-of-inductions -mllvm -optimize-strided-mem-cost
-floop-transform -mllvm -unroll-aggressive -mllvm -unroll-threshold=500
-lamdlibm -lflang -lamdalloc -ldl
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

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## Base Other Flags (Continued)

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64

502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64

505.mcf\_r: -DSPEC\_LP64

520.omnetpp\_r: -DSPEC\_LP64

523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64

525.x264\_r: -DSPEC\_LP64

531.deepsjeng\_r: -DSPEC\_LP64

541.leela\_r: -DSPEC\_LP64

548.exchange2\_r: -DSPEC\_LP64

557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

500.perlbench\_r: basepeak = yes

502.gcc\_r: -m32 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand

-fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner

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**Software Availability:** Sep-2024

## Peak Optimization Flags (Continued)

502.gcc\_r (continued):

```
-z muldefs -Ofast -march=znver5 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdalloc
```

505.mcf\_r: basepeak = yes

525.x264\_r: basepeak = yes

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: -m64 -std=c++14

```
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -fno-PIE
-no-pie -fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lamdalloc-ext
-ldl
```

523.xalancbmk\_r: -m64 -std=c++14

```
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lflang
-lamdalloc-ext -ldl
```

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes



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## Peak Other Flags

C benchmarks (except as noted below):

-Wno-unused-command-line-argument

502.gcc\_r: -L/usr/lib32 -Wno-unused-command-line-argument

-L/home/work/cpu2017/v119/aocc5/1316/amd\_rate\_aocc500\_znver5\_A\_lib/lib32

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.html>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-v3-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.xml>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-v3-revA.xml>

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Tested with SPEC CPU®2017 v1.1.9 on 2025-01-28 04:50:59-0500.

Report generated on 2025-02-25 19:07:28 by CPU2017 PDF formatter v6716.

Originally published on 2025-02-25.