



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Kaytus Systems Pte. Ltd.

SPECrate®2017\_int\_base = 248

## KR1280V2 (Intel Xeon Silver 4510)

SPECrate®2017\_int\_peak = 255

CPU2017 License: 6865

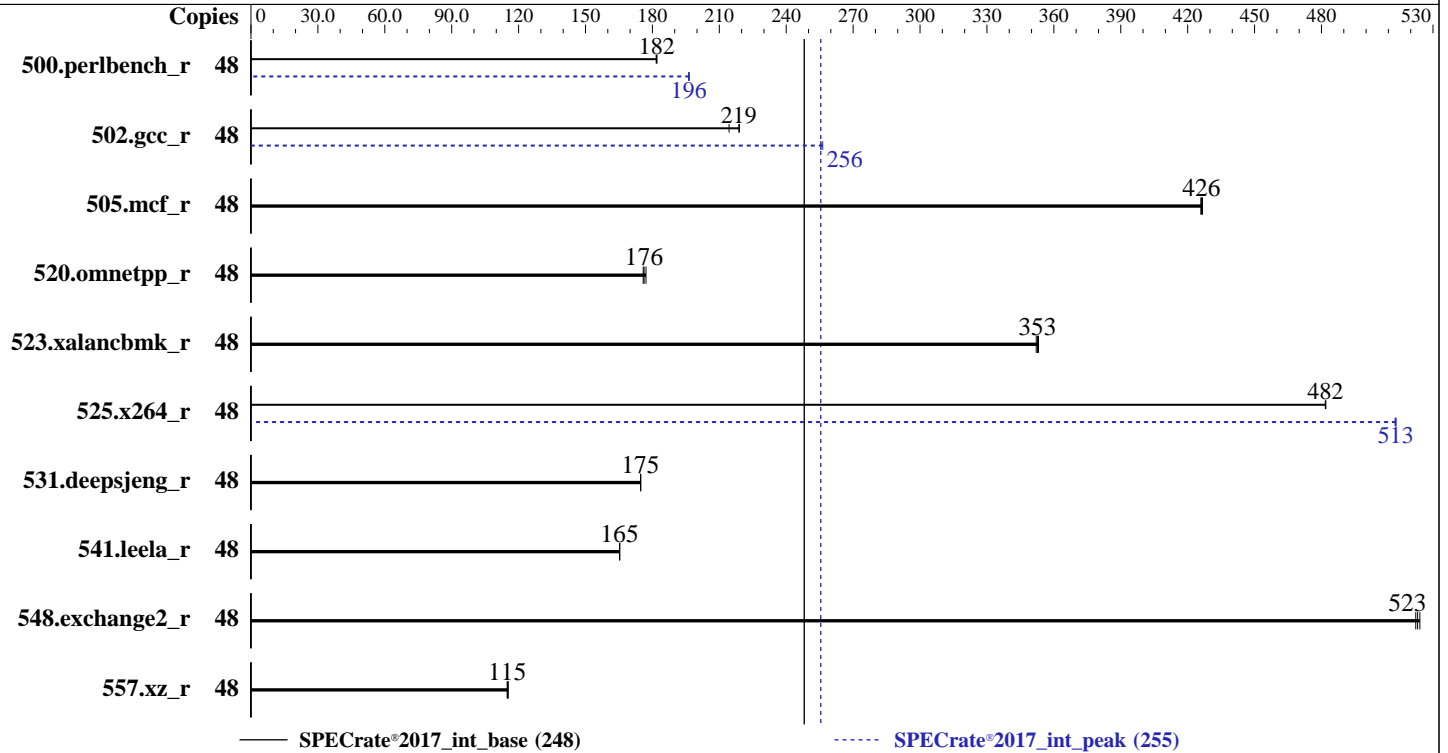
Test Date: Jul-2024

Test Sponsor: Kaytus Systems Pte. Ltd.

Hardware Availability: Dec-2023

Tested by: Kaytus Systems Pte. Ltd.

Software Availability: Mar-2024



### Hardware

CPU Name: Intel Xeon Silver 4510  
 Max MHz: 4100  
 Nominal: 2400  
 Enabled: 24 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 30 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 4400)  
 Storage: 1 x 1.92 TB NVME SSD  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP5  
 5.14.21-150500.53-default  
 Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++  
 Compiler for Linux;  
 Fortran: Version 2024.1 of Intel Fortran Compiler  
 for Linux;  
 Parallel: No  
 Firmware: Version 05.13.00 released Nov-2023  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost  
 of additional power usage.



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Kaytus Systems Pte. Ltd.

SPECrate®2017\_int\_base = 248

## KR1280V2 (Intel Xeon Silver 4510)

SPECrate®2017\_int\_peak = 255

CPU2017 License: 6865

Test Sponsor: Kaytus Systems Pte. Ltd.

Tested by: Kaytus Systems Pte. Ltd.

Test Date: Jul-2024

Hardware Availability: Dec-2023

Software Availability: Mar-2024

### Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	48	420	182	<b><u>420</u></b>	<b><u>182</u></b>	421	182	48	389	197	389	196	<b><u>389</u></b>	<b><u>196</u></b>
502.gcc_r	48	317	214	<b><u>311</u></b>	<b><u>219</u></b>	310	219	48	265	256	<b><u>266</u></b>	<b><u>256</u></b>	266	256
505.mcf_r	48	182	426	<b><u>182</u></b>	<b><u>426</u></b>	182	427	48	182	426	<b><u>182</u></b>	<b><u>426</u></b>	182	427
520.omnetpp_r	48	358	176	<b><u>357</u></b>	<b><u>176</u></b>	356	177	48	358	176	<b><u>357</u></b>	<b><u>176</u></b>	356	177
523.xalancbmk_r	48	144	352	<b><u>144</u></b>	<b><u>353</u></b>	144	353	48	144	352	<b><u>144</u></b>	<b><u>353</u></b>	144	353
525.x264_r	48	174	482	<b><u>174</u></b>	<b><u>482</u></b>	174	482	48	164	513	<b><u>164</u></b>	<b><u>513</u></b>	164	513
531.deepsjeng_r	48	315	175	315	175	<b><u>315</u></b>	<b><u>175</u></b>	48	315	175	315	175	<b><u>315</u></b>	<b><u>175</u></b>
541.leela_r	48	481	165	481	165	<b><u>481</u></b>	<b><u>165</u></b>	48	481	165	481	165	<b><u>481</u></b>	<b><u>165</u></b>
548.exchange2_r	48	240	524	<b><u>240</u></b>	<b><u>523</u></b>	241	522	48	240	524	<b><u>240</u></b>	<b><u>523</u></b>	241	522
557.xz_r	48	451	115	449	115	<b><u>451</u></b>	<b><u>115</u></b>	48	451	115	449	115	<b><u>451</u></b>	<b><u>115</u></b>

SPECrate®2017\_int\_base = 248

SPECrate®2017\_int\_peak = 255

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/lib/ia32:/home/CPU2017/je5.0.1-32"  
MALLOC\_CONF = "retain:true"

### General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Kaytus Systems Pte. Ltd.

SPECrate®2017\_int\_base = 248

## KR1280V2 (Intel Xeon Silver 4510)

SPECrate®2017\_int\_peak = 255

**CPU2017 License:** 6865

**Test Sponsor:** Kaytus Systems Pte. Ltd.

**Tested by:** Kaytus Systems Pte. Ltd.

**Test Date:** Jul-2024

**Hardware Availability:** Dec-2023

**Software Availability:** Mar-2024

### General Notes (Continued)

is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

BIOS configuration:  
ENERGY\_PERF\_BIAS\_CFG mode set to Performance  
Hardware Prefetch set to Disable  
VT Support set to Disable  
Sub NUMA Cluster (SNC) set to SNC2

Sysinfo program /home/CPU2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Tue Jul 9 12:10:13 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

-----

1. uname -a  
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT\_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)  
x86\_64 x86\_64 x86\_64 GNU/Linux

-----

2. w  
12:10:13 up 1 min, 1 user, load average: 0.11, 0.06, 0.02  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root tty1 - 12:09 12.00s 1.17s 0.03s sh  
reportable-ic2024.1-lin-sapphirerapids-rate-smt-on-20240308.sh

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Kaytus Systems Pte. Ltd.

SPECrate®2017\_int\_base = 248

## KR1280V2 (Intel Xeon Silver 4510)

SPECrate®2017\_int\_peak = 255

**CPU2017 License:** 6865

**Test Sponsor:** Kaytus Systems Pte. Ltd.

**Tested by:** Kaytus Systems Pte. Ltd.

**Test Date:** Jul-2024

**Hardware Availability:** Dec-2023

**Software Availability:** Mar-2024

### Platform Notes (Continued)

-----  
3. Username

From environment variable \$USER: root

-----  
4. ulimit -a

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 4124641
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 4124641
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

-----  
5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
sh reportable-ic2024.1-lin-sapphirerapids-rate-smt-on-20240308.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=48 -c
  ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=24 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=48 --configfile
  ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=24 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
  --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/CPU2017
```

-----  
6. /proc/cpuinfo

```
model name      : INTEL(R) XEON(R) SILVER 4510
vendor_id       : GenuineIntel
cpu family      : 6
model           : 143
stepping        : 8
microcode       : 0x2b000461
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores       : 12
siblings        : 24
2 physical ids (chips)
48 processors (hardware threads)
physical id 0:  core ids 0-11
physical id 1:  core ids 0-11
physical id 0:  apicids 0-23
physical id 1:  apicids 64-87
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Kaytus Systems Pte. Ltd.

SPECrate®2017\_int\_base = 248

## KR1280V2 (Intel Xeon Silver 4510)

SPECrate®2017\_int\_peak = 255

CPU2017 License: 6865

Test Sponsor: Kaytus Systems Pte. Ltd.

Tested by: Kaytus Systems Pte. Ltd.

Test Date: Jul-2024

Hardware Availability: Dec-2023

Software Availability: Mar-2024

### Platform Notes (Continued)

-----  
7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                48
On-line CPU(s) list:   0-47
Vendor ID:             GenuineIntel
Model name:            INTEL(R) XEON(R) SILVER 4510
CPU family:            6
Model:                 143
Thread(s) per core:    2
Core(s) per socket:    12
Socket(s):             2
Stepping:              8
CPU max MHz:           4100.0000
CPU min MHz:           800.0000
BogoMIPS:              3800.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                        x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                        abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                        intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase
                        tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f
                        avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd
                        sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc
                        cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect avx_vnni
                        avx512_bf16 wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp
                        hwp_pkg_req avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
                        vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
                        bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
                        tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_1ld arch_capabilities
L1d cache:             1.1 MiB (24 instances)
L1i cache:             768 KiB (24 instances)
L2 cache:              48 MiB (24 instances)
L3 cache:              60 MiB (2 instances)
NUMA node(s):         4
NUMA node0 CPU(s):    0-5,24-29
NUMA node1 CPU(s):    6-11,30-35
NUMA node2 CPU(s):    12-17,36-41
NUMA node3 CPU(s):    18-23,42-47
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:    Not affected
Vulnerability Mds:     Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW
                        sequence
Vulnerability Srbds:   Not affected
Vulnerability Tsx async abort: Not affected

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Kaytus Systems Pte. Ltd.

SPECrate®2017\_int\_base = 248

## KR1280V2 (Intel Xeon Silver 4510)

SPECrate®2017\_int\_peak = 255

CPU2017 License: 6865

Test Date: Jul-2024

Test Sponsor: Kaytus Systems Pte. Ltd.

Hardware Availability: Dec-2023

Tested by: Kaytus Systems Pte. Ltd.

Software Availability: Mar-2024

### Platform Notes (Continued)

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.1M	12	Data	1	64	1	64
L1i	32K	768K	8	Instruction	1	64	1	64
L2	2M	48M	16	Unified	2	2048	1	64
L3	30M	60M	15	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0-5,24-29
node 0 size: 257514 MB
node 0 free: 257037 MB
node 1 cpus: 6-11,30-35
node 1 size: 258044 MB
node 1 free: 257679 MB
node 2 cpus: 12-17,36-41
node 2 size: 258044 MB
node 2 free: 257266 MB
node 3 cpus: 18-23,42-47
node 3 size: 257585 MB
node 3 free: 257212 MB
node distances:
node  0  1  2  3
  0: 10 12 21 21
  1: 12 10 21 21
  2: 21 21 10 12
  3: 21 21 12 10

```

9. /proc/meminfo

MemTotal: 1055938616 kB

10. who -r

run-level 3 Jul 9 12:08

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

```

Default Target Status
multi-user      running

```

12. Services, from systemctl list-unit-files

```

STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance
issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections postfix
purge-kernels rollback rsyslog smartd sshd systemd-pstore
enabled-runtime systemd-remount-fs
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
firewalld gpm grub2-once haveged haveged-switch-root ipmi ipmievd issue-add-ssh-keys kdump
kdump-early kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nvme-fc-autoconnect
rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd
systemd-boot-check-no-failures systemd-network-generator systemd-sysext
systemd-time-wait-sync systemd-timesyncd tuned udisks2 vncserver@ wicked wickedd-auto4
wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
indirect wickedd

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Kaytus Systems Pte. Ltd.

SPECrate®2017\_int\_base = 248

## KR1280V2 (Intel Xeon Silver 4510)

SPECrate®2017\_int\_peak = 255

CPU2017 License: 6865

Test Sponsor: Kaytus Systems Pte. Ltd.

Tested by: Kaytus Systems Pte. Ltd.

Test Date: Jul-2024

Hardware Availability: Dec-2023

Software Availability: Mar-2024

### Platform Notes (Continued)

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=0a76fbd6-6541-4f13-928e-2a3caa636f4c
splash=silent
resume=/dev/disk/by-uuid/e6a5bcc3-5591-4cd4-a737-ee6cc3cdc083
mitigations=auto
quiet
security=apparmor
crashkernel=447M,high
crashkernel=72M,low

```

```

-----
14. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 800 MHz and 4.10 GHz.
                   The governor "powersave" may decide which speed to use
                   within this range.
  boost state support:
    Supported: yes
    Active: yes

```

```

-----
15. tuned-adm active
It seems that tuned daemon is not running, preset profile is not activated.
Preset profile: throughput-performance

```

```

-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                  60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000

```

(Continued on next page)





# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Kaytus Systems Pte. Ltd.

SPECrate®2017\_int\_base = 248

## KR1280V2 (Intel Xeon Silver 4510)

SPECrate®2017\_int\_peak = 255

**CPU2017 License:** 6865

**Test Sponsor:** Kaytus Systems Pte. Ltd.

**Tested by:** Kaytus Systems Pte. Ltd.

**Test Date:** Jul-2024

**Hardware Availability:** Dec-2023

**Software Availability:** Mar-2024

### Platform Notes (Continued)

```

defrag                1
max_ptes_none        511
max_ptes_shared      256
max_ptes_swap        64
pages_to_scan        4096
scan_sleep_millisecs 10000

```

```

-----
19. OS release
    From /etc/*-release /etc/*-version
    os-release SUSE Linux Enterprise Server 15 SP5

```

```

-----
20. Disk information
SPEC is set to: /home/CPU2017
Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/nvme0n1p4 xfs   1.3T  233G 1011G  19% /home

```

```

-----
21. /sys/devices/virtual/dmi/id
Vendor:      KAYTUS
Product:     KR1280-X2-A0-R0-00
Product Family: Not specified
Serial:      000000000

```

```

-----
22. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  8x Samsung M321R8GA0PB0-CWMKH 64 GB 2 rank 5600, configured at 4400
  8x Samsung M321R8GA0PB0-CWMXH 64 GB 2 rank 5600, configured at 4400

```

```

-----
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:   American Megatrends International, LLC.
BIOS Version:  05.13.00
BIOS Date:     11/30/2023

```

### Compiler Version Notes

```

=====
C      | 502.gcc_r(peak)

```

```

-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

```

```

=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)

```

```

-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

```

(Continued on next page)





# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Kaytus Systems Pte. Ltd.

SPECrate®2017\_int\_base = 248

KR1280V2 (Intel Xeon Silver 4510)

SPECrate®2017\_int\_peak = 255

CPU2017 License: 6865

Test Date: Jul-2024

Test Sponsor: Kaytus Systems Pte. Ltd.

Hardware Availability: Dec-2023

Tested by: Kaytus Systems Pte. Ltd.

Software Availability: Mar-2024

## Compiler Version Notes (Continued)

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base, peak) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
541.leela\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Fortran | 548.exchange2\_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64

502.gcc\_r: -DSPEC\_LP64

505.mcf\_r: -DSPEC\_LP64

520.omnetpp\_r: -DSPEC\_LP64

523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Kaytus Systems Pte. Ltd.

SPECrate®2017\_int\_base = 248

KR1280V2 (Intel Xeon Silver 4510)

SPECrate®2017\_int\_peak = 255

CPU2017 License: 6865

Test Sponsor: Kaytus Systems Pte. Ltd.

Tested by: Kaytus Systems Pte. Ltd.

Test Date: Jul-2024

Hardware Availability: Dec-2023

Software Availability: Mar-2024

## Base Portability Flags (Continued)

525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto  
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Kaytus Systems Pte. Ltd.

SPECrate®2017\_int\_base = 248

KR1280V2 (Intel Xeon Silver 4510)

SPECrate®2017\_int\_peak = 255

CPU2017 License: 6865

Test Sponsor: Kaytus Systems Pte. Ltd.

Tested by: Kaytus Systems Pte. Ltd.

Test Date: Jul-2024

Hardware Availability: Dec-2023

Software Availability: Mar-2024

## Peak Portability Flags (Continued)

```

505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

```

## Peak Optimization Flags

C benchmarks:

```

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

502.gcc_r: -m32 -L/opt/intel/oneapi/compiler/2024.1/lib32 -std=gnu89
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

```

505.mcf\_r: basepeak = yes

```

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

```

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Kaytus Systems Pte. Ltd.

SPECrate®2017\_int\_base = 248

KR1280V2 (Intel Xeon Silver 4510)

SPECrate®2017\_int\_peak = 255

CPU2017 License: 6865

Test Sponsor: Kaytus Systems Pte. Ltd.

Tested by: Kaytus Systems Pte. Ltd.

Test Date: Jul-2024

Hardware Availability: Dec-2023

Software Availability: Mar-2024

## Peak Optimization Flags (Continued)

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Kaytus-Platform-Settings-intel-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Kaytus-Platform-Settings-intel-V1.0.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2024-07-09 12:10:13-0400.

Report generated on 2024-08-29 10:49:25 by CPU2017 PDF formatter v6716.

Originally published on 2024-08-27.