



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant DL380 Gen11

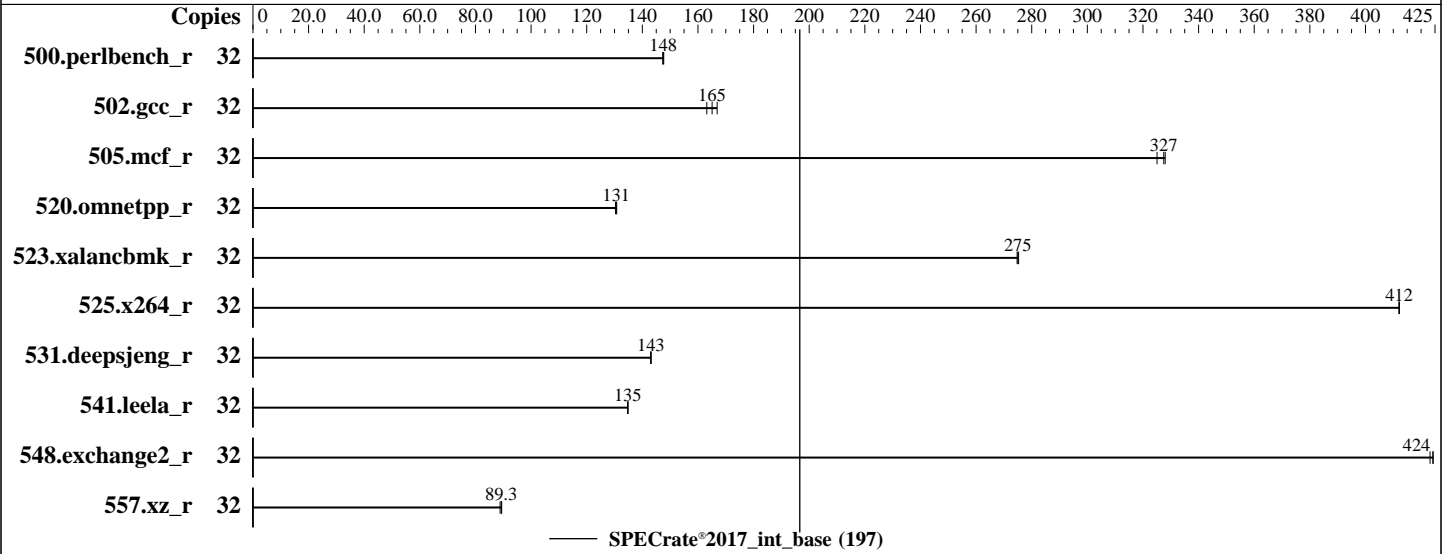
(3.60 GHz, Intel Xeon Gold 6544Y)

SPECrate®2017\_int\_base = 197

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2024  
**Hardware Availability:** Dec-2023  
**Software Availability:** Mar-2024



### Hardware

CPU Name: Intel Xeon Gold 6544Y  
Max MHz: 4100  
Nominal: 3600  
Enabled: 16 cores, 1 chip, 2 threads/core  
Orderable: 1 chip  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 45 MB I+D on chip per chip  
Other: None  
Memory: 256 GB (8 x 32 GB 2Rx8 PC5-5600B-R, running at 5200)  
Storage: 1 x 960 GB SATA SSD  
Other: CPU Cooling: Air

### Software

OS: Red Hat Enterprise Linux 9.2 (Plow)  
5.14.0-284.11.1.el9\_2.x86\_64  
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;  
Parallel: No  
Firmware: HPE BIOS Version v2.22 06/19/2024 released Jun-2024  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: Not Applicable  
Other: None  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL380 Gen11**

(3.60 GHz, Intel Xeon Gold 6544Y)

SPECrate®2017\_int\_base = 197

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2024  
**Hardware Availability:** Dec-2023  
**Software Availability:** Mar-2024

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	32	346	147	345	148	<u>345</u>	<u>148</u>							
502.gcc_r	32	<b>274</b>	<b>165</b>	271	167	278	163							
505.mcf_r	32	<b>158</b>	<b>327</b>	159	325	158	328							
520.omnetpp_r	32	<b>321</b>	<b>131</b>	322	130	321	131							
523.xalancbmk_r	32	123	275	<b>123</b>	<b>275</b>	123	275							
525.x264_r	32	136	412	136	412	<b>136</b>	<b>412</b>							
531.deepsjeng_r	32	256	143	256	143	<b>256</b>	<b>143</b>							
541.leela_r	32	<b>393</b>	<b>135</b>	393	135	393	135							
548.exchange2_r	32	198	424	<b>198</b>	<b>424</b>	198	423							
557.xz_r	32	<b>387</b>	<b>89.3</b>	389	88.9	386	89.4							

SPECrate®2017\_int\_base = 197

SPECrate®2017\_int\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
tuned-adm profile was set to throughput-performance using "tuned-adm profile throughput-performance"
```

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL380 Gen11**

(3.60 GHz, Intel Xeon Gold 6544Y)

SPECrate®2017\_int\_base = 197

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Jul-2024

**Hardware Availability:** Dec-2023

**Software Availability:** Mar-2024

## Platform Notes

BIOS Configuration:

Workload Profile set to General Throughput Compute  
Thermal Configuration set to Maximum Cooling  
Memory Patrol Scrubbing set to Disabled  
Enhanced Processor Performance Profile set to Aggressive  
Workload Profile set to Custom  
Minimum Processor Idle Power Package C-State set to Package C6 (non-retention) State

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Fri Jul 26 04:09:00 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-13.e19\_2)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

-----  
1. uname -a  
Linux localhost.localdomain 5.14.0-284.11.1.el9\_2.x86\_64 #1 SMP PREEMPT\_DYNAMIC Wed Apr 12 10:45:03 EDT 2023 x86\_64 x86\_64 x86\_64 GNU/Linux  
-----

-----  
2. w  
04:09:00 up 10 min, 1 user, load average: 0.07, 0.02, 0.00  
USER TTY LOGIN@ IDLE JCPU PCPU WHAT  
root pts/0 04:00 28.00s 0.00s 0.00s -bash  
-----

-----  
3. Username  
From environment variable \$USER: root  
-----

-----  
4. ulimit -a  
real-time non-blocking time (microseconds, -R) unlimited  
-----

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL380 Gen11**

**(3.60 GHz, Intel Xeon Gold 6544Y)**

SPECrate®2017\_int\_base = 197

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2024  
**Hardware Availability:** Dec-2023  
**Software Availability:** Mar-2024

## Platform Notes (Continued)

```

core file size          (blocks, -c) 0
data seg size          (kbytes, -d) unlimited
scheduling priority    (-e) 0
file size              (blocks, -f) unlimited
pending signals        (-i) 1030594
max locked memory      (kbytes, -l) 8192
max memory size        (kbytes, -m) unlimited
open files             (-n) 1024
pipe size              (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority     (-r) 0
stack size             (kbytes, -s) unlimited
cpu time               (seconds, -t) unlimited
max user processes     (-u) 1030594
virtual memory         (kbytes, -v) unlimited
file locks             (-x) unlimited

```

```

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 31
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
bash -c cd $SPEC/ && $SPEC/intrate.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 -c
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=16 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 --configfile
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=16 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base --nopower --runmode rate --tune base
--size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

```

```

-----
6. /proc/cpuinfo
model name      : INTEL(R) XEON(R) GOLD 6544Y
vendor_id      : GenuineIntel
cpu family     : 6
model          : 207
stepping       : 2
microcode      : 0x21000240
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores      : 16
siblings       : 32
1 physical ids (chips)
32 processors (hardware threads)
physical id 0: core ids 0-15
physical id 0: apicids 0-31
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

```

-----
7. lscpu

From lscpu from util-linux 2.37.4:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          46 bits physical, 57 bits virtual

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant DL380 Gen11

(3.60 GHz, Intel Xeon Gold 6544Y)

SPECrate®2017\_int\_base = 197

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2024  
**Hardware Availability:** Dec-2023  
**Software Availability:** Mar-2024

## Platform Notes (Continued)

```

Byte Order:                Little Endian
CPU(s):                    32
On-line CPU(s) list:      0-31
Vendor ID:                 GenuineIntel
BIOS Vendor ID:          Intel(R) Corporation
Model name:               INTEL(R) XEON(R) GOLD 6544Y
BIOS Model name:         INTEL(R) XEON(R) GOLD 6544Y
CPU family:               6
Model:                    207
Thread(s) per core:      2
Core(s) per socket:      16
Socket(s):                1
Stepping:                 2
BogoMIPS:                 7200.00
Flags:                    fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                          clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                          lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                          nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                          ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                          sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                          lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                          invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                          vmmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2
                          erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                          clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                          xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local avx_vnni
                          avx512_bf16 wbnoinvd dtherm ida arat pln pts hfi avx512vbmi umip pku ospke
                          waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
                          avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                          enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16
                          avx512_fp16 amx_tile amx_int8 flush_lld arch_capabilities
Virtualization:           VT-x
L1d cache:                768 KiB (16 instances)
L1i cache:                512 KiB (16 instances)
L2 cache:                 32 MiB (16 instances)
L3 cache:                 45 MiB (1 instance)
NUMA node(s):            2
NUMA node0 CPU(s):       0-7,16-23
NUMA node1 CPU(s):       8-15,24-31
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:      Not affected
Vulnerability Mds:       Not affected
Vulnerability Meltdown:  Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed:  Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBR SB-eIBRS SW
                          sequence
Vulnerability Srbds:     Not affected
Vulnerability Tsx async abort: Not affected

```

```

From lscpu --cache:
NAME ONE-SIZE ALL-SIZE WAYS TYPE          LEVEL SETS PHY-LINE COHERENCY-SIZE
L1d   48K      768K   12 Data          1     64     1           64
L1i   32K      512K    8 Instruction    1     64     1           64
L2    2M       32M    16 Unified       2  2048     1           64
L3    45M      45M    15 Unified       3 49152     1           64

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL380 Gen11**

(3.60 GHz, Intel Xeon Gold 6544Y)

SPECrate®2017\_int\_base = 197

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Jul-2024

**Hardware Availability:** Dec-2023

**Software Availability:** Mar-2024

## Platform Notes (Continued)

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-7,16-23

node 0 size: 128674 MB

node 0 free: 128104 MB

node 1 cpus: 8-15,24-31

node 1 size: 129015 MB

node 1 free: 128419 MB

node distances:

node 0 1

0: 10 20

1: 20 10

9. /proc/meminfo

MemTotal: 263874908 kB

10. who -r

run-level 3 Jul 26 03:58

11. Systemd service manager version: systemd 252 (252-13.el9\_2)

Default Target Status

multi-user running

12. Services, from systemctl list-unit-files

STATE UNIT FILES

```

enabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd crond
dbus-broker firewalld getty@ insights-client boot irqbalance kdump lvm2-monitor mdmonitor
microcode nis-domainname rhsmcertd rsyslog selinux-autorelabel-mark sshd sssd
systemd-boot-update systemd-network-generator tuned udisks2

enabled-runtime systemd-remount-fs
disabled blk-availability chrony-wait chronyd console-getty cpupower debug-shell dnf-system-upgrade
kvm_stat man-db-restart-cache-update nftables powertop rdisc rhcd rhsm rhsm-facts
rpmdb-rebuild selinux-check-proper-disable serial-getty@ sshd-keygen@
systemd-boot-check-no-failures systemd-pstore systemd-sysext

indirect sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
systemd-sysupdate-reboot

```

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT\_IMAGE=(hd1,gpt2)/vmlinuz-5.14.0-284.11.1.el9\_2.x86\_64

root=/dev/mapper/rhel-root

ro

resume=/dev/mapper/rhel-swap

rd.lvm.lv=rhel/root

rd.lvm.lv=rhel/swap

14. cpupower frequency-info

analyzing CPU 0:

Unable to determine current policy

boost state support:

Supported: yes

Active: yes

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL380 Gen11**

(3.60 GHz, Intel Xeon Gold 6544Y)

SPECrate®2017\_int\_base = 197

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Jul-2024

**Hardware Availability:** Dec-2023

**Software Availability:** Mar-2024

## Platform Notes (Continued)

15. tuned-adm active  
Current active profile: throughput-performance

```

16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space     2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 40
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio         1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                  10
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0

```

```

17. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force

```

```

18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                 1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swapped      64
pages_to_scan         4096
scan_sleep_millisecs 10000

```

```

19. OS release
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.2 (Plow)
redhat-release  Red Hat Enterprise Linux release 9.2 (Plow)
system-release  Red Hat Enterprise Linux release 9.2 (Plow)

```

```

20. Disk information
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   819G  33G  786G   4% /home

```

```

21. /sys/devices/virtual/dmi/id
Vendor:      HPE
Product:     ProLiant DL380 Gen11
Product Family: ProLiant

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL380 Gen11**

(3.60 GHz, Intel Xeon Gold 6544Y)

SPECrate®2017\_int\_base = 197

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Jul-2024

**Hardware Availability:** Dec-2023

**Software Availability:** Mar-2024

## Platform Notes (Continued)

Serial: CNX21000G7

### 22. dmidecode

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

8x Hynix HMC88AGBRA193N 32 GB 2 rank 5600, configured at 5200

### 23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE  
BIOS Version: 2.22  
BIOS Date: 06/19/2024  
BIOS Revision: 2.22  
Firmware Revision: 1.54

## Compiler Version Notes

=====  
C | 500.perlbench\_r(base) 502.gcc\_r(base) 505.mcf\_r(base) 525.x264\_r(base) 557.xz\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====  
C++ | 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base) 541.leela\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====  
Fortran | 548.exchange2\_r(base)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx





# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380 Gen11

(3.60 GHz, Intel Xeon Gold 6544Y)

SPECrate®2017\_int\_base = 197

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2024

Hardware Availability: Dec-2023

Software Availability: Mar-2024

## Base Portability Flags

```

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

```

## Base Optimization Flags

C benchmarks:

```

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

```

C++ benchmarks:

```

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

```

Fortran benchmarks:

```

-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2024-07-25 18:39:00-0400.

Report generated on 2024-08-14 14:03:56 by CPU2017 PDF formatter v6716.

Originally published on 2024-08-13.