



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST50 V3  
(3.50 GHz, Intel Xeon E-2486)

SPECrate®2017\_fp\_base = 97.0

SPECrate®2017\_fp\_peak = 97.3

CPU2017 License: 9017

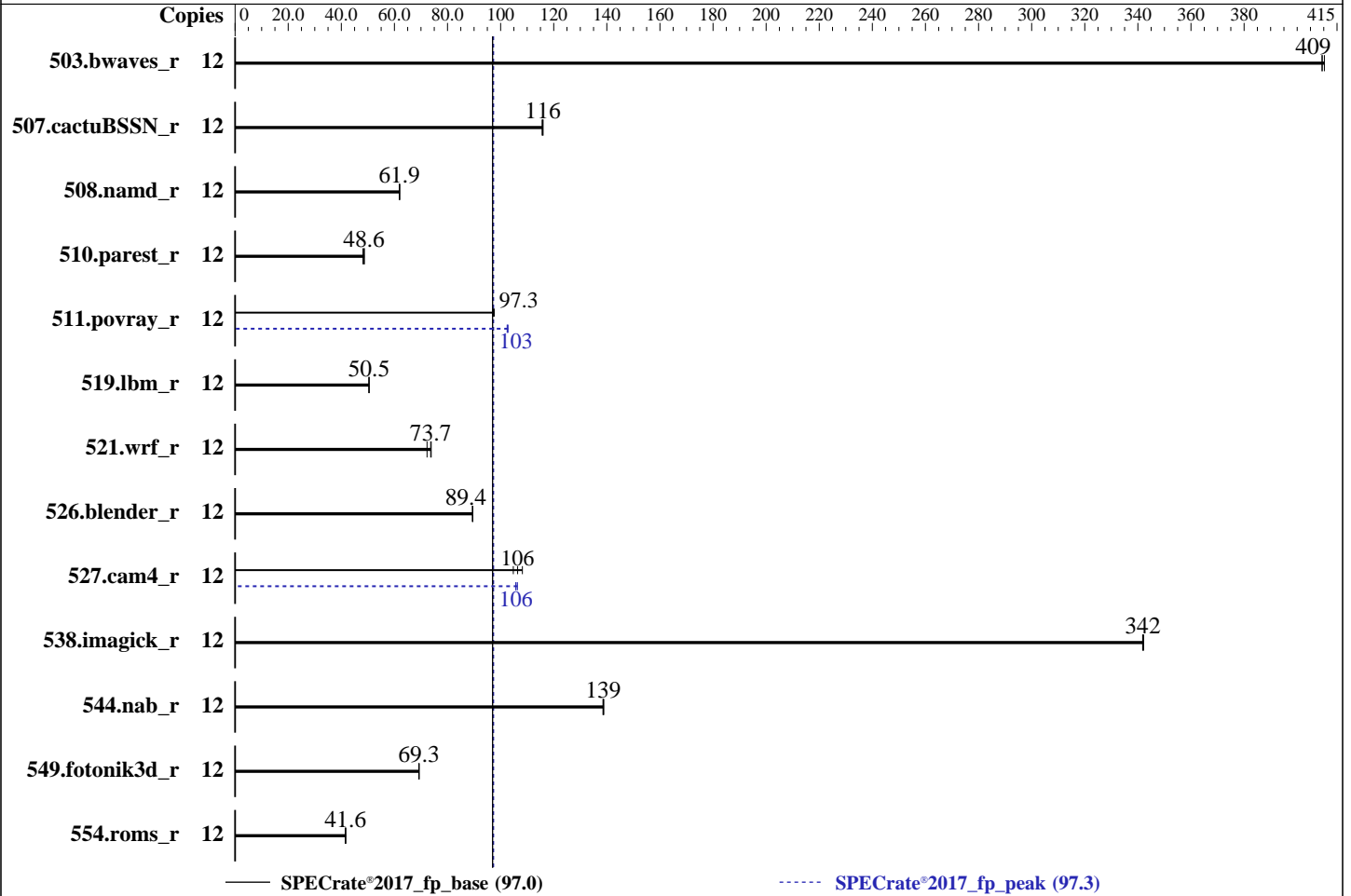
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: May-2024

Hardware Availability: May-2024

Software Availability: Mar-2024



### Hardware

CPU Name: Intel Xeon E-2486  
 Max MHz: 5600  
 Nominal: 3500  
 Enabled: 6 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 18 MB I+D on chip per chip  
 Other: None  
 Memory: 64 GB (2 x 32 GB 2Rx8 PC5-4800B-E, running at 4400)  
 Storage: 1 x 960 GB SATA SSD  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP5  
 Kernel 5.14.21-150500.53-default  
 Compiler: C/C++: Version 2024.0.2 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2024.0.2 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Lenovo BIOS Version CTE105H 1.10 released Mar-2024  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage



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### Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	12	294	409	<b><u>294</u></b>	<b><u>409</u></b>	293	410	12	294	409	<b><u>294</u></b>	<b><u>409</u></b>	293	410
507.cactuBSSN_r	12	<b><u>131</u></b>	<b><u>116</u></b>	131	116	131	116	12	<b><u>131</u></b>	<b><u>116</u></b>	131	116	131	116
508.namd_r	12	184	62.0	<b><u>184</u></b>	<b><u>61.9</u></b>	184	61.9	12	184	62.0	<b><u>184</u></b>	<b><u>61.9</u></b>	184	61.9
510.parest_r	12	652	48.2	<b><u>647</u></b>	<b><u>48.6</u></b>	645	48.7	12	652	48.2	<b><u>647</u></b>	<b><u>48.6</u></b>	645	48.7
511.povray_r	12	287	97.6	<b><u>288</u></b>	<b><u>97.3</u></b>	288	97.2	12	273	102	<b><u>273</u></b>	<b><u>103</u></b>	273	103
519.lbm_r	12	251	50.5	251	50.4	<b><u>251</u></b>	<b><u>50.5</u></b>	12	251	50.5	251	50.4	<b><u>251</u></b>	<b><u>50.5</u></b>
521.wrf_r	12	<b><u>365</u></b>	<b><u>73.7</u></b>	372	72.3	364	73.9	12	<b><u>365</u></b>	<b><u>73.7</u></b>	372	72.3	364	73.9
526.blender_r	12	204	89.5	<b><u>204</u></b>	<b><u>89.4</u></b>	204	89.4	12	204	89.5	<b><u>204</u></b>	<b><u>89.4</u></b>	204	89.4
527.cam4_r	12	194	108	200	105	<b><u>197</u></b>	<b><u>106</u></b>	12	197	106	198	106	<b><u>198</u></b>	<b><u>106</u></b>
538.imagick_r	12	87.3	342	87.2	342	<b><u>87.3</u></b>	<b><u>342</u></b>	12	87.3	342	87.2	342	<b><u>87.3</u></b>	<b><u>342</u></b>
544.nab_r	12	145	139	146	139	<b><u>146</u></b>	<b><u>139</u></b>	12	145	139	146	139	<b><u>146</u></b>	<b><u>139</u></b>
549.fotonik3d_r	12	<b><u>675</u></b>	<b><u>69.3</u></b>	675	69.3	675	69.3	12	<b><u>675</u></b>	<b><u>69.3</u></b>	675	69.3	675	69.3
554.roms_r	12	<b><u>458</u></b>	<b><u>41.6</u></b>	457	41.7	460	41.5	12	<b><u>458</u></b>	<b><u>41.6</u></b>	457	41.7	460	41.5

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017-1.1.9-ic2024.0.2/lib/intel64:/home/cpu2017-1.1.9-ic2024.0.2/je5.0.1-64"  
MALLOCONF = "retain:true"

### General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

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**Test Date:** May-2024  
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### General Notes (Continued)

is mitigated in the system as tested and documented.  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

BIOS configuration:  
Choose Operating Mode set to Maximum Performance  
Turbo Limit for 95W CPU set to Disabled

Sysinfo program /home/cpu2017-1.1.9-ic2024.0.2/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Sat May 18 14:21:41 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

-----  
1. uname -a  
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT\_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)  
x86\_64 x86\_64 x86\_64 GNU/Linux  
-----

-----  
2. w  
14:21:42 up 1 day, 19:28, 2 users, load average: 3.75, 7.43, 9.55  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root tty1 - Thu18 37:05m 0.61s 0.00s -bash  
root tty2 - Thu18 43:26m 0.00s 0.00s -bash  
-----

-----  
3. Username  
From environment variable \$USER: root  
-----

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### Platform Notes (Continued)

```

-----
4. ulimit -a
   core file size          (blocks, -c) unlimited
   data seg size          (kbytes, -d) unlimited
   scheduling priority      (-e) 0
   file size              (blocks, -f) unlimited
   pending signals        (-i) 256771
   max locked memory      (kbytes, -l) 64
   max memory size        (kbytes, -m) unlimited
   open files             (-n) 1024
   pipe size              (512 bytes, -p) 8
   POSIX message queues   (bytes, -q) 819200
   real-time priority     (-r) 0
   stack size            (kbytes, -s) unlimited
   cpu time              (seconds, -t) unlimited
   max user processes     (-u) 256771
   virtual memory         (kbytes, -v) unlimited
   file locks            (-x) unlimited

```

```

-----
5. sysinfo process ancestry
   /usr/lib/systemd/systemd --switched-root --system --deserialize 30
   login -- root
   -bash
   -bash
   -bash
   runcpu --nobuild --action validate --define default-platform-flags --define numcopies=12 -c
     ic2024.0.2-lin-core-avx2-rate-20231213.cfg --define smt-on --define cores=6 --define physicallogical
     --define no-numa --tune base,peak -o all --define drop_caches fprate
   runcpu --nobuild --action validate --define default-platform-flags --define numcopies=12 --configfile
     ic2024.0.2-lin-core-avx2-rate-20231213.cfg --define smt-on --define cores=6 --define physicallogical
     --define no-numa --tune base,peak --output_format all --define drop_caches --nopower --runmode rate --tune
     base:peak --size refrate fprate --nopreenv --note-preenv --logfile
     $$SPEC/tmp/CPU2017.197/templogs/preenv.fprate.197.0.log --lognum 197.0 --from_runcpu 2
   specperl $$SPEC/bin/sysinfo
   $$SPEC = /home/cpu2017-1.1.9-ic2024.0.2

```

```

-----
6. /proc/cpuinfo
   model name      : Intel(R) Xeon(R) E E-2486
   vendor_id      : GenuineIntel
   cpu family     : 6
   model         : 183
   stepping      : 1
   microcode     : 0x11f
   bugs          : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
   cpu cores     : 6
   siblings      : 12
   1 physical ids (chips)
   12 processors (hardware threads)
   physical id 0: core ids 0-5
   physical id 0: apicids 0-11
   Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
   virtualized systems. Use the above data carefully.

```

```

-----
7. lscpu

From lscpu from util-linux 2.37.4:

```

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### Platform Notes (Continued)

```

Architecture:                x86_64
CPU op-mode(s):              32-bit, 64-bit
Address sizes:                42 bits physical, 48 bits virtual
Byte Order:                   Little Endian
CPU(s):                       12
On-line CPU(s) list:         0-11
Vendor ID:                    GenuineIntel
Model name:                   Intel(R) Xeon(R) E E-2486
CPU family:                   6
Model:                        183
Thread(s) per core:          2
Core(s) per socket:          6
Socket(s):                    1
Stepping:                     1
BogoMIPS:                     6988.80
Flags:                         fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                                lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                                nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                                ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2
                                x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                                abm 3dnowprefetch cpuid_fault epb invpcid_single ssbd ibrs ibpb stibp
                                ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase
                                tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed adx smap clflushopt
                                clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves split_lock_detect
                                avx_vnni dtherm ida arat pln pts hfi umip pku ospke waitpkg gfni vaes
                                vpclmulqdq tme rdpid movdiri movdir64b fsrm md_clear serialize pconfig
                                arch_lbr flush_l1d arch_capabilities

Virtualization:              VT-x
L1d cache:                   288 KiB (6 instances)
L1i cache:                   192 KiB (6 instances)
L2 cache:                     12 MiB (6 instances)
L3 cache:                     18 MiB (1 instance)
NUMA node(s):                1
NUMA node0 CPU(s):          0-11
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:          Not affected
Vulnerability Mds:           Not affected
Vulnerability Meltdown:      Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed:      Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:     Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:     Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW
                                sequence
Vulnerability Srbds:          Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	288K	12	Data	1	64	1	64
L1i	32K	192K	8	Instruction	1	64	1	64
L2	2M	12M	16	Unified	2	2048	1	64
L3	18M	18M	9	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 1 nodes (0)

node 0 cpus: 0-11

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### Platform Notes (Continued)

```
node 0 size: 64222 MB
node 0 free: 62744 MB
node distances:
node 0
0: 10
```

```
-----
9. /proc/meminfo
MemTotal: 65764252 kB
-----
```

```
10. who -r
run-level 3 May 16 18:53
-----
```

```
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
Default Target Status
multi-user running
-----
```

```
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance issue-generator
kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog smartd sshd
systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
firewalld gpm grub2-once haveged haveged-switch-root ipmi ipmievd issue-add-ssh-keys
kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd
serial-getty@ smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures
systemd-network-generator systemd-sysexit systemd-time-wait-sync systemd-timesyncd
indirect wickedd
-----
```

```
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=178ca5f7-ae8d-49cb-88b7-ce796ad62ec9
splash=silent
quiet
security=apparmor
mitigations=auto
-----
```

```
14. cpupower frequency-info
analyzing CPU 0:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes
-----
```

```
15. sysctl
kernel.numa_balancing 0
kernel.randomize_va_space 2
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
-----
```

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### Platform Notes (Continued)

```

vm.dirty_ratio                20
vm.dirty_writeback_centisecs  500
vm.dirtytime_expire_seconds  43200
vm.extfrag_threshold          500
vm.min_unmapped_ratio         1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy     0
vm.nr_overcommit_hugepages    0
vm.swappiness                  60
vm.watermark_boost_factor     15000
vm.watermark_scale_factor     10
vm.zone_reclaim_mode          0

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs   10000

```

```

-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5

```

```

-----
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-ic2024.0.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       xfs   893G  30G  863G  4% /

```

```

-----
20. /sys/devices/virtual/dmi/id
Vendor:          Lenovo
Product:         ThinkSystem ST50 V3
Product Family: ThinkSystem
Serial:          1234567890

```

```

-----
21. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  1x SK Hynix HMC88AE8A081N 32 GB 2 rank 4800, configured at 4400
  1x SK Hynix HMC88AE8A084N 32 GB 2 rank 4800, configured at 4400

```

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### Platform Notes (Continued)

22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Lenovo  
BIOS Version: CTE105H-1.10  
BIOS Date: 03/28/2024  
BIOS Revision: 1.10  
Firmware Revision: 1.20

### Compiler Version Notes

```

=====
C          | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----

=====
C++       | 508.namd_r(base, peak) 510.parest_r(base, peak)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----

=====
C++, C    | 511.povray_r(base, peak) 526.blender_r(base, peak)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----

=====
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----

=====
Fortran   | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
-----
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----

=====
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
-----
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
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-----

```





# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem ST50 V3  
(3.50 GHz, Intel Xeon E-2486)

SPECrate®2017\_fp\_base = 97.0

SPECrate®2017\_fp\_peak = 97.3

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** May-2024

**Hardware Availability:** May-2024

**Software Availability:** Mar-2024

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64

507.cactuBSSN\_r: -DSPEC\_LP64

508.namd\_r: -DSPEC\_LP64

510.parest\_r: -DSPEC\_LP64

511.povray\_r: -DSPEC\_LP64

519.lbm\_r: -DSPEC\_LP64

521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian

526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char

527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG

538.imagick\_r: -DSPEC\_LP64

544.nab\_r: -DSPEC\_LP64

549.fotonik3d\_r: -DSPEC\_LP64

554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math

-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

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**Hardware Availability:** May-2024

**Tested by:** Lenovo Global Technology

**Software Availability:** Mar-2024

## Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

544.nab\_r: basepeak = yes

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: basepeak = yes

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

554.roms\_r: basepeak = yes

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

527.cam4\_r: -w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int  
-nostandard-realloc-lhs -align array32byte -auto

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## Peak Optimization Flags (Continued)

527.cam4\_r (continued):

```
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2 -fltto
-Ofast -ffast-math -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Catlow-A.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Catlow-A.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

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