



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017\_fp\_base = 501

H3C UniServer R4900 G6 (Intel Xeon Gold 5418N)

SPECrate®2017\_fp\_peak = 517

CPU2017 License: 9066

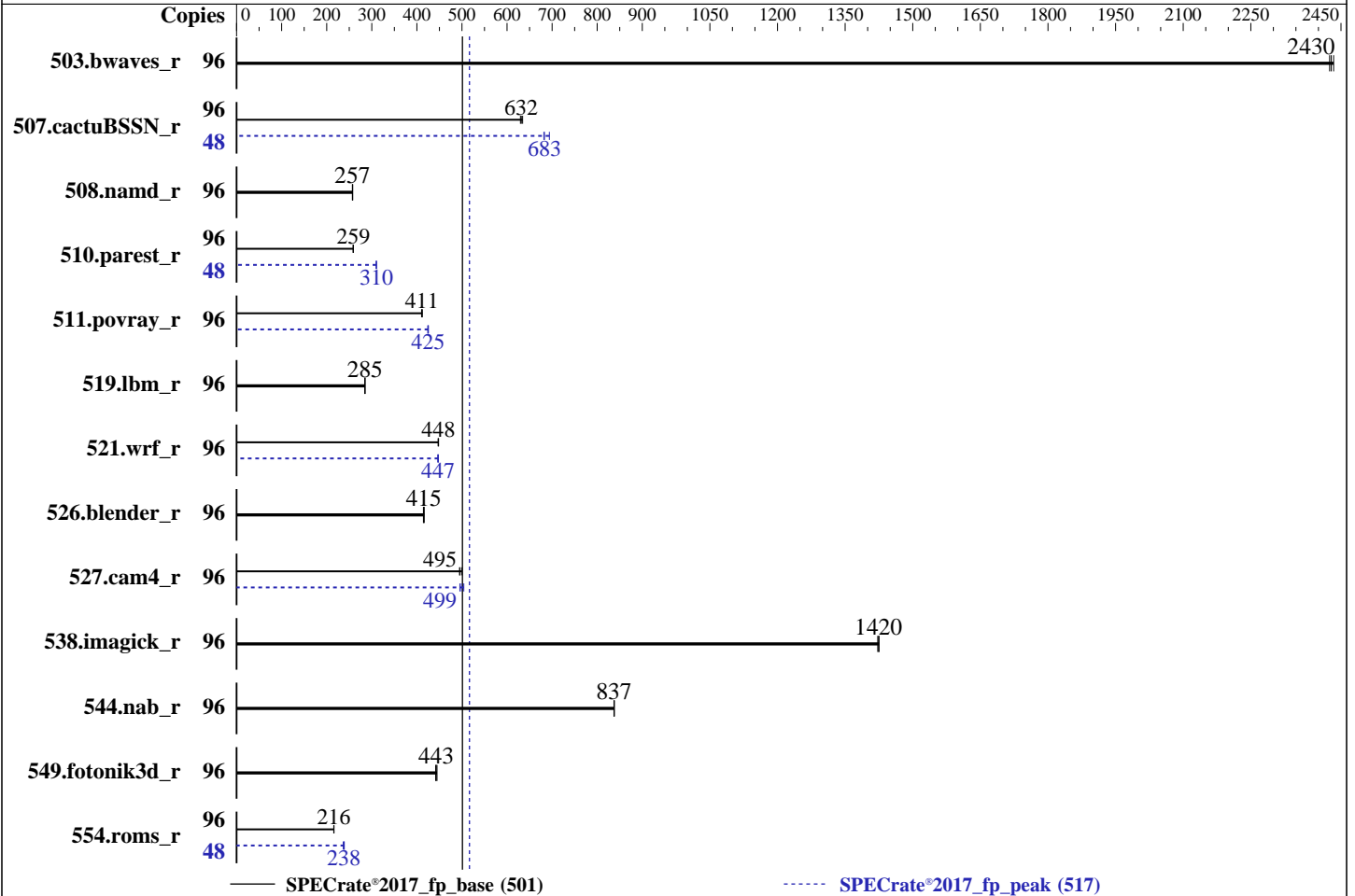
Test Date: May-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Oct-2023

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Apr-2024



### Hardware

CPU Name: Intel Xeon Gold 5418N  
 Max MHz: 3800  
 Nominal: 1800  
 Enabled: 48 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 45 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx8 PC5-4800B-R, running at 4000)  
 Storage: 1 x 1.92 TB SSD  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP5 5.14.21-150500.53-default  
 Compiler: C/C++: Version 2024.0.2 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2024.0.2 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Version 5.31 released Dec-2023 BIOS  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	96	<b><u>396</u></b>	<b><u>2430</u></b>	397	2420	396	2430	96	<b><u>396</u></b>	<b><u>2430</u></b>	397	2420	396	2430
507.cactuBSSN_r	96	<b><u>192</u></b>	<b><u>632</u></b>	193	630	192	635	48	<b><u>89.0</u></b>	<b><u>683</u></b>	89.1	682	87.6	694
508.namd_r	96	<b><u>355</u></b>	<b><u>257</u></b>	354	258	355	257	96	<b><u>355</u></b>	<b><u>257</u></b>	354	258	355	257
510.parest_r	96	971	259	970	259	<b><u>970</u></b>	<b><u>259</u></b>	48	<b><u>405</u></b>	<b><u>310</u></b>	405	310	405	310
511.povray_r	96	<b><u>545</u></b>	<b><u>411</u></b>	546	410	544	412	96	<b><u>527</u></b>	<b><u>425</u></b>	527	426	529	424
519.lbm_r	96	356	284	<b><u>355</u></b>	<b><u>285</u></b>	355	285	96	356	284	<b><u>355</u></b>	<b><u>285</u></b>	355	285
521.wrf_r	96	<b><u>480</u></b>	<b><u>448</u></b>	480	448	481	447	96	<b><u>481</u></b>	<b><u>447</u></b>	480	448	482	446
526.blender_r	96	353	415	<b><u>352</u></b>	<b><u>415</u></b>	351	416	96	353	415	<b><u>352</u></b>	<b><u>415</u></b>	351	416
527.cam4_r	96	<b><u>339</u></b>	<b><u>495</u></b>	339	495	336	499	96	<b><u>337</u></b>	<b><u>499</u></b>	333	504	339	495
538.imagick_r	96	168	1420	<b><u>168</u></b>	<b><u>1420</u></b>	168	1430	96	168	1420	<b><u>168</u></b>	<b><u>1420</u></b>	168	1430
544.nab_r	96	<b><u>193</u></b>	<b><u>837</u></b>	193	838	193	837	96	<b><u>193</u></b>	<b><u>837</u></b>	193	838	193	837
549.fotonik3d_r	96	841	445	847	442	<b><u>845</u></b>	<b><u>443</u></b>	96	841	445	847	442	<b><u>845</u></b>	<b><u>443</u></b>
554.roms_r	96	707	216	706	216	<b><u>707</u></b>	<b><u>216</u></b>	48	<b><u>320</u></b>	<b><u>238</u></b>	319	239	322	237

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)



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## General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS Settings:

Set SNC to Enable SNC2 (2-clusters)

Set Power Performance Tuning to BIOS Controls EFB

Set ENERGY\_PERF\_BIAS\_CFG mode to Performance

Set CPU C6 Report to Enabled

Sysinfo program /home/speccpu/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost.localdomain Wed May 1 00:12:40 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

-----

1. uname -a  
Linux localhost.localdomain 5.14.21-150500.53-default #1 SMP PREEMPT\_DYNAMIC Wed May 10 07:56:26 UTC 2023  
(b630043) x86\_64 x86\_64 x86\_64 GNU/Linux

-----

2. w  
00:12:40 up 7:23, 1 user, load average: 59.91, 87.28, 93.01  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

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## Platform Notes (Continued)

```
root      tty1      -                16:50      6:43m  0.88s  0.01s /bin/sh
./reportable-ic2024.0.2-lin-sapphirerapids-rate-smt-on-20231213.sh
```

### 3. Username

From environment variable \$USER: root

### 4. ulimit -a

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size                (blocks, -f) unlimited
pending signals         (-i) 4125104
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 4125104
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

### 5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 29
login -- root
-bash
/bin/sh ./reportable-ic2024.0.2-lin-sapphirerapids-rate-smt-on-20231213.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=96 -c
ic2024.0.2-lin-sapphirerapids-rate-20231213.cfg --define smt-on --define cores=48 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=96 --configfile
ic2024.0.2-lin-sapphirerapids-rate-20231213.cfg --define smt-on --define cores=48 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.051/templogs/preenv.fprate.051.0.log --lognum 051.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/speccpu
```

### 6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Gold 5418N
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 8
microcode      : 0x2b0004d0
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores      : 24
siblings       : 48
2 physical ids (chips)
96 processors (hardware threads)
physical id 0: core ids 0-23
physical id 1: core ids 0-23
physical id 0: apicids 0-47
physical id 1: apicids 128-175
```

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## Platform Notes (Continued)

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 96
On-line CPU(s) list:   0-95
Vendor ID:              GenuineIntel
Model name:             Intel(R) Xeon(R) Gold 5418N
CPU family:             6
Model:                  143
Thread(s) per core:    2
Core(s) per socket:    24
Socket(s):              2
Stepping:               8
CPU max MHz:           3800.0000
CPU min MHz:           800.0000
BogoMIPS:              3600.00
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                        sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                        lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                        invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced
                        tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmil hle
                        avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                        xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                        cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                        arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req hfi avx512vbmi umip
                        pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
                        avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote
                        movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr
                        avx512_fp16 amx_tile flush_l1d arch_capabilities
Virtualization:        VT-x
L1d cache:             2.3 MiB (48 instances)
L1i cache:             1.5 MiB (48 instances)
L2 cache:              96 MiB (48 instances)
L3 cache:              90 MiB (2 instances)
NUMA node(s):         4
NUMA node0 CPU(s):    0-11,48-59
NUMA node1 CPU(s):    12-23,60-71
NUMA node2 CPU(s):    24-35,72-83
NUMA node3 CPU(s):    36-47,84-95
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:    Not affected
Vulnerability Mds:     Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization

```

(Continued on next page)



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## Platform Notes (Continued)

Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW sequence

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	2.3M	12	Data	1	64	1	64
L1i	32K	1.5M	8	Instruction	1	64	1	64
L2	2M	96M	16	Unified	2	2048	1	64
L3	45M	90M	15	Unified	3	49152	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0-11,48-59
node 0 size: 257602 MB
node 0 free: 244530 MB
node 1 cpus: 12-23,60-71
node 1 size: 258041 MB
node 1 free: 247339 MB
node 2 cpus: 24-35,72-83
node 2 size: 258007 MB
node 2 free: 247183 MB
node 3 cpus: 36-47,84-95
node 3 size: 257647 MB
node 3 free: 247019 MB
node distances:
node  0  1  2  3
 0:  10  12  21  21
 1:  12  10  21  21
 2:  21  21  10  12
 3:  21  21  12  10

```

9. /proc/meminfo

MemTotal: 1056051044 kB

10. who -r

run-level 3 Apr 30 16:49

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

```

Default Target Status
multi-user      running

```

12. Services, from systemctl list-unit-files

```

STATE UNIT FILES
enabled ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online apparmor
auditd cron firewalld getty@ irqbalance issue-generator kbdsettings kdump kdump-early
postfix purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4
wickedd-dhcp6 wickedd-nanny wpa_supplicant
enabled-runtime systemd-remount-fs
disabled boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell dnsmasq ebttables
grub2-once haveged switch-root issue-add-ssh-keys kexec-load lunmask nfs
nfs-blkmap rpcbind rpmconfigcheck serial-getty@ systemd-boot-check-no-failures
systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned

```

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## Platform Notes (Continued)

indirect wpa\_supplicant@  
wickedd

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=8941a345-b7dd-4a3e-98f3-0af9eb97f32e
splash=silent
resume=/dev/disk/by-uuid/06a94d0a-0f9e-4e93-848f-045322d8f16d
mitigations=auto
quiet
security=apparmor
crashkernel=373M,high
crashkernel=72M,low
transparent_hugepage=always

```

```

-----
14. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 800 MHz and 3.80 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.

  boost state support:
    Supported: yes
    Active: yes

```

```

-----
15. tuned-adm active
It seems that tuned daemon is not running, preset profile is not activated.
Preset profile: throughput-performance

```

```

-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness     20
vm.dirty_background_bytes       0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                  1
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold            500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                    60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode           0

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force

```

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## Platform Notes (Continued)

```

18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000

```

```

19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5

```

```

20. Disk information
SPEC is set to: /home/speccpu
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 1.5T 71G 1.4T 5% /home

```

```

21. /sys/devices/virtual/dmi/id
Vendor: H3C
Product: H3C UniServer R4900 G6
Product Family: Rack
Serial: N/A

```

```

22. dmidecode
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
1x Samsung M321R8GA0BB0-CQKDG 64 GB 2 rank 4800, configured at 4000
1x Samsung M321R8GA0BB0-CQKEG 64 GB 2 rank 4800, configured at 4000
14x Samsung M321R8GA0BB0-CQKMG 64 GB 2 rank 4800, configured at 4000

```

```

23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 6.00.34
BIOS Date: 12/19/2023
BIOS Revision: 5.31

```

## Compiler Version Notes

```

=====
C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
=====

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
=====

```

```

=====
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)
=====

```

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-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
-----

=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)  
-----

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-----

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)  
-----

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-----

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)  
-----

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-----

=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
-----

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-----

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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New H3C Technologies Co., Ltd.

SPECrate®2017\_fp\_base = 501

H3C UniServer R4900 G6 (Intel Xeon Gold 5418N)

SPECrate®2017\_fp\_peak = 517

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: May-2024

Hardware Availability: Oct-2023

Software Availability: Apr-2024

## Base Compiler Invocation (Continued)

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
 507.cactuBSSN\_r: -DSPEC\_LP64  
 508.namd\_r: -DSPEC\_LP64  
 510.parest\_r: -DSPEC\_LP64  
 511.povray\_r: -DSPEC\_LP64  
 519.lbm\_r: -DSPEC\_LP64  
 521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
 526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
 527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
 538.imagick\_r: -DSPEC\_LP64  
 544.nab\_r: -DSPEC\_LP64  
 549.fotonik3d\_r: -DSPEC\_LP64  
 554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
 -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
 -Wno-implicit-int -mprefer-vector-width=512 -ljemalloc  
 -L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast  
 -ffast-math -flto -mfpmath=sse -funroll-loops  
 -qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc  
 -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto  
 -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
 -nostandard-realloc-lhs -align array32byte -auto -ljemalloc

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: May-2024

Hardware Availability: Oct-2023

Software Availability: Apr-2024

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

`-L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using both Fortran and C:

`-w -m64 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using both C and C++:

`-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using Fortran, C, and C++:

`-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib`

## Peak Compiler Invocation

C benchmarks:

`icx`

C++ benchmarks:

`icpx`

Fortran benchmarks:

`ifx`

Benchmarks using both Fortran and C:

`ifx icx`

Benchmarks using both C and C++:

`icpx icx`

Benchmarks using Fortran, C, and C++:

`icpx icx ifx`



# SPEC CPU®2017 Floating Point Rate Result

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New H3C Technologies Co., Ltd.

SPECrate®2017\_fp\_base = 501

H3C UniServer R4900 G6 (Intel Xeon Gold 5418N)

SPECrate®2017\_fp\_peak = 517

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: May-2024

Hardware Availability: Oct-2023

Software Availability: Apr-2024

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

544.nab\_r: basepeak = yes

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

554.roms\_r: -w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

511.povray\_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1) -fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)

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# SPEC CPU®2017 Floating Point Rate Result

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New H3C Technologies Co., Ltd.

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SPECrate®2017\_fp\_peak = 517

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: May-2024

Hardware Availability: Oct-2023

Software Availability: Apr-2024

## Peak Optimization Flags (Continued)

511.povray\_r (continued):

```
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64\\_revB.html](http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64_revB.html)

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-V1.0-SPR-RevD.html](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-SPR-RevD.html)

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64\\_revB.xml](http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64_revB.xml)

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-V1.0-SPR-RevD.xml](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-SPR-RevD.xml)

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