SPEC CPU®2017 Integer Speed Result

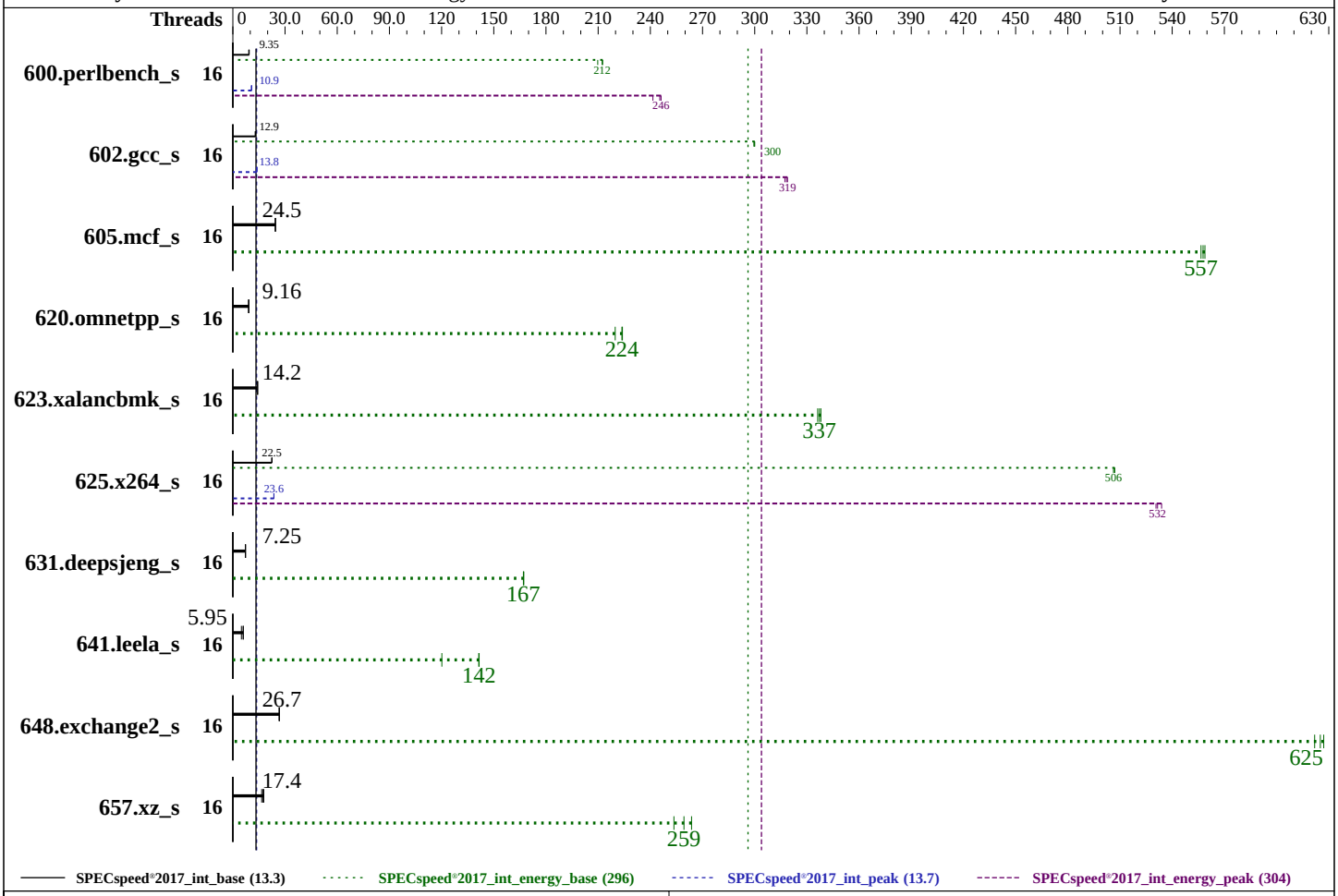
Lenovo Global Technology

ThinkSystem SR250 V3
(3.20 GHz,Intel Xeon E-2488)

SPECspeed®2017_int_base =	13.3
SPECspeed®2017_int_energy_base =	296
SPECspeed®2017_int_peak =	13.7
SPECspeed®2017_int_energy_peak =	304

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Mar-2024
Hardware Availability: Feb-2024
Software Availability: Mar-2024



Hardware

CPU Name:	Intel Xeon E-2488
Max MHz:	5600
Nominal:	3200
Enabled:	8 cores, 1 chip, 2 threads/core
Orderable:	1 chip
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	24 MB I+D on chip per chip
Other:	None
Memory:	64 GB (2 x 32 GB 2Rx8 PC5-4800B-E, running at 4400)
Storage:	1 x 480 GB SATA SSD
Other:	Cooling: Air

Software

OS:	SUSE Linux Enterprise Server 15 SP5 Kernel 5.14.21-150500.53-default
Compiler:	C/C++: Version 2024.0.2 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2024.0.2 of Intel Fortran Compiler for Linux;
Parallel:	Yes
Firmware:	Lenovo BIOS Version CTE104A 1.10 released Jan-2024
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS and OS set to balance power and performance

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Power

Max. Power (W): 111.65
Idle Power (W): 30.19
Min. Temperature (C): 20.63
Elevation (m): 43
Line Standard: 220 V / 50 Hz / 1 phase / 3 wires
Provisioning: Line-powered

Power Settings

Management FW: Version 1.10 of CTX304A
Memory Mode: Normal

Power-Relevant Hardware

Power Supply: 1 x 800 W (non-redundant)
Details: ThinkSystem 800W Titanium Power Supply 4P57A87056
Backplane: 8 x 2.5-inch HDD back plane
Other Storage: None
Storage Model #s: 4XB7A82289
NICs Installed: 1 x Broadcom 2-port BCM5720 embedded @ 1 Gb
NICs Enabled (FW/OS): 2 / 1
NICs Connected/Speed: 1 @ 1 Gb
Other HW Model #s: 4 x Standard fans

Power Analyzer

Power Analyzer: WIN:9888
Hardware Vendor: YOKOGAWA, Inc.
Model: YokogawaWT310E
Serial Number: C3UD17024E
Input Connection: Default
Metrology Institute: CNAS
Calibration By: GRG METROLOGY & TEST (BEIJING) CO., LTD.
Calibration Label: J202308266858A-0006
Calibration Date: 16-Oct-2023
PTDaemon® Version: 1.10.0 (82175bac; 2022-08-17)
Setup Description: Connected to PSU1
Current Ranges Used: 1A
Voltage Range Used: 300V

Temperature Meter

Temperature Meter: WIN:9889
Hardware Vendor: Digi International, Inc.
Model: DigiWATCHPORT_H
Serial Number: W63074363
Input Connection: USB
PTDaemon Version: 1.10.0 (82175bac; 2022-08-17)
Setup Description: 50 mm in front of SUT main intake

Base Results Table

Benchmark	Threads	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power
600.perlbench_s	16	194	9.13	9.18	210	47.2	48.1	**190**	**9.35**	**9.07**	**212**	**47.8**	**48.5**	189	9.37	9.06	213	47.8	48.5
602.gcc_s	16	308	12.9	14.4	300	46.9	49.0	**308**	**12.9**	**14.4**	**300**	**46.9**	**49.0**	308	12.9	14.4	300	47.0	49.0
605.mcf_s	16	193	24.5	9.22	559	47.7	52.5	193	24.5	9.23	558	47.8	52.1	**193**	**24.5**	**9.25**	**557**	**47.9**	**52.4**
620.omnetpp_s	16	**178**	**9.16**	**7.93**	**224**	**44.5**	**45.1**	181	9.00	8.07	220	44.6	45.1	177	9.20	7.92	224	44.7	45.3
623.xalancbmk_s	16	100	14.1	4.58	336	45.7	49.7	**99.8**	**14.2**	**4.56**	**337**	**45.7**	**49.6**	99.4	14.3	4.55	338	45.8	49.9
625.x264_s	16	78.7	22.4	3.79	507	48.1	48.8	78.5	22.5	3.79	507	48.2	48.8	**78.6**	**22.5**	**3.79**	**506**	**48.3**	**49.0**
631.deepsjeng_s	16	198	7.25	9.31	167	47.1	51.4	198	7.24	9.32	167	47.1	49.9	**198**	**7.25**	**9.33**	**167**	**47.2**	**51.2**
641.leela_s	16	**287**	**5.95**	**13.1**	**142**	**45.5**	**46.0**	347	4.92	15.4	120	44.3	44.8	286	5.96	13.1	141	45.7	46.1
648.exchange2_s	16	**110**	**26.7**	**5.12**	**625**	**46.5**	**47.1**	110	26.7	5.10	627	46.4	46.8	111	26.6	5.14	622	46.5	47.0
657.xz_s	16	348	17.8	25.5	264	73.4	111	373	16.6	26.6	254	71.2	110	**356**	**17.4**	**26.0**	**259**	**72.9**	**109**

SPECspeed®2017_int_base = 13.3

SPECspeed®2017_int_energy_base = 296

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Peak Results Table

Benchmark	Threads	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power
600.perlbench_s	16	167	10.6	7.98	241	47.7	48.7	163	10.9	7.85	245	48.0	48.8	**164**	**10.9**	**7.82**	**246**	**47.8**	**48.9**
602.gcc_s	16	290	13.8	13.6	317	47.1	49.1	289	13.8	13.6	318	47.1	48.9	**289**	**13.8**	**13.6**	**319**	**47.0**	**48.9**
605.mcf_s	16	193	24.5	9.22	559	47.7	52.5	193	24.5	9.23	558	47.8	52.1	**193**	**24.5**	**9.25**	**557**	**47.9**	**52.4**
620.omnetpp_s	16	**178**	**9.16**	**7.93**	**224**	**44.5**	**45.1**	181	9.00	8.07	220	44.6	45.1	177	9.20	7.92	224	44.7	45.3
623.xalancbmk_s	16	100	14.1	4.58	336	45.7	49.7	**99.8**	**14.2**	**4.56**	**337**	**45.7**	**49.6**	99.4	14.3	4.55	338	45.8	49.9
625.x264_s	16	74.8	23.6	3.62	531	48.4	48.9	74.6	23.6	3.59	534	48.2	48.9	**74.7**	**23.6**	**3.61**	**532**	**48.3**	**48.8**
631.deepsjeng_s	16	198	7.25	9.31	167	47.1	51.4	198	7.24	9.32	167	47.1	49.9	**198**	**7.25**	**9.33**	**167**	**47.2**	**51.2**
641.leela_s	16	**287**	**5.95**	**13.1**	**142**	**45.5**	**46.0**	347	4.92	15.4	120	44.3	44.8	286	5.96	13.1	141	45.7	46.1
648.exchange2_s	16	**110**	**26.7**	**5.12**	**625**	**46.5**	**47.1**	110	26.7	5.10	627	46.4	46.8	111	26.6	5.14	622	46.5	47.0
657.xz_s	16	348	17.8	25.5	264	73.4	111	373	16.6	26.6	254	71.2	110	**356**	**17.4**	**26.0**	**259**	**72.9**	**109**

SPECspeed®2017_int_peak = **13.7**

SPECspeed®2017_int_energy_peak = **304**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017-1.1.9-ic2024.0.2/lib/intel64:/home/cpu2017-1.1.9-ic2024.0.2/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

```
 Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
 memory using Redhat Enterprise Linux 8.0
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3>       /proc/sys/vm/drop_caches
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS configuration:
Choose Operating Mode set to Custom Mode
CPU P-state Control set to Cooperative with Legacy
```

(Continued on next page)

Platform Notes (Continued)

```
Sysinfo program /home/cpu2017-1.1.9-ic2024.0.2/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Thu Mar 21 00:21:29 2024

SUT (System Under Test) info as seen by some common utilities.

------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   00:21:29 up 42 min,  1 user,  load average: 0.06, 2.66, 2.70
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                23:39   41.00s  0.91s  0.00s sh
  Run745-compliant-ic2024.0.2-lin-sierraforest-speedint-20231213.sh

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 256760
```

(Continued on next page)

Platform Notes (Continued)

```
  max locked memory       (kbytes, -l) 64
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 256760
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize 30
 login -- root
 -bash
 /bin/bash ./run_SR250V3.sh
 sh Run745-compliant-ic2024.0.2-lin-sierraforest-speedint-20231213.sh
 runcpu --power --nobuild --action validate --define default-platform-flags -c
   ic2024.0.2-lin-sierraforest-speed-20231213.cfg --define cores=16 --tune base,peak -o all --define
   intspeedaffinity --define smt-on --define drop_caches intspeed
 runcpu --power --nobuild --action validate --define default-platform-flags --configfile
   ic2024.0.2-lin-sierraforest-speed-20231213.cfg --define cores=16 --tune base,peak --output_format all
   --define intspeedaffinity --define smt-on --define drop_caches --runmode speed --tune base:peak --size
   refspeed intspeed --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.051/templogs/preenv.intspeed.051.0.log --lognum 051.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2024.0.2

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) E E-2488
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 183
    stepping        : 1
    microcode       : 0x11f
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
    cpu cores       : 8
    siblings        : 16
    1 physical ids (chips)
    16 processors (hardware threads)
    physical id 0: core ids 0-7
    physical id 0: apicids 0-15
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.4:
  Architecture:                    x86_64
  CPU op-mode(s):                  32-bit, 64-bit
  Address sizes:                   42 bits physical, 48 bits virtual
  Byte Order:                      Little Endian
  CPU(s):                          16
```

(Continued on next page)

Platform Notes (Continued)

```
  On-line CPU(s) list:             0-15
  Vendor ID:                       GenuineIntel
  Model name:                      Intel(R) Xeon(R) E E-2488
  CPU family:                      6
  Model:                           183
  Thread(s) per core:              2
  Core(s) per socket:              8
  Socket(s):                       1
  Stepping:                        1
  CPU max MHz:                     7200.0000
  CPU min MHz:                     800.0000
  BogoMIPS:                        6374.40
  Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                   clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                                   lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                                   nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                                   ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2
                                   x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                                   abm 3dnowprefetch cpuid_fault epb invpcid_single ssbd ibrs ibpb stibp
                                   ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase
                                   tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed adx smap clflushopt
                                   clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves split_lock_detect
                                   avx_vnni dtherm ida arat pln pts hwp hwp_notify hwp_act_window hwp_epp
                                   hwp_pkg_req hfi umip pku ospke waitpkg gfni vaes vpclmulqdq tme rdpid
                                   movdiri movdir64b fsrm md_clear serialize pconfig arch_lbr flush_l1d
                                   arch_capabilities
  Virtualization:                  VT-x
  L1d cache:                       384 KiB (8 instances)
  L1i cache:                       256 KiB (8 instances)
  L2 cache:                        16 MiB (8 instances)
  L3 cache:                        24 MiB (1 instance)
  NUMA node(s):                    1
  NUMA node0 CPU(s):               0-15
  Vulnerability Itlb multihit:     Not affected
  Vulnerability L1tf:              Not affected
  Vulnerability Mds:               Not affected
  Vulnerability Meltdown:          Not affected
  Vulnerability Mmio stale data:   Not affected
  Vulnerability Retbleed:          Not affected
  Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
  Vulnerability Spectre v1:        Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:        Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW
                                   sequence
  Vulnerability Srbds:             Not affected
  Vulnerability Tsx async abort:   Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     384K   12 Data            1    64        1             64
     L1i       32K     256K    8 Instruction     1    64        1             64
     L2         2M      16M   16 Unified         2  2048        1             64
     L3        24M      24M   12 Unified         3 32768        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 1 nodes (0)
```

(Continued on next page)

Platform Notes (Continued)

```
  node 0 cpus: 0-15
  node 0 size: 64221 MB
  node 0 free: 63141 MB
  node distances:
  node   0
    0:  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       65763240 kB

------------------------------------------------------------
10. who -r
  run-level 3 Mar 20 23:39

------------------------------------------------------------
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          YaST2-Firstboot YaST2-Second-Stage apparmor auditd chronyd cron getty@ irqbalance
                   issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog
                   smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                   console-getty cups cups-browsed debug-shell ebtables exchange-bmc-os-info firewalld gpm
                   grub2-once haveged haveged-switch-root ipmi ipmievd issue-add-ssh-keys kexec-load ksm
                   kvm_stat lunmask man-db-create multipathd nfs nfs-blkmap nmb ntp-wait ntpd rpcbind
                   rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts smb snmpd snmptrapd svnserve
                   systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                   systemd-time-wait-sync systemd-timesyncd udisks2
  indirect         wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
  root=UUID=a535059d-421f-4d75-8efd-d000d99daec0
  splash=silent
  mitigations=auto
  quiet
  security=apparmor

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 0:
    current policy: frequency should be within 800 MHz and 6.90 GHz.
                    The governor "powersave" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. sysctl
```

(Continued on next page)

Platform Notes (Continued)

```
  kernel.numa_balancing              0
  kernel.randomize_va_space          2
  vm.compaction_proactiveness       20
  vm.dirty_background_bytes          0
  vm.dirty_background_ratio         10
  vm.dirty_bytes                     0
  vm.dirty_expire_centisecs       3000
  vm.dirty_ratio                    20
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      60
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP5

------------------------------------------------------------
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-ic2024.0.2
  Filesystem     Type  Size  Used Avail Use% Mounted on
  /dev/sda2      xfs   446G   44G  403G  10% /

------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
    Vendor:         Lenovo
    Product:        ThinkSystem SR250 V3
    Product Family: ThinkSystem
    Serial:         1234567890

------------------------------------------------------------
21. dmidecode
```

(Continued on next page)

Platform Notes (Continued)

```
  Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    1x Samsung M324R4GA3BB0-CQKOD 32 GB 2 rank 4800, configured at 4400
    1x Samsung M324R4GA3BB0-CQKOL 32 GB 2 rank 4800, configured at 4400


 ------------------------------------------------------------
 22. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       Lenovo
    BIOS Version:      CTE104A-1.10
    BIOS Date:         01/10/2024
    BIOS Revision:     1.10
    Firmware Revision: 1.10
```

Compiler Version Notes

```
==============================================================================================================
C       | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
        | 657.xz_s(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
        | 641.leela_s(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
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==============================================================================================================
Fortran | 648.exchange2_s(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
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```

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifx

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xsierraforest -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-w -std=c++14 -m64 -Wl,-z,muldefs -xsierraforest -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xsierraforest -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -w -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2 -flto
-Ofast(pass 1) -O3 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -w -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2 -flto
-Ofast(pass 1) -O3 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -w -std=c11 -m64 -Wl,-z,muldefs -xsierraforest -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

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Peak Optimization Flags (Continued)

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Catlow-A.html
http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Catlow-A.xml
http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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