



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Pte Ltd)

Tyrone Camarero SDI200A3N-212
(2.00 GHz, Intel Xeon Silver 4416+)

SPECrate®2017_int_base = 353

SPECrate®2017_int_peak = 364

CPU2017 License: 006042

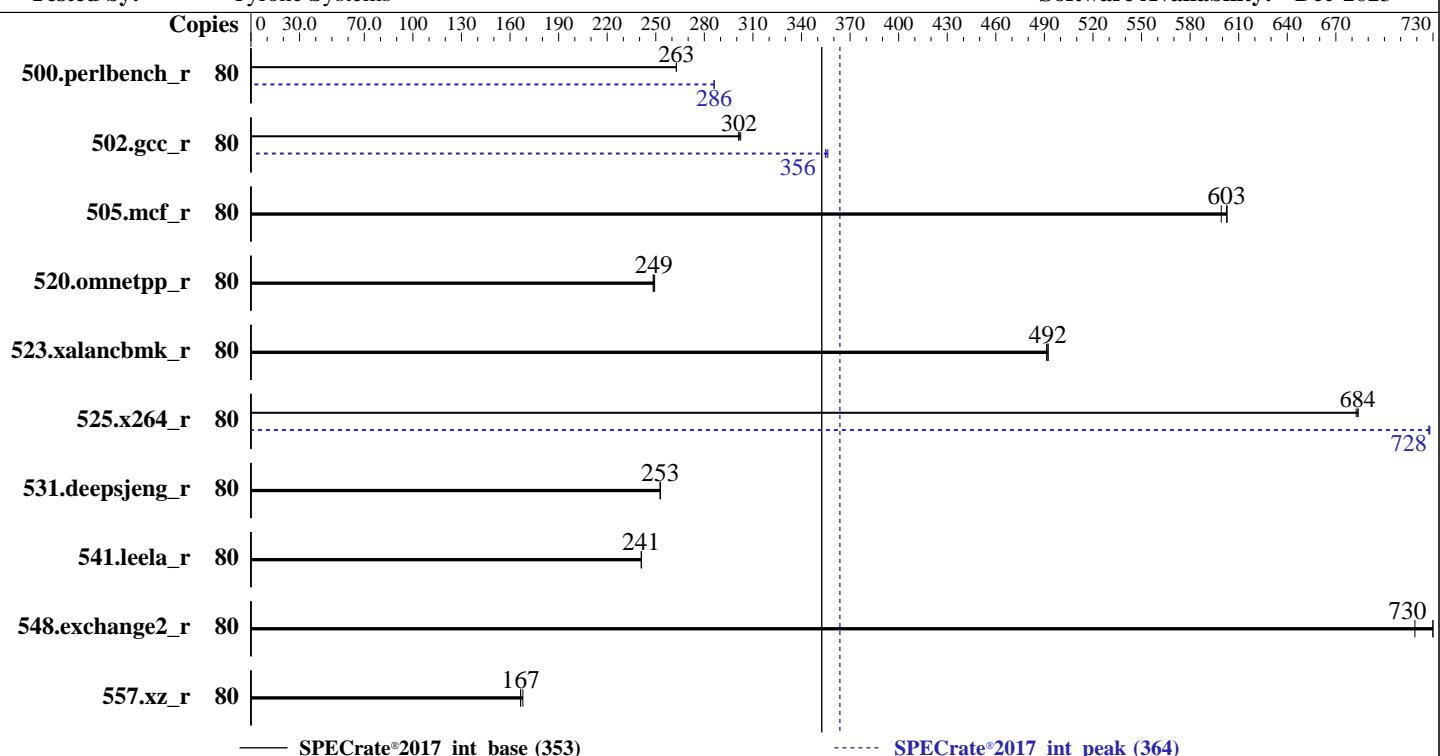
Test Date: Jan-2024

Test Sponsor: Netweb Pte Ltd

Hardware Availability: Jan-2023

Tested by: Tyrone Systems

Software Availability: Dec-2023



Hardware

CPU Name: Intel Xeon Silver 4416+
Max MHz: 3900
Nominal: 2000
Enabled: 40 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 37.5 MB I+D on chip per chip
Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R, running at 4000)
Storage: 1 x 960 GB NVMe
Other: None

OS:

Red Hat Enterprise Linux 9.3 (Plow)
5.14.0-362.13.1.el9_3.x86_64

Compiler:

C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;

Parallel:

No

Firmware:

Version 1.3 released Jun-2023

File System:

xfs

System State:

Run level 3 (multi-user)

Base Pointers:

64-bit

Peak Pointers:

32/64-bit

Other:

jemalloc memory allocator V5.0.1

Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	80	485	263	485	263	485	263	80	445	286	445	286	445	286	445	286
502.gcc_r	80	375	302	376	302	376	301	80	318	356	318	356	319	355	319	355
505.mcf_r	80	214	603	215	603	216	599	80	214	603	215	603	216	599	216	599
520.omnetpp_r	80	421	249	423	248	422	249	80	421	249	423	248	422	249	422	249
523.xalancbmk_r	80	172	492	172	492	172	491	80	172	492	172	492	172	491	172	491
525.x264_r	80	205	683	205	684	205	684	80	193	728	192	728	193	727	193	727
531.deepsjeng_r	80	362	253	363	253	363	253	80	362	253	363	253	363	253	363	253
541.leela_r	80	550	241	550	241	549	241	80	550	241	550	241	549	241	549	241
548.exchange2_r	80	292	719	287	730	287	730	80	292	719	287	730	287	730	287	730
557.xz_r	80	515	168	519	166	519	167	80	515	168	519	166	519	167	519	167

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

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General Notes (Continued)

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Settings:
ENERGY_PERF_BIAS_CFG mode = Maximum Performance
KTI Prefetch = Enable
LLC Dead Line Alloc = Disable

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Sat Jan 20 12:05:58 2024

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
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4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
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9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-18.el9)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
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18. /sys/kernel/mm/transparent_hugepage
19. /sys/kernel/mm/transparent_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS

1. uname -a
Linux localhost.localdomain 5.14.0-362.13.1.el9_3.x86_64 #1 SMP PREEMPT_DYNAMIC Fri Nov 24 01:57:57 EST
2023 x86_64 x86_64 x86_64 GNU/Linux

2. w
12:05:58 up 47 min, 1 user, load average: 0.00, 0.00, 0.00
USER TTY LOGIN@ IDLE JCPU PCPU WHAT
root tty1 12:04 5.00s 0.88s 0.01s -bash

3. Username

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Platform Notes (Continued)

From environment variable \$USER: root

```
4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 4126736
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 4126736
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=80 -c
  ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=40 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=80 --configfile
  ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=40 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
  --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
6. /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4416+
vendor_id : GenuineIntel
cpu family : 6
model : 143
stepping : 8
microcode : 0x2b0004d0
bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrss_pbrsb
cpu cores : 20
siblings : 40
2 physical ids (chips)
80 processors (hardware threads)
physical id 0: core ids 0-19
physical id 1: core ids 0-19
physical id 0: apicids 0-39
physical id 1: apicids 128-167
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

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Platform Notes (Continued)

7. lscpu

```
From lscpu from util-linux 2.37.4:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 80
On-line CPU(s) list: 0-79
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
Model name: Intel(R) Xeon(R) Silver 4416+
BIOS Model name: Intel(R) Xeon(R) Silver 4416+
CPU family: 6
Model: 143
Thread(s) per core: 2
Core(s) per socket: 20
Socket(s): 2
Stepping: 8
CPU max MHz: 3900.0000
CPU min MHz: 800.0000
BogoMIPS: 4000.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
       clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb
       rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl
       xtopology nonstop_tsc cpuid aperf mperf tsc_known_freq pnpi pclmulqdq
       dtes64 monitor ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid
       dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx
       fl6c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2
       cdp_l3 invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp
       ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqmq
       rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb
       intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1
       xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local
       split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
       avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
       avx512_vnni avx512_bitlg tme avx512_vpocntdq la57 rdpid
       bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear
       serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile
       amx_int8 flush_l1d arch_capabilities
L1d cache: 1.9 MiB (40 instances)
L1i cache: 1.3 MiB (40 instances)
L2 cache: 80 MiB (40 instances)
L3 cache: 75 MiB (2 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-9,40-49
NUMA node1 CPU(s): 10-19,50-59
NUMA node2 CPU(s): 20-29,60-69
NUMA node3 CPU(s): 30-39,70-79
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS, IBPB conditional, RSB filling,
```

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Platform Notes (Continued)

PBRSB-eIBRS SW sequence

Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.9M	12	Data	1	64	1	64
L1i	32K	1.3M	8	Instruction	1	64	1	64
L2	2M	80M	16	Unified	2	2048	1	64
L3	37.5M	75M	15	Unified	3	40960	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)
node 0 cpus: 0-9,40-49
node 0 size: 257680 MB
node 0 free: 256969 MB
node 1 cpus: 10-19,50-59
node 1 size: 258042 MB
node 1 free: 257349 MB
node 2 cpus: 20-29,60-69
node 2 size: 258042 MB
node 2 free: 257041 MB
node 3 cpus: 30-39,70-79
node 3 size: 257983 MB
node 3 free: 257386 MB
node distances:
node 0 1 2 3
0: 10 12 21 21
1: 12 10 21 21
2: 21 21 10 12
3: 21 21 12 10

9. /proc/meminfo

MemTotal: 1056511908 kB

10. who -r
run-level 3 Jan 20 11:18

11. Systemd service manager version: systemd 252 (252-18.el9)
Default Target Status
multi-user degraded

12. Failed units, from systemctl list-units --state=failed
UNIT LOAD ACTIVE SUB DESCRIPTION
* dnf-makecache.service loaded failed failed dnf makecache

13. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online
accounts-daemon atd auditd avahi-daemon bluetooth chronyd crond cups dbus-broker firewalld
gdm getty@ insights-client-boot irqbalance iscsi iscsi-onboot kdump libstoragemgmt
lm_sensors low-memory-monitor lvm2-monitor mcelog mdmonitor microcode multipathd
nis-domainname nvmefc-boot-connections ostree-remount pmcd pmie pmlogger

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Platform Notes (Continued)

```

power-profiles-daemon qemu-guest-agent rhsmcertd rpcbind rsyslog rtkit-daemon
selinux-autorelabel-mark smartd sshd sssd switcheroo-control sysstat systemd-boot-update
systemd-network-generator tuned udisks2 upower vgaauthd virtqemud vmtoolsd
systemd-remount-fs
enabled-runtime
disabled
arp-ethers autofs blk-availability brltty canberra-system-bootup canberra-system-shutdown
canberra-system-shutdown-reboot chrony-wait cni-dhcp console-getty cpupower cups-browsed
dbus-daemon debug-shell dnf-system-upgrade dnsmasq dovecot fancontrol fcoe grafana-server
gssproxy httpd httpd@ ibacm iprdump iprinit iprupdate ipsec iscsid iscsiuiio kpatch
kvm_stat ledmon libvirt-guests libvirts 1ldpad man-db-restart-cache-update named
named-chroot netavark-dhcp-proxy nfs-blkmap nfs-server nftables nmb numad nvmf-autoconnect
ostree-readonly-sysroot-migration pesign pmfind pmie_farm pmlogger_farm pmproxy podman
podman-auto-update podman-clean-transient podman-kube@ podman-restart postfix powertop
psacct ras-mc-ctl rasdaemon rdisc rhcd rhsm rhsm-facts rpmdb-rebuild rrdcached saslauthd
selinux-check-proper-disable serial-getty@ smb snmpd snmptrapd spamassassin
speech-dispatcherd srp_daemon srp_daemon_port@ sshd-keygen@ systemd-boot-check-no-failures
systemd-nspawn@ systemd-pstore systemd-sysext target targetcid tog-pegasus trace-cmd
virtinterfaced virtnetworkd virtnodeudev virtnwfilterd virtproxyd virtsecretd virtstoraged
vsftpd wpa_supplicant
indirect
pcscd spice-vdagentd sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
systemd-sysupdate systemd-sysupdate-reboot virtlockd virtlogd vsftpd@
```

14. Linux kernel boot-time arguments, from /proc/cmdline
 BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-362.13.1.el9_3.x86_64
 root=/dev/mapper/rhel-root
 ro
 resume=/dev/mapper/rhel-swap
 rd.lvm.lv=rhel/root
 rd.lvm.lv=rhel/swap
 rhgb
 quiet

15. cpupower frequency-info
 analyzing CPU 0:
 current policy: frequency should be within 3.90 GHz and 3.90 GHz.
 The governor "performance" may decide which speed to use
 within this range.
 boost state support:
 Supported: yes
 Active: yes

16. tuned-adm active
 Current active profile: throughput-performance

17. sysctl
 kernel.numa_balancing 1
 kernel.randomize_va_space 2
 vm.compaction_proactiveness 20
 vm.dirty_background_bytes 0
 vm.dirty_background_ratio 10
 vm.dirty_bytes 0
 vm.dirty_expire_centisecs 3000
 vm.dirty_ratio 40
 vm.dirty_writeback_centisecs 500
 vm.dirtytime_expire_seconds 43200
 vm.extfrag_threshold 500
 vm.min_unmapped_ratio 1

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Platform Notes (Continued)

```
vm.nr_hugepages          0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness             10
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode      0

-----
18. /sys/kernel/mm/transparent_hugepage
    defrag      always defer defer+madvise [madvise] never
    enabled     [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled  always within_size advise [never] deny force

-----
19. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs 60000
    defrag                 1
    max_ptes_none          511
    max_ptes_shared         256
    max_ptes_swap           64
    pages_to_scan           4096
    scan_sleep_millisecs   10000

-----
20. OS release
    From /etc/*-release /etc/*-version
    os-release      Red Hat Enterprise Linux 9.3 (Plow)
    redhat-release  Red Hat Enterprise Linux release 9.3 (Plow)
    system-release  Red Hat Enterprise Linux release 9.3 (Plow)

-----
21. Disk information
    SPEC is set to: /home/cpu2017
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/mapper/rhel-home xfs   856G  260G  597G  31% /home

-----
22. /sys/devices/virtual/dmi/id
    Vendor:      Tyrone Systems
    Product:     Tyrone Camarero SDI200A3N-212
    Product Family: Family
    Serial:      2X21292023D

-----
23. dmidecode
    Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section.
    The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
    "DMTF SMBIOS" standard.
    Memory:
        16x Samsung M321R8GA0BB0-CQKZJ 64 GB 2 rank 4800, configured at 4000

-----
24. BIOS
    (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor: American Megatrends International, LLC.
    BIOS Version: 1.3
```

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Platform Notes (Continued)

BIOS Date: 06/01/2023
BIOS Revision: 5.31

Compiler Version Notes

=====

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
| 541.leela_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

Fortran | 548.exchange2_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
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Base Compiler Invocation

C benchmarks:

icx

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SPEC CPU®2017 Integer Rate Result

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Tyrone Systems

(Test Sponsor: Netweb Pte Ltd)

Tyrone Camarero SDI200A3N-212
(2.00 GHz, Intel Xeon Silver 4416+)

SPECrate®2017_int_base = 353

SPECrate®2017_int_peak = 364

CPU2017 License: 006042

Test Sponsor: Netweb Pte Ltd

Tested by: Tyrone Systems

Test Date: Jan-2024

Hardware Availability: Jan-2023

Software Availability: Dec-2023

Base Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc



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Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

502.gcc_r: -D_FILE_OFFSET_BITS=64

505.mcf_r: -DSPEC_LP64

520.omnetpp_r: -DSPEC_LP64

523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX

525.x264_r: -DSPEC_LP64

531.deepsjeng_r: -DSPEC_LP64

541.leela_r: -DSPEC_LP64

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs

-fprofile-generate(pass 1)

-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)

-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse

-funroll-loops -qopt-mem-layout-trans=4

-fno-strict-overflow

-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin

-lqkmalloc

502.gcc_r: -m32

-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/ia32_lin

-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)

-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)

-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse

-funroll-loops -qopt-mem-layout-trans=4

-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

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Peak Optimization Flags (Continued)

505.mcf_r: basepeak = yes

```
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -futo -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-SPR-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-SPR-revC.xml>

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