



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017_int_base = 68.4

PowerEdge T360 (Intel Xeon E-2436)

SPECrate®2017_int_peak = 71.0

CPU2017 License: 6573

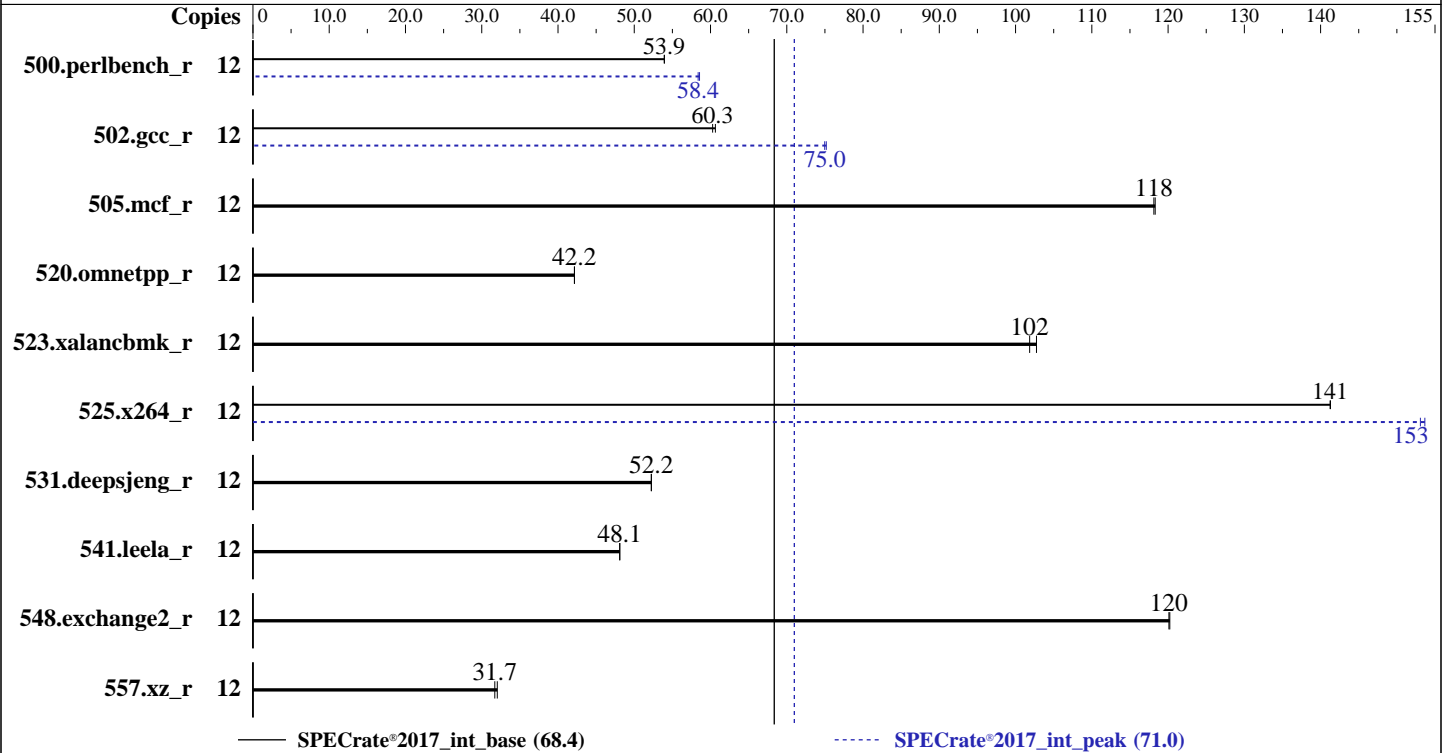
Test Date: Nov-2023

Test Sponsor: Dell Inc.

Hardware Availability: Dec-2023

Tested by: Dell Inc.

Software Availability: Dec-2023



Hardware

CPU Name: Intel Xeon E-2436
 Max MHz: 5000
 Nominal: 2900
 Enabled: 6 cores, 1 chip, 2 threads/core
 Orderable: 1 chip
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 18 MB I+D on chip per chip
 Other: None
 Memory: 64 GB (2 x 32 GB 2Rx8 PC5-4800B-E, running at 4400)
 Storage: 40 GB on tmpfs
 Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4
 5.14.21-150400.22-default
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++
 Compiler for Linux;
 Fortran: Version 2023.2.3 of Intel Fortran
 Compiler for Linux;
 Parallel: No
 Firmware: Version 1.0.0 released Oct-2023
 File System: tmpfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance
 at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	12	354	54.0	354	53.9			12	327	58.4	326	58.6		
502.gcc_r	12	282	60.3	280	60.6			12	226	75.2	227	75.0		
505.mcf_r	12	164	118	164	118			12	164	118	164	118		
520.omnetpp_r	12	373	42.2	373	42.2			12	373	42.2	373	42.2		
523.xalancbmk_r	12	123	103	124	102			12	123	103	124	102		
525.x264_r	12	149	141	149	141			12	137	154	137	153		
531.deepsjeng_r	12	263	52.2	263	52.2			12	263	52.2	263	52.2		
541.leela_r	12	413	48.1	413	48.1			12	413	48.1	413	48.1		
548.exchange2_r	12	262	120	262	120			12	262	120	262	120		
557.xz_r	12	405	32.0	409	31.7			12	405	32.0	409	31.7		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
"/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/lib/ia32:/mnt
/ramdisk/cpu2017-1.1.9-ic2023.2.3/je5.0.1-32"
MALLOCONF = "retain:true"
```

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)

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General Notes (Continued)

is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 40 GB ramdisk created with the cmd: "mount -t tmpfs -o size=40G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:

DIMM Self Healing on
Uncorrectable Memory Error : Disabled

Virtualization Technology : Disabled
LLC Prefetch : Disabled
Dead Line LLC Alloc : Disabled

System Profile : Custom
CPU Power Management : Maximum Performance
C1E : Disabled
C States : Autonomous
PCI ASPM L1 Link
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Thu Nov 23 18:19:58 2023

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a

(Continued on next page)



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Platform Notes (Continued)

Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux

2. w
18:19:58 up 9 min, 1 user, load average: 0.30, 0.28, 0.20
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty1 - 18:12 1:34 0.98s 0.00s /bin/bash ./dell-run-speccpu.sh rate
--define DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1 --define DL-VERS=v4.8.4
--output_format html,pdf,txt --define DL-LQC=1

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 255238
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 255238
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
/bin/bash ./DELL_rate.sh
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1
--define DL-VERS=v4.8.4 --output_format html,pdf,txt --define DL-LQC=1
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1
--define DL-VERS=v4.8.4 --output_format html,pdf,txt --define DL-LQC=1
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=12 -c
ic2023.2.3-lin-core-avx2-rate-20231121.cfg --define smt-on --define cores=6 --define physicallogical
--define no-numa --tune base,peak -o all --define drop_caches --iterations 2 --define
DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1 --define DL-VERS=v4.8.4 --output_format
html,pdf,txt --define DL-LQC=1 intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=12 --configfile
ic2023.2.3-lin-core-avx2-rate-20231121.cfg --define smt-on --define cores=6 --define physicallogical
--define no-numa --tune base,peak --output_format all --define drop_caches --iterations 2 --define
DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1 --define DL-VERS=v4.8.4 --output_format
html,pdf,txt --define DL-LQC=1 --nopower --runmode rate --tune base:peak --size refrate intrate --nopreenv
--note-preenv --logfile \$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0
--from_runcpu 2
specperl \$SPEC/bin/sysinfo
\$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3

(Continued on next page)



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Platform Notes (Continued)

6. /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) E E-2436
vendor_id      : GenuineIntel
cpu family     : 6
model          : 183
stepping       : 1
microcode      : 0x11f
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 6
siblings       : 12
1 physical ids (chips)
12 processors (hardware threads)
physical id 0: core ids 0-5
physical id 0: apicids 0-11

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.2:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         42 bits physical, 48 bits virtual
Byte Order:            Little Endian
CPU(s):                12
On-line CPU(s) list:  0-11
Vendor ID:             GenuineIntel
Model name:            Intel(R) Xeon(R) E E-2436
CPU family:            6
Model:                 183
Thread(s) per core:   2
Core(s) per socket:   6
Socket(s):             1
Stepping:              1
BogoMIPS:              5836.80
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                      clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                      lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                      nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                      ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic
                      movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                      3dnowprefetch cpuid_fault invpcid_single ssbd ibrs ibpb stibp
                      ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed
                      adx smap clflushopt clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves
                      avx_vnni dtherm ida arat pln pts umip pku ospke waitpkg gfni vaes
                      vpclmulqdq tme rdpid movdiri movdir64b fsrm md_clear serialize pconfig
                      arch_lbr flush_l1d arch_capabilities

L1d cache:             288 KiB (6 instances)
L1i cache:             192 KiB (6 instances)
L2 cache:              12 MiB (6 instances)
L3 cache:              18 MiB (1 instance)
NUMA node(s):         1
NUMA node0 CPU(s):    0-11
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:   Not affected
Vulnerability Mds:    Not affected
Vulnerability Meltdown: Not affected

```

(Continued on next page)



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Platform Notes (Continued)

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
 Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
 Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
 Vulnerability Srbds: Not affected
 Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	288K	12	Data	1	64	1	64
L1i	32K	192K	8	Instruction	1	64	1	64
L2	2M	12M	16	Unified	2	2048	1	64
L3	18M	18M	9	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.
 available: 1 nodes (0)
 node 0 cpus: 0-11
 node 0 size: 63833 MB
 node 0 free: 52599 MB
 node distances:
 node 0
 0: 10

9. /proc/meminfo

MemTotal: 65365484 kB

10. who -r

run-level 3 Nov 23 18:11

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)

Default Target Status
 multi-user running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	YaST2-Firstboot YaST2-Second-Stage apparmor auditd chronyd cron display-manager firewalld getty@ haveged irqbalance issue-generator kbdsettings kdump kdump-early klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info gpm grub2-once haveged-switch-root ipmi ipmievd issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rdisc rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures systemd-network-generator systemd-sysexit systemd-time-wait-sync systemd-timesyncd
indirect	wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
 root=UUID=2927c481-4b28-4dff-91cd-49e4759e703c
 splash=silent
 mitigations=auto
 quiet

(Continued on next page)



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Platform Notes (Continued)

```
security=apparmor
crashkernel=230M,high
crashkernel=72M,low
```

```
-----
14. cpupower frequency-info
analyzing CPU 0:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes
```

```
-----
15. sysctl
kernel.numa_balancing          0
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                  20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds   43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                    60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs   10000
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4
```

```
-----
19. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3
Filesystem      Type  Size  Used Avail Use% Mounted on
```

(Continued on next page)



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Platform Notes (Continued)

tmpfs tmpfs 40G 5.1G 35G 13% /mnt/ramdisk

20. /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge T360
Product Family: PowerEdge
Serial: SLT3605

21. dmidecode
Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
2x 002C00001201 MTC20C2085S1EC48BA1 32 GB 2 rank 4800, configured at 4400

22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: Dell Inc.
BIOS Version: 1.0.0
BIOS Date: 10/13/2023
BIOS Revision: 1.0

Compiler Version Notes

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
557.xz_r(base, peak)

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(Continued on next page)



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Compiler Version Notes (Continued)

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
541.leela_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Fortran | 548.exchange2_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

(Continued on next page)



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Base Optimization Flags (Continued)

C benchmarks (continued):

```
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmallocc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmallocc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmallocc
```

Peak Compiler Invocation

C benchmarks:

```
icx
```

C++ benchmarks:

```
icpx
```

Fortran benchmarks:

```
ifx
```

Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```



SPEC CPU®2017 Integer Rate Result

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Dell Inc.

SPECrate®2017_int_base = 68.4

PowerEdge T360 (Intel Xeon E-2436)

SPECrate®2017_int_peak = 71.0

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Nov-2023

Hardware Availability: Dec-2023

Software Availability: Dec-2023

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2 -flto
-Ofast -ffast-math -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-strict-overflow
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmallocc
```

```
502.gcc_r: -m32
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2 -flto
-Ofast -ffast-math -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib
-ljemallocc
```

505.mcf_r: basepeak = yes

```
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmallocc
```

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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