



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11

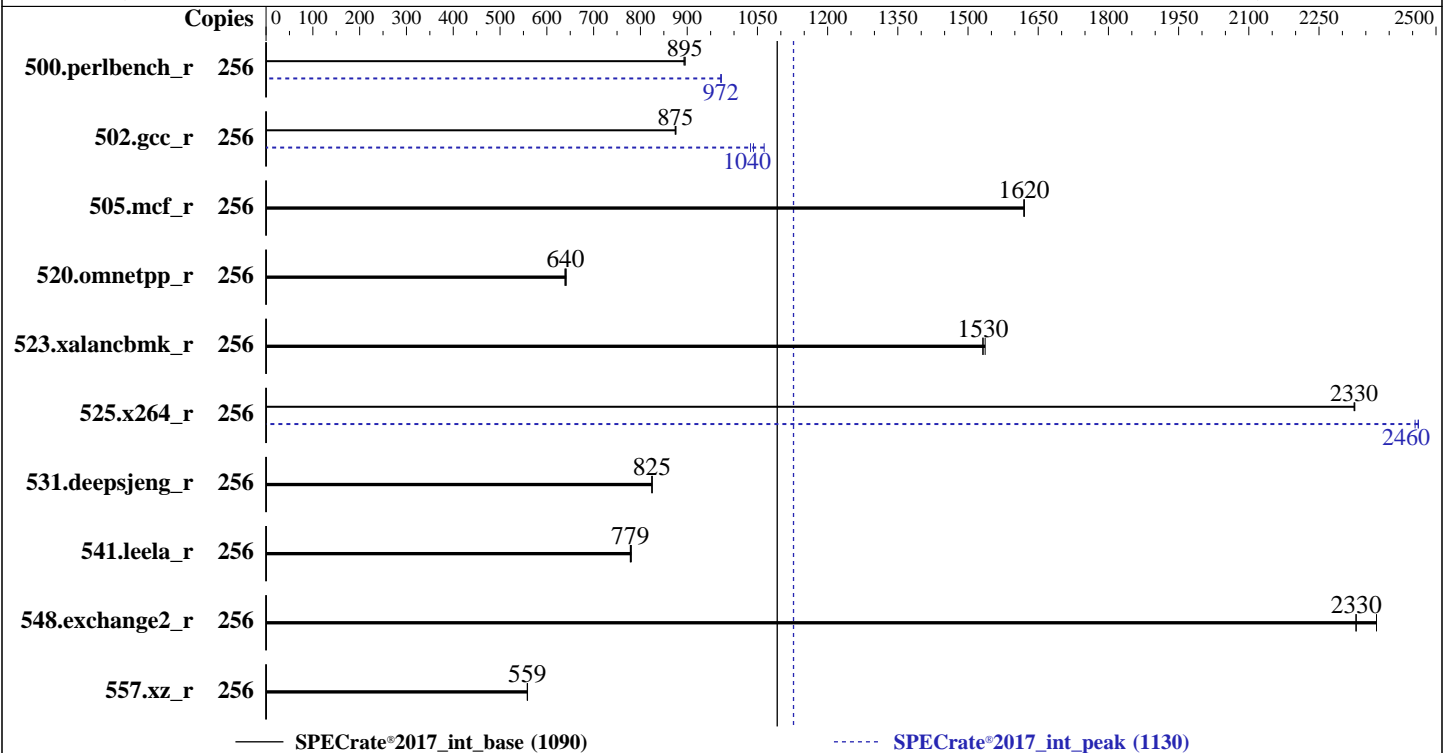
(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_int_base = 1090

SPECrate®2017_int_peak = 1130

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Nov-2023
Hardware Availability: Feb-2024
Software Availability: Dec-2023



Hardware

CPU Name: Intel Xeon Platinum 8592+
 Max MHz: 3900
 Nominal: 1900
 Enabled: 128 cores, 2 chips, 2 threads/core
 Orderable: 1, 2 chip(s)
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 320 MB I+D on chip per chip
 Other: None
 Memory: 512 GB (16 x 32 GB 2Rx8 PC5-5600B-R)
 Storage: 1 x 960 GB SATA SSD
 Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP5
 Kernel 5.14.21-150500.53-default
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
 Parallel: No
 Firmware: HPE BIOS Version v2.10 10/26/2023 released Oct-2023
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11

(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_int_base = 1090

SPECrate®2017_int_peak = 1130

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Nov-2023
Hardware Availability: Feb-2024
Software Availability: Dec-2023

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	256	456	893	455	895	455	895	256	419	972	419	973	419	972
502.gcc_r	256	414	876	414	875	415	874	256	341	1060	350	1040	348	1040
505.mcf_r	256	255	1620	255	1620	255	1620	256	255	1620	255	1620	255	1620
520.omnetpp_r	256	526	639	524	641	525	640	256	526	639	524	641	525	640
523.xalancbmk_r	256	177	1530	176	1530	176	1540	256	177	1530	176	1530	176	1540
525.x264_r	256	193	2330	193	2330	193	2330	256	182	2460	183	2460	182	2460
531.deepsjeng_r	256	356	825	356	825	356	825	256	356	825	356	825	356	825
541.leela_r	256	544	779	543	781	545	778	256	544	779	543	781	545	778
548.exchange2_r	256	288	2330	283	2370	288	2330	256	288	2330	283	2370	288	2330
557.xz_r	256	495	559	495	558	495	559	256	495	559	495	558	495	559

SPECrate®2017_int_base = **1090**

SPECrate®2017_int_peak = **1130**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017_new/lib/intel64:/home/cpu2017_new/lib/ia32:/home/cpu2017_new/je5.0.1-32"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11

(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_int_base = 1090

SPECrate®2017_int_peak = 1130

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2023

Hardware Availability: Feb-2024

Software Availability: Dec-2023

General Notes (Continued)

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

The system ROM used for this result contains Intel microcode version 0x210001a0 for the Intel Xeon Platinum 8592+ processor.

BIOS Configuration:

Workload Profile set to General Throughput Compute
Memory Patrol Scrubbing set to Disabled
Last Level Cache (LLC) Dead Line Allocation set to Disabled
Intel UPI Link Enablement set to Single Link
Enhanced Processor Performance Profile set to Aggressive
Thermal Configuration set to Maximum Cooling
Workload Profile set to Custom
DCU Stream Prefetcher set to Disabled
Adjacent Sector Prefetch set to Disabled
Intel UPI Link Power Management set to Enabled
Minimum Processor Idle Power Package C-State set to Package C6 (non-retention) State

Sysinfo program /home/cpu2017_new/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on dhcp-10-30-23-138 Thu Nov 23 03:33:01 2023

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

1. uname -a
Linux dhcp-10-30-23-138 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023
(b630043) x86_64 x86_64 x86_64 GNU/Linux

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11

(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_int_base = 1090

SPECrate®2017_int_peak = 1130

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Nov-2023
Hardware Availability: Feb-2024
Software Availability: Dec-2023

Platform Notes (Continued)

2. w
03:33:01 up 1 min, 0 users, load average: 0.40, 0.14, 0.05
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 2062341
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 2062341
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root@notty
bash -c cd \$SPEC/ && \$SPEC/intrateon.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=256 -c
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=128 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=256 --configfile
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=128 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
\$SPEC/tmp/CPU2017.001/temlogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl \$SPEC/bin/sysinfo
\$SPEC = /home/cpu2017_new

6. /proc/cpuinfo
model name : INTEL(R) XEON(R) PLATINUM 8592+
vendor_id : GenuineIntel
cpu family : 6
model : 207
stepping : 2
microcode : 0x210001a0
bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores : 64
siblings : 128
2 physical ids (chips)
256 processors (hardware threads)
physical id 0: core ids 0-63

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11

(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_int_base = 1090

SPECrate®2017_int_peak = 1130

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Nov-2023
Hardware Availability: Feb-2024
Software Availability: Dec-2023

Platform Notes (Continued)

physical id 1: core ids 0-63
physical id 0: apicids 0-127
physical id 1: apicids 128-255

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:                x86_64
CPU op-mode(s):              32-bit, 64-bit
Address sizes:               46 bits physical, 57 bits virtual
Byte Order:                  Little Endian
CPU(s):                      256
On-line CPU(s) list:        0-255
Vendor ID:                   GenuineIntel
Model name:                   INTEL(R) XEON(R) PLATINUM 8592+
CPU family:                  6
Model:                       207
Thread(s) per core:         2
Core(s) per socket:         64
Socket(s):                   2
Stepping:                    2
BogoMIPS:                    3800.00
Flags:                        fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                              clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                              lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                              nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                              ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                              sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                              lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                              invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                              vmmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep
                              bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                              avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                              xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                              cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
                              avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                              avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                              cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                              arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
Virtualization:              VT-x
L1d cache:                   6 MiB (128 instances)
L1i cache:                   4 MiB (128 instances)
L2 cache:                    256 MiB (128 instances)
L3 cache:                    640 MiB (2 instances)
NUMA node(s):                4
NUMA node0 CPU(s):          0-31,128-159
NUMA node1 CPU(s):          32-63,160-191
NUMA node2 CPU(s):          64-95,192-223
NUMA node3 CPU(s):          96-127,224-255
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:         Not affected
Vulnerability Mds:          Not affected
Vulnerability Meltdown:     Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed:     Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:   Mitigation; usercopy/swapgs barriers and __user pointer sanitization

```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11

(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_int_base = 1090

SPECrate®2017_int_peak = 1130

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2023

Hardware Availability: Feb-2024

Software Availability: Dec-2023

Platform Notes (Continued)

Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW sequence

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	6M	12	Data	1	64	1	64
L1i	32K	4M	8	Instruction	1	64	1	64
L2	2M	256M	16	Unified	2	2048	1	64
L3	320M	640M	20	Unified	3	262144	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0-31,128-159
node 0 size: 128667 MB
node 0 free: 128024 MB
node 1 cpus: 32-63,160-191
node 1 size: 129008 MB
node 1 free: 128024 MB
node 2 cpus: 64-95,192-223
node 2 size: 129008 MB
node 2 free: 128423 MB
node 3 cpus: 96-127,224-255
node 3 size: 128933 MB
node 3 free: 128426 MB
node distances:
node  0  1  2  3
  0:  10  20  30  30
  1:  20  10  30  30
  2:  30  30  10  20
  3:  30  30  20  10

```

9. /proc/meminfo

MemTotal: 527991220 kB

10. who -r

run-level 3 Nov 23 03:32

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

```

Default Target Status
multi-user      running

```

12. Services, from systemctl list-unit-files

```

STATE UNIT FILES
enabled ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd
bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings klog
lvm2-monitor nscd postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked
wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant
enabled-runtime systemd-remount-fs
disabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon autofs
autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates
chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables
exchange-bmc-os-info firewallld gpm grub2-once haveged haveged-switch-root

```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11

(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_int_base = 1090

SPECrate®2017_int_peak = 1130

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2023

Hardware Availability: Feb-2024

Software Availability: Dec-2023

Platform Notes (Continued)

```

hwloc-dump-hwdata ipmi ipmievd issue-add-ssh-keys kexec-load lunmask man-db-create
multipathd nfs nfs-blkmap nmb openvpn@ ostree-remount rpcbind rpmconfigcheck rsyncd
rtkit-daemon serial-getty@ smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd
systemd-boot-check-no-failures systemd-network-generator systemd-sysext
systemd-time-wait-sync systemd-timesyncd tuned udisks2 update-system-flatpaks upower
vncserver@ wpa_supplicant@
pcscd saned@ wickedd

```

indirect

13. Linux kernel boot-time arguments, from /proc/cmdline

```

BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=da74e710-a3db-4f33-9e12-6a2e672dd5ee
splash=silent
resume=/dev/disk/by-uuid/ef943a7c-3213-42e5-81d6-d2759bb8ad29
mitigations=auto
quiet
security=apparmor

```

14. cpupower frequency-info

```

analyzing CPU 0:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes

```

15. tuned-adm active

```

It seems that tuned daemon is not running, preset profile is not activated.
Preset profile: throughput-performance

```

16. sysctl

```

kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds   43200
vm.extfrag_threshold          500
vm.min_unmapped_ratio         1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy     0
vm.nr_overcommit_hugepages    0
vm.swappiness                  60
vm.watermark_boost_factor     15000
vm.watermark_scale_factor     10
vm.zone_reclaim_mode          0

```

17. /sys/kernel/mm/transparent_hugepage

```

defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force

```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11

(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_int_base = 1090

SPECrate®2017_int_peak = 1130

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2023

Hardware Availability: Feb-2024

Software Availability: Dec-2023

Platform Notes (Continued)

```

18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000

```

```

19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5

```

```

20. Disk information
SPEC is set to: /home/cpu2017_new
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 350G 143G 208G 41% /home

```

```

21. /sys/devices/virtual/dmi/id
Vendor: HPE
Product: ProLiant DL380 Gen11
Product Family: ProLiant
Serial: CNX21000G7

```

```

22. dmidecode
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
16x Hynix HMCG88AGBRA193N 32 GB 2 rank 5600

```

```

23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: HPE
BIOS Version: 2.10
BIOS Date: 10/26/2023
BIOS Revision: 2.10
Firmware Revision: 1.54

```

Compiler Version Notes

```

=====
C | 502.gcc_r(peak)
=====

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
=====

```

```

=====
C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)
=====

```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11

(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_int_base = 1090

SPECrate®2017_int_peak = 1130

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2023

Hardware Availability: Feb-2024

Software Availability: Dec-2023

Compiler Version Notes (Continued)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
541.leela_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
Fortran | 548.exchange2_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

502.gcc_r: -DSPEC_LP64

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11

(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_int_base = 1090

SPECrate®2017_int_peak = 1130

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2023

Hardware Availability: Feb-2024

Software Availability: Dec-2023

Base Portability Flags (Continued)

```
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11

(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_int_base = 1090

SPECrate®2017_int_peak = 1130

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2023

Hardware Availability: Feb-2024

Software Availability: Dec-2023

Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: basepeak = yes
```

C++ benchmarks:

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11

(1.90 GHz, Intel Xeon Platinum 8592+)

SPECrate®2017_int_base = 1090

SPECrate®2017_int_peak = 1130

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Nov-2023

Hardware Availability: Feb-2024

Software Availability: Dec-2023

Peak Optimization Flags (Continued)

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-SPR-rev3.0.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-SPR-rev3.0.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2023-11-22 17:03:01-0500.

Report generated on 2023-12-14 14:52:57 by CPU2017 PDF formatter v6716.

Originally published on 2023-12-14.