



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.

**SPECrate®2017\_int\_base = 1240**

NF8260M7 (Intel Xeon Gold 6448H)

**SPECrate®2017\_int\_peak = 1270**

CPU2017 License: 3358

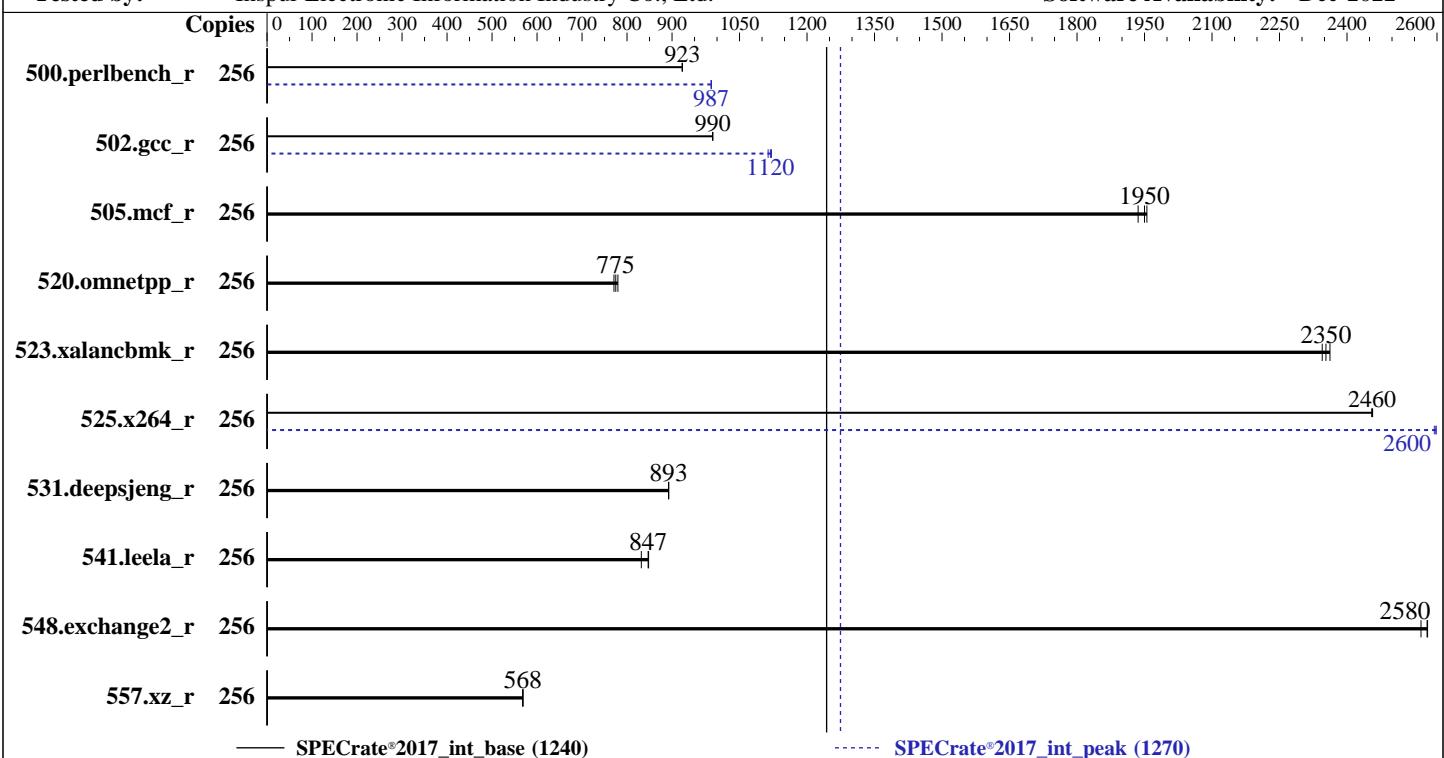
**Test Date:** Aug-2023

**Test Sponsor:** Inspur Electronic Information Industry Co., Ltd.

**Hardware Availability:** Apr-2023

**Tested by:** Inspur Electronic Information Industry Co., Ltd.

**Software Availability:** Dec-2022



— SPECrate®2017\_int\_base (1240)

··· SPECrate®2017\_int\_peak (1270)

## Hardware

CPU Name: Intel Xeon Gold 6448H  
 Max MHz: 4100  
 Nominal: 2400  
 Enabled: 128 cores, 4 chips, 2 threads/core  
 Orderable: 2,4 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 60 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (32 x 32 GB 2Rx4 PC5-4800B-R)  
 Storage: 1 x 1 TB NVME SSD  
 Other: None

## OS:

Red Hat Enterprise Linux 9.0 (Plow)  
 5.14.0-70.13.1.el9\_0.x86\_64

## Compiler:

C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;

## Parallel:

No

## Firmware:

Version 03.00.00 released Dec-2022

## File System:

xfs

## System State:

Run level 3 (multi-user)

## Base Pointers:

64-bit

## Peak Pointers:

32/64-bit

## Other:

jemalloc memory allocator V5.0.1

## Power Management:

BIOS and OS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.

**SPECrate®2017\_int\_base = 1240**

NF8260M7 (Intel Xeon Gold 6448H)

**SPECrate®2017\_int\_peak = 1270**

CPU2017 License: 3358

Test Date: Aug-2023

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Hardware Availability: Apr-2023

Tested by: Inspur Electronic Information Industry Co., Ltd.

Software Availability: Dec-2022

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	256	442	923	<b>442</b>	<b>923</b>	442	923	256	<b>413</b>	<b>987</b>	413	987	413	987	413	987
502.gcc_r	256	366	991	<b>366</b>	<b>990</b>	366	990	256	323	1120	326	1110	<b>324</b>	<b>1120</b>	326	1110
505.mcf_r	256	<b>212</b>	<b>1950</b>	212	1960	214	1940	256	<b>212</b>	<b>1950</b>	212	1960	214	1940	214	1940
520.omnetpp_r	256	431	780	436	771	<b>433</b>	<b>775</b>	256	431	780	436	771	<b>433</b>	<b>775</b>	436	771
523.xalancbmk_r	256	<b>115</b>	<b>2350</b>	114	2360	115	2340	256	<b>115</b>	<b>2350</b>	114	2360	115	2340	115	2340
525.x264_r	256	183	2450	182	2460	<b>183</b>	<b>2460</b>	256	173	2590	<b>173</b>	<b>2600</b>	173	2600	173	2600
531.deepsjeng_r	256	329	893	<b>329</b>	<b>893</b>	329	892	256	329	893	<b>329</b>	<b>893</b>	329	892	329	892
541.leela_r	256	510	832	<b>501</b>	<b>847</b>	500	848	256	510	832	<b>501</b>	<b>847</b>	500	848	500	848
548.exchange2_r	256	260	2580	262	2560	<b>260</b>	<b>2580</b>	256	260	2580	262	2560	<b>260</b>	<b>2580</b>	262	2560
557.xz_r	256	486	569	<b>486</b>	<b>568</b>	487	568	256	486	569	<b>486</b>	<b>568</b>	487	568	487	568

**SPECrate®2017\_int\_base = 1240**

**SPECrate®2017\_int\_peak = 1270**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk\_r / 623.xalancbmk\_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 [https://www.spec.org/cpu2017/Docs/runrules.html#rule\\_1.4](https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4)), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"  
MALLOC\_CONF = "retain:true"



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.

SPECrate®2017\_int\_base = 1240

NF8260M7 (Intel Xeon Gold 6448H)

SPECrate®2017\_int\_peak = 1270

CPU2017 License: 3358

Test Date: Aug-2023

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Hardware Availability: Apr-2023

Tested by: Inspur Electronic Information Industry Co., Ltd.

Software Availability: Dec-2022

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS configuration:

ENERGY\_PERF\_BIAS\_CFG mode set to Performance

Hardware Prefetch set to Disable

VT Support set to Disable

Sub NUMA Cluster (SNC) set to SNC4

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197 running on localhost.localdomain Wed Aug 16 12:41:09 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
  10. who -r
  11. Systemd service manager version: systemd 250 (250-6.el9\_0)
  12. Failed units, from systemctl list-units --state=failed
  13. Services, from systemctl list-unit-files
  14. Linux kernel boot-time arguments, from /proc/cmdline
  15. cpupower frequency-info
  16. sysctl
  17. /sys/kernel/mm/transparent\_hugepage
  18. /sys/kernel/mm/transparent\_hugepage/khugepaged
  19. OS release
  20. Disk information
  21. /sys/devices/virtual/dmi/id
  22. dmidecode
  23. BIOS
- 

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.

SPECrate®2017\_int\_base = 1240

NF8260M7 (Intel Xeon Gold 6448H)

SPECrate®2017\_int\_peak = 1270

CPU2017 License: 3358

Test Date: Aug-2023

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Hardware Availability: Apr-2023

Tested by: Inspur Electronic Information Industry Co., Ltd.

Software Availability: Dec-2022

## Platform Notes (Continued)

```
1. uname -a
Linux localhost.localdomain 5.14.0-70.13.1.el9_0.x86_64 #1 SMP PREEMPT Thu Apr 14 12:42:38 EDT 2022 x86_64
x86_64 x86_64 GNU/Linux
```

```
2. w
12:41:09 up 58 min, 1 user, load average: 108.15, 214.91, 234.63
USER      TTY      LOGIN@     IDLE     JCPU    PCPU WHAT
root      tty1     11:43   13.00s  1.09s  0.01s sh
reportable-ic2023.0-lin-sapphirerapids-rate-smt-on-20221201.sh
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
real-time non-blocking time  (microseconds, -R) unlimited
core file size              (blocks, -c) 0
data seg size               (kbytes, -d) unlimited
scheduling priority         (-e) 0
file size                   (blocks, -f) unlimited
pending signals             (-i) 4126438
max locked memory           (kbytes, -l) 64
max memory size             (kbytes, -m) unlimited
open files                  (-n) 1024
pipe size                   (512 bytes, -p) 8
POSIX message queues        (bytes, -q) 819200
real-time priority          (-r) 0
stack size                  (kbytes, -s) unlimited
cpu time                    (seconds, -t) unlimited
max user processes          (-u) 4126438
virtual memory               (kbytes, -v) unlimited
file locks                  (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 18
login -- root
-bash
sh reportable-ic2023.0-lin-sapphirerapids-rate-smt-on-20221201.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=256 -c
  ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=128 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=256 --configfile
  ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=128 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
  --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperf $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) Gold 6448H
vendor_id       : GenuineIntel
cpu family     : 6
model          : 143
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.

SPECrate®2017\_int\_base = 1240

NF8260M7 (Intel Xeon Gold 6448H)

SPECrate®2017\_int\_peak = 1270

CPU2017 License: 3358

Test Date: Aug-2023

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Hardware Availability: Apr-2023

Tested by: Inspur Electronic Information Industry Co., Ltd.

Software Availability: Dec-2022

## Platform Notes (Continued)

```
stepping      : 8
microcode     : 0x2b000130
bugs          : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores     : 32
siblings       : 64
4 physical ids (chips)
256 processors (hardware threads)
physical id 0: core ids 0-31
physical id 1: core ids 0-31
physical id 2: core ids 0-31
physical id 3: core ids 0-31
physical id 0: apicids 0-63
physical id 1: apicids 128-191
physical id 2: apicids 256-319
physical id 3: apicids 384-447
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

-----  
7. lscpu

From lscpu from util-linux 2.37.4:

```
Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                256
On-line CPU(s) list:   0-255
Vendor ID:              GenuineIntel
BIOS Vendor ID:        Intel(R) Corporation
Model name:             Intel(R) Xeon(R) Gold 6448H
BIOS Model name:        Intel(R) Xeon(R) Gold 6448H
CPU family:             6
Model:                 143
Thread(s) per core:    2
Core(s) per socket:    32
Socket(s):              4
Stepping:               8
Frequency boost:       enabled
CPU max MHz:           2401.0000
CPU min MHz:            800.0000
BogoMIPS:               4800.00
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperf mperf tsc_known_freq pni pclmulqdq dtes64 ds_cpl
                        smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic
                        movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                        3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                        intel_ppin cdp_l2 ssbd mba ibrs stibp ibrs_enhanced fsgsbase
                        tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq
                        rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
                        avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occp_llc
                        cqmq_mbm_total cqmq_mbm_local split_lock_detect avx_vnni avx512_bf16
                        wbnoinvd dtherm ida arat pln pts avx512vbm1 umip pku ospke waitpkg
                        avx512_vbm12 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
                        avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdir64b
                        enqcmd fsrm md_clear serialize tsxlptrk pconfig arch_lbr avx512_fp16
                        amx_tile flush_lll arch_capabilities
L1d cache:              6 MiB (128 instances)
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.

SPECCrate®2017\_int\_base = 1240

NF8260M7 (Intel Xeon Gold 6448H)

SPECCrate®2017\_int\_peak = 1270

CPU2017 License: 3358

Test Date: Aug-2023

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Hardware Availability: Apr-2023

Tested by: Inspur Electronic Information Industry Co., Ltd.

Software Availability: Dec-2022

## Platform Notes (Continued)

L1i cache:	4 MiB (128 instances)
L2 cache:	256 MiB (128 instances)
L3 cache:	240 MiB (4 instances)
NUMA node(s):	8
NUMA node0 CPU(s):	0-15,128-143
NUMA node1 CPU(s):	16-31,144-159
NUMA node2 CPU(s):	32-47,160-175
NUMA node3 CPU(s):	48-63,176-191
NUMA node4 CPU(s):	64-79,192-207
NUMA node5 CPU(s):	80-95,208-223
NUMA node6 CPU(s):	96-111,224-239
NUMA node7 CPU(s):	112-127,240-255
Vulnerability Itlb multihit:	Not affected
Vulnerability Llft:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	6M	12	Data	1	64	1	64
L1i	32K	4M	8	Instruction	1	64	1	64
L2	2M	256M	16	Unified	2	2048	1	64
L3	60M	240M	15	Unified	3	65536	1	64

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)
node 0 cpus: 0-15,128-143
node 0 size: 128586 MB
node 0 free: 118837 MB
node 1 cpus: 16-31,144-159
node 1 size: 129017 MB
node 1 free: 121613 MB
node 2 cpus: 32-47,160-175
node 2 size: 128980 MB
node 2 free: 122001 MB
node 3 cpus: 48-63,176-191
node 3 size: 129017 MB
node 3 free: 122115 MB
node 4 cpus: 64-79,192-207
node 4 size: 129017 MB
node 4 free: 122164 MB
node 5 cpus: 80-95,208-223
node 5 size: 129017 MB
node 5 free: 122148 MB
node 6 cpus: 96-111,224-239
node 6 size: 129017 MB
node 6 free: 122149 MB
node 7 cpus: 112-127,240-255
node 7 size: 128997 MB
node 7 free: 122155 MB
node distances:
node 0 1 2 3 4 5 6 7
0: 10 12 21 21 21 21 21 21

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.

SPECrate®2017\_int\_base = 1240

NF8260M7 (Intel Xeon Gold 6448H)

SPECrate®2017\_int\_peak = 1270

CPU2017 License: 3358

Test Date: Aug-2023

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Hardware Availability: Apr-2023

Tested by: Inspur Electronic Information Industry Co., Ltd.

Software Availability: Dec-2022

## Platform Notes (Continued)

```
1: 12 10 21 21 21 21 21 21 21  
2: 21 21 10 12 21 21 21 21 21  
3: 21 21 12 10 21 21 21 21 21  
4: 21 21 21 21 10 12 21 21 21  
5: 21 21 21 21 12 10 21 21 21  
6: 21 21 21 21 21 21 10 12 12  
7: 21 21 21 21 21 21 21 12 10
```

```
-----  
9. /proc/meminfo  
MemTotal: 1056409368 kB
```

```
-----  
10. who -r  
run-level 3 Aug 16 11:42
```

```
-----  
11. Systemd service manager version: systemd 250 (250-6.el9_0)  
Default Target Status  
multi-user degraded
```

```
-----  
12. Failed units, from systemctl list-units --state=failed  
UNIT LOAD ACTIVE SUB DESCRIPTION  
* NetworkManager-wait-online.service loaded failed failed Network Manager Wait Online
```

```
-----  
13. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd chronyd crond  
dbus-broker firewalld getty@ irqbalance kdump lvm2-monitor microcode nis-domainname  
rhsmcertd rsyslog selinux-autorelabel-mark sshd sssd systemd-network-generator upower  
enabled-runtime systemd-remount-fs  
disabled blk-availability canberra-system-bootup canberra-system-shutdown  
canberra-system-shutdown-reboot chrony-wait console-getty cpupower debug-shell kvm_stat  
man-db-restart-cache-update nftables rdisc rhsm rhsm-facts rpmdb-rebuild serial-getty@  
sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysext  
indirect sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
```

```
-----  
14. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT_IMAGE=(hd0,msdos1)/vmlinuz-5.14.0-70.13.1.el9_0.x86_64  
root=/dev/mapper/rhel-root  
ro  
resume=/dev/mapper/rhel-swap  
rd.lvm.lv=rhel/root  
rd.lvm.lv=rhel/swap
```

```
-----  
15. cpupower frequency-info  
analyzing CPU 0:  
    current policy: frequency should be within 800 MHz and 2.40 GHz.  
    The governor "performance" may decide which speed to use  
    within this range.  
boost state support:  
    Supported: yes  
    Active: yes
```

```
-----  
16. sysctl
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.

SPECrate®2017\_int\_base = 1240

NF8260M7 (Intel Xeon Gold 6448H)

SPECrate®2017\_int\_peak = 1270

CPU2017 License: 3358

Test Date: Aug-2023

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Hardware Availability: Apr-2023

Tested by: Inspur Electronic Information Industry Co., Ltd.

Software Availability: Dec-2022

## Platform Notes (Continued)

```
kernel.numa_balancing      1
kernel.randomize_va_space  2
vm.compaction_proactiveness 20
vm.dirty_background_bytes  0
vm.dirty_background_ratio  10
vm.dirty_bytes             0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio             20
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold       500
vm.min_unmapped_ratio     1
vm.nr_hugepages            0
vm.nr_hugepages_mempolicy  0
vm.nr_overcommit_hugepages 0
vm.swappiness               60
vm.watermark_boost_factor 15000
vm.watermark_scale_factor  10
vm.zone_reclaim_mode       0
```

---

```
17. /sys/kernel/mm/transparent_hugepage
    defrag      always defer defer+madvise [madvise] never
    enabled     [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled always within_size advise [never] deny force
```

---

```
18. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs 60000
    defrag                 1
    max_ptes_none          511
    max_ptes_shared         256
    max_ptes_swap           64
    pages_to_scan          4096
    scan_sleep_millisecs   10000
```

---

```
19. OS release
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.0 (Plow)
redhat-release Red Hat Enterprise Linux release 9.0 (Plow)
system-release Red Hat Enterprise Linux release 9.0 (Plow)
```

---

```
20. Disk information
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs  819G  64G  756G  8% /home
```

---

```
21. /sys/devices/virtual/dmi/id
Vendor:        Inspur
Product:       NF8260-M7-A0-R0-00
Product Family: Not specified
Serial:        21B545466
```

---

```
22. dmidecode
Additional information from dmidecode 3.3 follows.  WARNING: Use caution when you interpret this section.
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.

SPECrate®2017\_int\_base = 1240

NF8260M7 (Intel Xeon Gold 6448H)

SPECrate®2017\_int\_peak = 1270

CPU2017 License: 3358

Test Date: Aug-2023

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Hardware Availability: Apr-2023

Tested by: Inspur Electronic Information Industry Co., Ltd.

Software Availability: Dec-2022

## Platform Notes (Continued)

The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

32x Micron MTC20F2085S1RC48BA1 32 GB 2 rank 4800

-----  
23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.

BIOS Version: 03.00.00

BIOS Date: 12/16/2022

## Compiler Version Notes

=====

C | 502.gcc\_r(peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====

=====

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====

=====

C | 502.gcc\_r(peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====

=====

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====

=====

C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
| 541.leela\_r(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====

=====

Fortran | 548.exchange2\_r(base, peak)

=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.

SPECrate®2017\_int\_base = 1240

NF8260M7 (Intel Xeon Gold 6448H)

SPECrate®2017\_int\_peak = 1270

CPU2017 License: 3358

Test Date: Aug-2023

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Hardware Availability: Apr-2023

Tested by: Inspur Electronic Information Industry Co., Ltd.

Software Availability: Dec-2022

## Compiler Version Notes (Continued)

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64\_lin  
-lqkmalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64\_lin  
-lqkmalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.

SPECrate®2017\_int\_base = 1240

NF8260M7 (Intel Xeon Gold 6448H)

SPECrate®2017\_int\_peak = 1270

CPU2017 License: 3358

Test Date: Aug-2023

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Hardware Availability: Apr-2023

Tested by: Inspur Electronic Information Industry Co., Ltd.

Software Availability: Dec-2022

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto  
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin  
-lqkmalloc
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64

502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64

505.mcf\_r: -DSPEC\_LP64

520.omnetpp\_r: -DSPEC\_LP64

523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX

525.x264\_r: -DSPEC\_LP64

531.deepsjeng\_r: -DSPEC\_LP64

541.leela\_r: -DSPEC\_LP64

548.exchange2\_r: -DSPEC\_LP64

557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4  
-fno-strict-overflow
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.

SPECrate®2017\_int\_base = 1240

NF8260M7 (Intel Xeon Gold 6448H)

SPECrate®2017\_int\_peak = 1270

CPU2017 License: 3358

Test Date: Aug-2023

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Hardware Availability: Apr-2023

Tested by: Inspur Electronic Information Industry Co., Ltd.

Software Availability: Dec-2022

## Peak Optimization Flags (Continued)

500.perlbench\_r (continued):

```
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin  
-lqkmalloc
```

502.gcc\_r: -m32

```
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/ia32_lin  
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc
```

505.mcf\_r: basepeak = yes

```
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fno-alias  
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin  
-lqkmalloc
```

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-intel-V3.3.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-intel-V3.3.xml>



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.

SPECrate®2017\_int\_base = 1240

NF8260M7 (Intel Xeon Gold 6448H)

SPECrate®2017\_int\_peak = 1270

CPU2017 License: 3358

Test Date: Aug-2023

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Hardware Availability: Apr-2023

Tested by: Inspur Electronic Information Industry Co., Ltd.

Software Availability: Dec-2022

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2023-08-16 12:41:08-0400.

Report generated on 2024-01-29 18:08:17 by CPU2017 PDF formatter v6716.

Originally published on 2023-09-13.