



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017_int_base = 686

PowerEdge MX760c (Intel Xeon Platinum 8460Y+)

SPECrate®2017_int_peak = 709

CPU2017 License: 6573

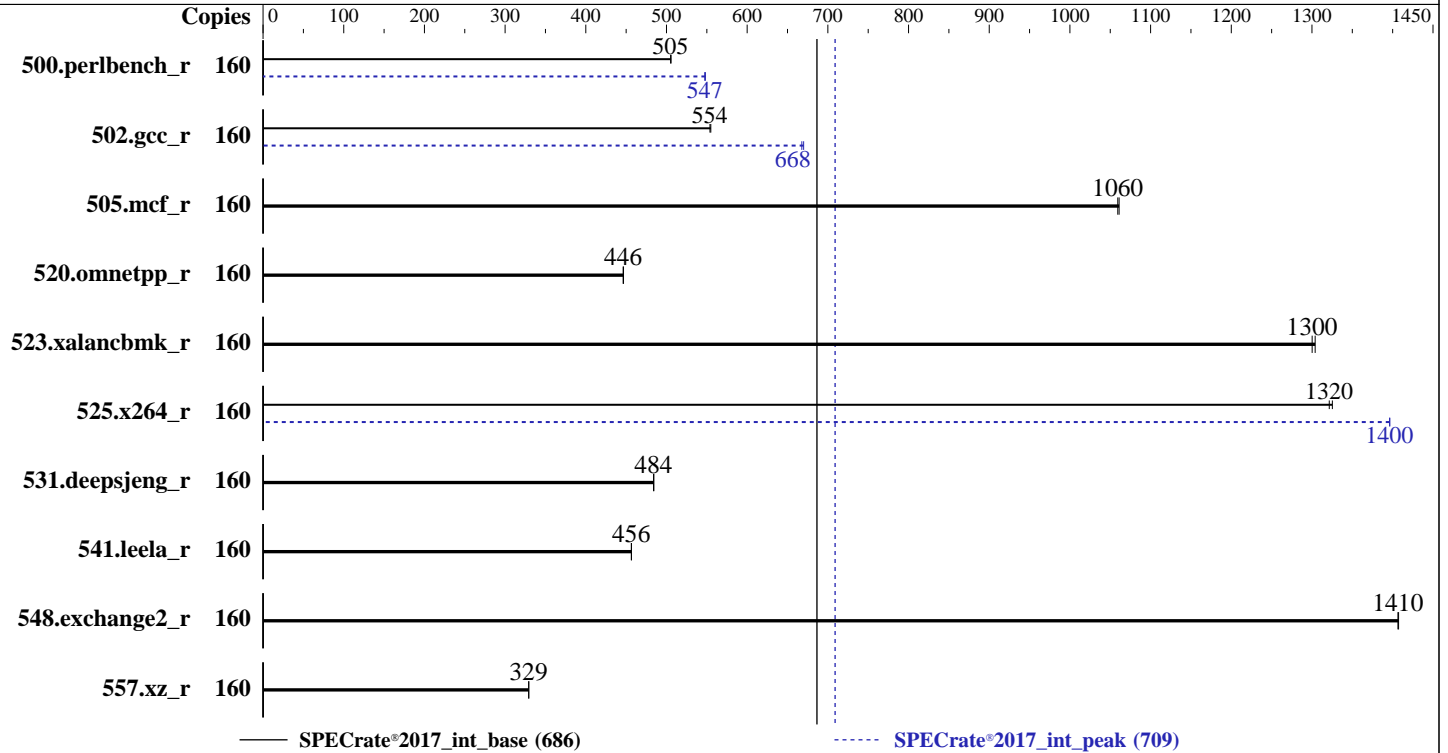
Test Date: May-2023

Test Sponsor: Dell Inc.

Hardware Availability: Feb-2023

Tested by: Dell Inc.

Software Availability: Dec-2022



Hardware

CPU Name: Intel Xeon Platinum 8460Y+
 Max MHz: 3700
 Nominal: 2000
 Enabled: 80 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 105 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)
 Storage: 100 GB on tmpfs
 Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4
 5.14.21-150400.22-default
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++
 Compiler for Linux;
 Fortran: Version 2023.0 of Intel Fortran Compiler
 for Linux;
 Parallel: No
 Firmware: Version 1.3.2 released Mar-2023
 File System: tmpfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance
 at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	160	504	505	504	506			160	465	547	465	548		
502.gcc_r	160	409	554	408	555			160	339	668	338	670		
505.mcf_r	160	244	1060	244	1060			160	244	1060	244	1060		
520.omnetpp_r	160	470	446	470	447			160	470	446	470	447		
523.xalancbmk_r	160	130	1300	130	1300			160	130	1300	130	1300		
525.x264_r	160	212	1320	211	1330			160	201	1400	201	1400		
531.deepsjeng_r	160	379	484	379	484			160	379	484	379	484		
541.leela_r	160	580	457	580	456			160	580	457	580	456		
548.exchange2_r	160	298	1410	298	1410			160	298	1410	298	1410		
557.xz_r	160	525	329	525	329			160	525	329	525	329		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalancbmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
"/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/lib/ia32:/mnt/ramdisk/cpu2017-1.1.9-ic2023.0/je5.0.1-32"
MALLOCONF = "retain:true"
```



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General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
 memory using Red Hat Enterprise Linux 8.4
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
`sync; echo 3> /proc/sys/vm/drop_caches`
 runcpu command invoked through numactl i.e.:
`numactl --interleave=all runcpu <etc>`
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 100 GB ramdisk created with the cmd: "mount -t tmpfs -o size=100G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:

```

      ADDDC Setting : Disabled
      DIMM Self Healing on
      Uncorrectable Memory Error : Disabled
      Virtualization Technology : Disabled
      DCU Streamer Prefetcher : Disabled
      Sub NUMA Cluster : 4-way Clustering
      LLC Prefetch : Disabled
      Dead Line LLC Alloc : Disabled
      Optimizer Mode : Enabled

      System Profile : Custom
      CPU Power Management : Maximum Performance
      CLE : Disabled
      C States : Autonomous
      Memory Patrol Scrub : Disabled
      Energy Efficiency Policy : Performance
      PCI ASPM L1 Link
      Power Management : Disabled
  
```

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.0/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost Wed May 10 03:49:05 2023

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. `uname -a`
2. `w`
3. Username
4. `ulimit -a`
5. `sysinfo process ancestry`
6. `/proc/cpuinfo`

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Platform Notes (Continued)

```

7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

```

```

-----
1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux

```

```

-----
2. w
 03:49:05 up 4 min,  1 user,  load average: 0.99, 2.65, 1.48
USER      TTY      FROM          LOGIN@   IDLE   JCPU   PCPU   WHAT
root     tty1    -              03:45   60.00s  1.10s  0.00s  /bin/bash ./dell-run-speccpu.sh rate
--define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-adddcD=1 --define DL-BIOS-VirtD=1 --define
DL-BIOS-SNC=4 --output_format csv,html,pdf,txt

```

```

-----
3. Username
From environment variable $USER:  root

```

```

-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size                (blocks, -f) unlimited
pending signals         (-i) 4124453
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 4124453
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited

```

```

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 29
login -- root
-bash

```

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Platform Notes (Continued)

```

/bin/bash ./DELL_rate.sh
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-adddcD=1
--define DL-BIOS-VirtD=1 --define DL-BIOS-SNC=4 --output_format csv,html,pdf,txt
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-adddcD=1
--define DL-BIOS-VirtD=1 --define DL-BIOS-SNC=4 --output_format csv,html,pdf,txt
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=160 -c
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=80 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all --iterations 2 --define
DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-adddcD=1 --define DL-BIOS-VirtD=1 --define DL-BIOS-SNC=4
--output_format csv,html,pdf,txt intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=160 --configfile
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=80 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --iterations 2
--define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-adddcD=1 --define DL-BIOS-VirtD=1 --define
DL-BIOS-SNC=4 --output_format csv,html,pdf,txt --nopower --runmode rate --tune base:peak --size refrate
intrate --nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.001/templots/preenv.intrate.001.0.log
--lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.0

```

6. /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) Platinum 8460Y+
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 8
microcode     : 0x2b0001b0
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 40
siblings       : 80
2 physical ids (chips)
160 processors (hardware threads)
physical id 0: core ids 0-39
physical id 1: core ids 0-39
physical id 0: apicids 0-79
physical id 1: apicids 128-207

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.2:

```

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 160
On-line CPU(s) list: 0-159
Vendor ID: GenuineIntel
Model name: Intel(R) Xeon(R) Platinum 8460Y+
CPU family: 6
Model: 143
Thread(s) per core: 2
Core(s) per socket: 40
Socket(s): 2
Stepping: 8

```

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Platform Notes (Continued)

```

BogoMIPS:          4000.00
Flags:             fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                  clflush dts acpi mmx fxsr sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                  lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                  nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                  ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                  x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                  abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                  cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle
                  avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                  avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                  xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                  cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                  arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
                  vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
                  bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
                  tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
L1d cache:        3.8 MiB (80 instances)
L1i cache:        2.5 MiB (80 instances)
L2 cache:         160 MiB (80 instances)
L3 cache:         210 MiB (2 instances)
NUMA node(s):     8
NUMA node0 CPU(s): 0,4,8,12,16,20,24,28,32,36,80,84,88,92,96,100,104,108,112,116
NUMA node1 CPU(s): 40,44,48,52,56,60,64,68,72,76,120,124,128,132,136,140,144,148,152,156
NUMA node2 CPU(s): 2,6,10,14,18,22,26,30,34,38,82,86,90,94,98,102,106,110,114,118
NUMA node3 CPU(s): 42,46,50,54,58,62,66,70,74,78,122,126,130,134,138,142,146,150,154,158
NUMA node4 CPU(s): 1,5,9,13,17,21,25,29,33,37,81,85,89,93,97,101,105,109,113,117
NUMA node5 CPU(s): 41,45,49,53,57,61,65,69,73,77,121,125,129,133,137,141,145,149,153,157
NUMA node6 CPU(s): 3,7,11,15,19,23,27,31,35,39,83,87,91,95,99,103,107,111,115,119
NUMA node7 CPU(s): 43,47,51,55,59,63,67,71,75,79,123,127,131,135,139,143,147,151,155,159
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:         Not affected
Vulnerability Mds:         Not affected
Vulnerability Meltdown:    Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:  Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:  Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds:       Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	3.8M	12	Data	1	64	1	64
L1i	32K	2.5M	8	Instruction	1	64	1	64
L2	2M	160M	16	Unified	2	2048	1	64
L3	105M	210M	15	Unified	3	114688	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0,4,8,12,16,20,24,28,32,36,80,84,88,92,96,100,104,108,112,116
node 0 size: 128398 MB
node 0 free: 127385 MB
node 1 cpus: 40,44,48,52,56,60,64,68,72,76,120,124,128,132,136,140,144,148,152,156
node 1 size: 129018 MB
node 1 free: 128797 MB
node 2 cpus: 2,6,10,14,18,22,26,30,34,38,82,86,90,94,98,102,106,110,114,118
node 2 size: 128984 MB
node 2 free: 128562 MB

```

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Platform Notes (Continued)

```

node 3 cpus: 42,46,50,54,58,62,66,70,74,78,122,126,130,134,138,142,146,150,154,158
node 3 size: 129018 MB
node 3 free: 128837 MB
node 4 cpus: 1,5,9,13,17,21,25,29,33,37,81,85,89,93,97,101,105,109,113,117
node 4 size: 129018 MB
node 4 free: 128719 MB
node 5 cpus: 41,45,49,53,57,61,65,69,73,77,121,125,129,133,137,141,145,149,153,157
node 5 size: 129018 MB
node 5 free: 120142 MB
node 6 cpus: 3,7,11,15,19,23,27,31,35,39,83,87,91,95,99,103,107,111,115,119
node 6 size: 129018 MB
node 6 free: 128822 MB
node 7 cpus: 43,47,51,55,59,63,67,71,75,79,123,127,131,135,139,143,147,151,155,159
node 7 size: 128657 MB
node 7 free: 128379 MB
node distances:
node  0  1  2  3  4  5  6  7
 0:  10  12  12  12  21  21  21  21
 1:  12  10  12  12  21  21  21  21
 2:  12  12  10  12  21  21  21  21
 3:  12  12  12  10  21  21  21  21
 4:  21  21  21  21  10  12  12  12
 5:  21  21  21  21  12  10  12  12
 6:  21  21  21  21  12  12  10  12
 7:  21  21  21  21  12  12  12  10

```

```

-----
9. /proc/meminfo
   MemTotal:      1055882740 kB

```

```

-----
10. who -r
    run-level 3 May 10 03:45

```

```

-----
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
    Default Target   Status
    multi-user       running

```

```

-----
12. Services, from systemctl list-unit-files
STATE      UNIT FILES
enabled    apparmor auditd cron firewalld getty@ haveged irqbalance issue-generator kbdsettings kdump
           kdump-early lvm2-monitor postfix purge-kernels rollback sshd wickedd wickedd-auto4
           wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime  systemd-remount-fs
disabled        blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell
           ebttables grub2-once haveged-switch-root issue-add-ssh-keys kexec-load lunmask nfs
           nfs-blkmap rpcbind rpmconfigcheck rsyncd serial-getty@ systemd-boot-check-no-failures
           systemd-network-generator systemd-sysexec systemd-time-wait-sync systemd-timesyncd tuned
indirect      wickedd

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=4068cb7a-41a3-4eea-ab49-bc6f15bea24c
splash=silent
mitigations=auto
quiet
security=apparmor

```

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Platform Notes (Continued)

crashkernel=314M,high
crashkernel=72M,low

14. cpupower frequency-info
analyzing CPU 0:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes

15. tuned-adm active
It seems that tuned daemon is not running, preset profile is not activated.
Preset profile: throughput-performance

16. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 2
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 20
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 60
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 0

17. /sys/kernel/mm/transparent_hugepage
defrag always defer defer+madvice [madvice] never
enabled [always] madvice never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000

19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4

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Platform Notes (Continued)

20. Disk information

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.0

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	100G	4.2G	96G	5%	/mnt/ramdisk

21. /sys/devices/virtual/dmi/id

```
Vendor:      Dell Inc.
Product:    PowerEdge MX760c
Product Family: PowerEdge
Serial:     SMX7608
```

22. dmidecode

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

```
12x 002C00B3002C MTC40F2046S1RC48BA1 64 GB 2 rank 4800
1x 002C0632002C MTC40F2046S1RC48BA1 64 GB 2 rank 4800
3x 002C069D002C MTC40F2046S1RC48BA1 64 GB 2 rank 4800
```

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

```
BIOS Vendor:      Dell Inc.
BIOS Version:     1.3.2
BIOS Date:        03/28/2023
BIOS Revision:    1.3
```

Compiler Version Notes

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201
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C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017_int_base = 686

PowerEdge MX760c (Intel Xeon Platinum 8460Y+)

SPECrate®2017_int_peak = 709

CPU2017 License: 6573

Test Date: May-2023

Test Sponsor: Dell Inc.

Hardware Availability: Feb-2023

Tested by: Dell Inc.

Software Availability: Dec-2022

Compiler Version Notes (Continued)

| 557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
541.leela_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
Fortran | 548.exchange2_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
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Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

502.gcc_r: -DSPEC_LP64

505.mcf_r: -DSPEC_LP64

520.omnetpp_r: -DSPEC_LP64

523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX

525.x264_r: -DSPEC_LP64

531.deepsjeng_r: -DSPEC_LP64

541.leela_r: -DSPEC_LP64

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64



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Test Date: May-2023

Hardware Availability: Feb-2023

Software Availability: Dec-2022

Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
```

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Hardware Availability: Feb-2023

Software Availability: Dec-2022

Peak Portability Flags (Continued)

557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

```
502.gcc_r: -m32
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc
```

505.mcf_r: basepeak = yes

```
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

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Test Date: May-2023

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Software Availability: Dec-2022

Peak Optimization Flags (Continued)

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.5.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.5.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2023-05-10 03:49:04-0400.

Report generated on 2024-01-29 17:56:18 by CPU2017 PDF formatter v6716.

Originally published on 2023-07-19.