



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS X410c M7 (Intel Xeon Gold 6448H, 2.40GHz)

SPECspeed®2017_int_base = 16.1

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9019

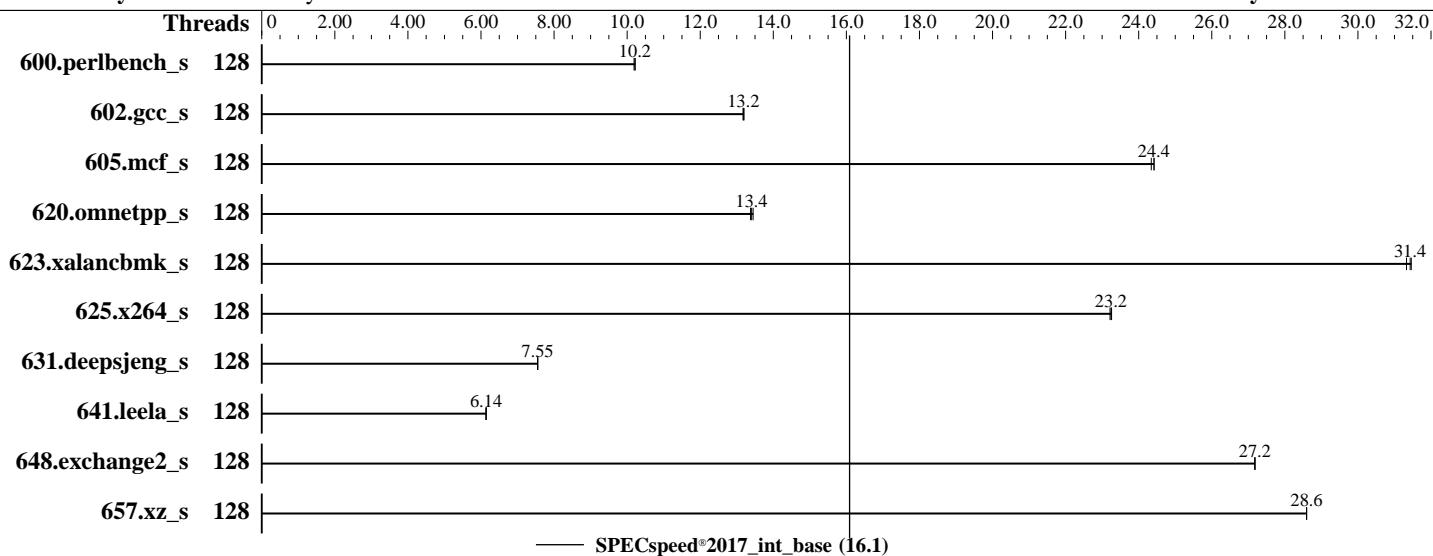
Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: May-2023

Hardware Availability: Jun-2023

Software Availability: Dec-2022



Hardware

CPU Name: Intel Xeon Gold 6448H
 Max MHz: 4100
 Nominal: 2400
 Enabled: 240 cores, 4 chips
 Orderable: 1,2,3,4 Chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 60 MB I+D on chip per chip
 Other: None
 Memory: 2 TB (32 x 64 GB 2Rx4 PC5-4800B-R)
 Storage: 1 x 1.6 TB NVMe SSD
 Other: None

Software

OS: Red Hat Enterprise Linux 9.1 (Plow)
 5.14.0-162.6.1.el9_1.x86_64
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;
 Parallel: Yes
 Firmware: Version 5.1.1b released Apr-2023
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	128	174	10.2	<u>174</u>	<u>10.2</u>	174	10.2							
602.gcc_s	128	302	13.2	<u>302</u>	<u>13.2</u>	302	13.2							
605.mcf_s	128	194	24.3	193	24.4	<u>193</u>	<u>24.4</u>							
620.omnetpp_s	128	122	13.4	<u>122</u>	<u>13.4</u>	121	13.4							
623.xalancbmk_s	128	45.2	31.3	45.0	31.5	<u>45.1</u>	<u>31.4</u>							
625.x264_s	128	76.0	23.2	<u>75.9</u>	<u>23.2</u>	75.8	23.3							
631.deepsjeng_s	128	190	7.55	190	7.55	<u>190</u>	<u>7.55</u>							
641.leela_s	128	278	6.14	278	6.14	<u>278</u>	<u>6.14</u>							
648.exchange2_s	128	<u>108</u>	<u>27.2</u>	108	27.2	108	27.2							
657.xz_s	128	216	28.6	216	28.6	<u>216</u>	<u>28.6</u>							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalancbmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation

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General Notes (Continued)

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Settings:

Intel HyperThreading set to Disabled

LLC Dead Line set to Disabled

Processor C6 Report set to Enabled

UPI Link Power Management Disabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on x410-spec Mon May 22 00:11:21 2023

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
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 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 250 (250-12.el9_1)
 12. Failed units, from systemctl list-units --state=failed
 13. Services, from systemctl list-unit-files
 14. Linux kernel boot-time arguments, from /proc/cmdline
 15. cpupower frequency-info
 16. tuned-adm active
 17. sysctl
 18. /sys/kernel/mm/transparent_hugepage
 19. /sys/kernel/mm/transparent_hugepage/khugepaged
 20. OS release
 21. Disk information
 22. /sys/devices/virtual/dmi/id
 23. dmidecode
 24. BIOS
-

1. uname -a
Linux x410-spec 5.14.0-162.6.1.el9_1.x86_64 #1 SMP PREEMPT_DYNAMIC Fri Sep 30 07:36:03 EDT 2022 x86_64
x86_64 x86_64 GNU/Linux

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Platform Notes (Continued)

2. w
00:11:21 up 1 day, 5:04, 2 users, load average: 0.66, 41.03, 82.33
USER TTY LOGIN@ IDLE JCPU PCPU WHAT
root ttym1 Sat19 21:52m 1.62s 0.36s -bash

3. Username
From environment variable \$USER: root

4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 8253502
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 8253502
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
login -- root
-bash
-bash
runcpu --define default-platform-flags -c ic2023.0-lin-sapphirerapids-speed-20221201 --define cores=128
--tune base -o all --define drop_caches intspeed
runcpu --define default-platform-flags --configfile ic2023.0-lin-sapphirerapids-speed-20221201 --define
cores=128 --tune base --output_format all --define drop_caches --nopower --runmode speed --tune base
--size refspeed intspeed --nopreenv --note-preenv --logfile
\$SPEC/tmp/CPU2017.146/templogs/preenv.intspeed.146.0.log --lognum 146.0 --from_runcpu 2
specperl \$SPEC/bin/sysinfo
\$SPEC = /home/cpu2017

6. /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6448H
vendor_id : GenuineIntel
cpu family : 6
model : 143
stepping : 8
microcode : 0x2b000461
bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrss_pbrss
cpu cores : 32
siblings : 32
4 physical ids (chips)
128 processors (hardware threads)
physical id 0: core ids 0-31

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Platform Notes (Continued)

```
physical id 1: core ids 0-31
physical id 2: core ids 0-31
physical id 3: core ids 0-31
physical id 0: apicids
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62
physical id 1: apicids
128,130,132,134,136,138,140,142,144,146,148,150,152,154,156,158,160,162,164,166,168,170,172,174,176,178,1
80,182,184,186,188,190
physical id 2: apicids
256,258,260,262,264,266,268,270,272,274,276,278,280,282,284,286,288,290,292,294,296,298,300,302,304,306,3
08,310,312,314,316,318
physical id 3: apicids
384,386,388,390,392,394,396,398,400,402,404,406,408,410,412,414,416,418,420,422,424,426,428,430,432,434,4
36,438,440,442,444,446
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

```
From lscpu from util-linux 2.37.4:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 128
On-line CPU(s) list: 0-127
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
Model name: Intel(R) Xeon(R) Gold 6448H
BIOS Model name: Intel(R) Xeon(R) Gold 6448H
CPU family: 6
Model: 143
Thread(s) per core: 1
Core(s) per socket: 32
Socket(s): 4
Stepping: 8
CPU max MHz: 4100.0000
CPU min MHz: 800.0000
BogoMIPS: 4800.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtTopology
nonstop_tsc cpuid aperf fmperf tsc_known_freq pni pclmulqdq dtes64 monitor
ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrp pdcm pcid dca sse4_1
sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13
invpcid_single intel_ppin cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced
tpr_shadow vmmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmil avx2
smep bmil erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
xsavopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req hfi avx512vbmi umip
pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
avx512_bitualg tme avx512_vpocntdq la57 rdpid bus_lock_detect cldemote
movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr
amx_bf16 avx512_fp16 amx_tile amx_int8 flush_lld arch_capabilities
Virtualization: VT-x
L1d cache: 6 MiB (128 instances)
```

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Platform Notes (Continued)

```
L1i cache: 4 MiB (128 instances)
L2 cache: 256 MiB (128 instances)
L3 cache: 240 MiB (4 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-31
NUMA node1 CPU(s): 32-63
NUMA node2 CPU(s): 64-95
NUMA node3 CPU(s): 96-127
Vulnerability Itlb multihit: Not affected
Vulnerability Lltf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW sequence
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected
```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	6M	12	Data	1	64	1	64
L1i	32K	4M	8	Instruction	1	64	1	64
L2	2M	256M	16	Unified	2	2048	1	64
L3	60M	240M	15	Unified	3	65536	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0-31
node 0 size: 515221 MB
node 0 free: 504335 MB
node 1 cpus: 32-63
node 1 size: 516088 MB
node 1 free: 514888 MB
node 2 cpus: 64-95
node 2 size: 516050 MB
node 2 free: 514945 MB
node 3 cpus: 96-127
node 3 size: 516075 MB
node 3 free: 514947 MB
node distances:
node 0 1 2 3
 0: 10 21 21 21
 1: 21 10 21 21
 2: 21 21 10 21
 3: 21 21 21 10
```

9. /proc/meminfo

```
MemTotal: 2112958404 kB
```

10. who -r
run-level 3 May 20 19:08 last=5

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Platform Notes (Continued)

11. Systemd service manager version: systemd 250 (250-12.e19_1)
Default Target Status
graphical degraded

12. Failed units, from systemctl list-units --state=failed
UNIT LOAD ACTIVE SUB DESCRIPTION
* NetworkManager-wait-online.service loaded failed failed Network Manager Wait Online

13. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online
accounts-daemon atd audittd avahi-daemon bluetooth chronyd crond cups dbus-broker firewalld
gdm getty@ insights-client-boot irqbalance iscsi iscsi-onboot kdump libstoragemgmt
low-memory-monitor lvm2-monitor mcelog mdmonitor microcode multipathd nis-domainname
nvmefc-boot-connections ostree-remount power-profiles-daemon qemu-guest-agent rhsmcertd
rsyslog rtkit-daemon selinux-autorelabel-mark smartd sshd sssd switcheroo-control
systemd-network-generator tuned udisks2 upower vgaauthd vmtoolsd
enabled-runtime systemd-remount-fs
disabled arp-ethers blk-availability brltty canberra-system-bootup canberra-system-shutdown
canberra-system-shutdown-reboot chrony-wait cni-dhcp console-getty cpupower cups-browsed
dbus-daemon debug-shell dnsmasq iprdump iprinit iprule update iscsid iscsiuio kpatch kvm_stat
ledmon man-db-restart-cache-update nftables nvme-fs-autoconnect podman podman-auto-update
podman-kube@ podman-restart psacct ras-mc-ctl rasdaemon rdisc rhcd rhsm rhsm-facts
rpmbuild rebuild serial-getty@ speech-dispatcherd sshd-keygen@ systemd-boot-check-no-failures
systemd-pstore systemd-sysext wpa_supplicant
indirect spice-vdagentd sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo

14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd1,gpt2)/vmlinuz-5.14.0-162.6.1.e19_1.x86_64
root=UUID=603f0b71-662a-4181-8a53-903bbb6e224a
ro
crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
resume=UUID=50d15411-6e15-4097-87ec-864a2d4ef1f6
rhgb
quiet

15. cpupower frequency-info
analyzing CPU 0:
current policy: frequency should be within 800 MHz and 4.10 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes

16. tuned-adm active
Current active profile: throughput-performance

17. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 0
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10

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Platform Notes (Continued)

```
vm.dirty_bytes          0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio          8
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold    500
vm.min_unmapped_ratio   1
vm.nr_hugepages          0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness            1
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode     1
```

```
18. /sys/kernel/mm/transparent_hugepage
    defrag      [always] defer defer+madvise madvise never
    enabled     [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled always within_size advise [never] deny force
```

```
19. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs 60000
    defrag                 1
    max_ptes_none          511
    max_ptes_shared         256
    max_ptes_swap           64
    pages_to_scan          4096
    scan_sleep_millisecs   10000
```

```
20. OS release
  From /etc/*-release /etc/*-version
  os-release      Red Hat Enterprise Linux 9.1 (Plow)
  redhat-release  Red Hat Enterprise Linux release 9.1 (Plow)
  system-release  Red Hat Enterprise Linux release 9.1 (Plow)
```

```
21. Disk information
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/nvme0n1p5  xfs   1.4T  26G  1.4T   2%  /home
```

```
22. /sys/devices/virtual/dmi/id
Vendor:        Cisco Systems Inc
Product:       UCSX-410C-M7
Serial:        FCH264873PT
```

```
23. dmidecode
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
```

```
Memory:
15x 0xAD00 HMCG94MEBRA109N 64 GB 2 rank 4800
9x 0xAD00 HMCG94MEBRA121N 64 GB 2 rank 4800
```

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Platform Notes (Continued)

8x 0xAD00 HMCG94MEBRA123N 64 GB 2 rank 4800

24. BIOS

(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: Cisco Systems, Inc.
BIOS Version: X410M7.5.1.1b.10.0424230829
BIOS Date: 04/24/2023
BIOS Revision: 5.29

Compiler Version Notes

=====

C | 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base) 657.xz_s(base)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====

C++ | 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base) 641.leela_s(base)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====

Fortran | 648.exchange2_s(base)

=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64

602.gcc_s: -DSPEC_LP64

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Base Portability Flags (Continued)

```
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -fno-finite-math-only
-mfpmath=sse -funroll-loops -fopt-mem-layout-trans=4 -fopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-fno-finite-math-only -mfpmath=sse -funroll-loops -fopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -fno-finite-math-only
-mfpmath=sse -funroll-loops -fopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-V1.0-SPR-revG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-V1.0-SPR-revG.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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